



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Result of III B.Tech II semester (R16) Supplementary Examinations Nov-2022

College name: DADI INSTT. OF ENGG. & TECH.,GAVARAPALEM, ANAKAPALLE:U4

Htno	Subcode	Subname	Grade	Credits
15U41A0421	R1632042	MICRO WAVE ENGINEERING	C	3
15U41A0421	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0440	R1632042	MICRO WAVE ENGINEERING	ABSENT	0
16U41A0449	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
16U41A0449	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0449	R1632043	VLSI DESIGN	F	0
16U41A0449	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0539	R1632051	COMPUTER NETWORKS	D	3
16U41A0539	R1632052	DATA WAREHOUSING AND MINING	F	0
16U41A0539	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	F	0
16U41A0539	R163205C	CYBER SECURITY	F	0
16U41A0586	R1632051	COMPUTER NETWORKS	ABSENT	0
16U41A0586	R1632052	DATA WAREHOUSING AND MINING	ABSENT	0
17U41A0214	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	ABSENT	0
17U41A0218	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	ABSENT	0
18U41A0106	R1632012	GEOTECHNICAL ENGINEERING - I	C	3
18U41A0202	R1632022	POWER SYSTEM ANALYSIS	F	0
18U41A0203	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	F	0
18U41A0203	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
18U41A0223	R1632024	DATA STRUCTURES	D	3
18U41A0405	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
18U41A0405	R1632042	MICRO WAVE ENGINEERING	F	0
18U41A0405	R1632043	VLSI DESIGN	F	0
18U41A0405	R1632044	DIGITAL SIGNAL PROCESSING	F	0
18U41A0405	R163204A	OOPS THROUGH JAVA	F	0
18U41A0429	R1632042	MICRO WAVE ENGINEERING	ABSENT	0
18U41A0433	R163204A	OOPS THROUGH JAVA	C	3
18U41A0434	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
18U41A0434	R1632043	VLSI DESIGN	F	0
18U41A0445	R1632044	DIGITAL SIGNAL PROCESSING	D	3
18U41A0453	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	ABSENT	0
18U41A0453	R1632044	DIGITAL SIGNAL PROCESSING	D	3
18U45A0253	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	F	0
19U45A0106	R1632012	GEOTECHNICAL ENGINEERING - I	B	3
19U45A0203	R163202B	OOPS THROUGH JAVA	B	3
19U45A0205	R1632022	POWER SYSTEM ANALYSIS	ABSENT	0
19U45A0215	R1632022	POWER SYSTEM ANALYSIS	ABSENT	0
19U45A0215	R1632024	DATA STRUCTURES	ABSENT	0
19U45A0217	R1632022	POWER SYSTEM ANALYSIS	F	0
19U45A0233	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	D	3
19U45A0242	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	D	3
19U45A0242	R1632024	DATA STRUCTURES	F	0
19U45A0243	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	F	0
19U45A0243	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
19U45A0243	R1632024	DATA STRUCTURES	F	0

Htno	Subcode	Subname	Grade	Credits
19U45A0247	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	F	0
19U45A0247	R1632022	POWER SYSTEM ANALYSIS	F	0
19U45A0247	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
19U45A0247	R1632024	DATA STRUCTURES	F	0
19U45A0256	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	C	3
19U45A0261	R1632022	POWER SYSTEM ANALYSIS	F	0
19U45A0261	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
19U45A0261	R1632024	DATA STRUCTURES	D	3
19U45A0404	R1632044	DIGITAL SIGNAL PROCESSING	F	0
19U45A0408	R1632044	DIGITAL SIGNAL PROCESSING	F	0
19U45A0416	R1632044	DIGITAL SIGNAL PROCESSING	D	3
19U45A0416	R163204A	OOPS THROUGH JAVA	D	3
19U45A0423	R1632044	DIGITAL SIGNAL PROCESSING	F	0
19U45A0426	R163204A	OOPS THROUGH JAVA	D	3

**Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 20-02-2023]

** Note:**

* -1 in the filed of externals indicates student is absent for the respective subject.

* -2 in the filed of externals indicates student result Withheld for the respective subject.

* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.

Date:13.02.2023


Controller of Examinations(UG)