


EXAM - TIMETABLE

M.Tech I Year I Semester (DR24) Regular Examinations January 2025


Time : 10:00 AM TO 01:00 PM

| Branch/ Date | VLSI |
|---------------------------|--|
| 27-01-2025 (Monday) | CMOS Analog IC Design (DR24MECT11) |
| 28-01-2025 (Tuesday) | CMOS Digital IC design (DR24MECT12) |
| 29-01-2025 (Wednesday) | Research methodology and IPR (DR24MECT13) |
| 30-01-2025 (Thursday) | VLSI Technology (DR24MECP1A) |
| 31-01-2025 (Friday) | Nanomaterials and Nanotechnology (DR24MECP1B) |

NOTE: ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS, IMMEDIATELY.


ACE - 1


CONTROLLER OF EXAMINATIONS


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