

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY



Approved by AICTE & Permanently Affiliated to JNTUK

NAAC Accredited Institute & Inclusion u/s 2(f) & 12(B) of the UGC Act

NH-16, Anakapalle, Visakhapatnam-531002, Andhra Pradesh, diet.edu.in

DESIGN OF ELECTRICAL CIRCUITS USING S/W TOOLS COURSE

Course Instructor :

Mr.T. Ramesh Babu

Asst. Professor, EEE Department

Dadi Institute of Engineering & Technology

Duration :

4 Weeks : (3/1/2021 – 31/1/2021)

Overview & Need for the Course :

Students will be able to learn the electric circuits which includes a device that gives energy to the charged particles constituting the current, such as a battery or a generator; devices that use current, such as lamps, electric motors, or computers; and the connecting wires or transmission lines. Two of the basic laws that mathematically describe the performance of electric circuits are Ohm's law and Kirchhoff's rules.

Course Objectives:

- ❖ develop the skill in Creating dynamic web pages
- ❖ Provide knowledge in connecting PHP programs with Mysql database.
- ❖ Develop the skill in server side programming
- ❖ Provide knowledge about Apache server
- ❖ Testing the application on an Application Server.
- ❖ Debugging Web applications locally and remotely

Course Outcomes:

- To Learn the fundamental of MATLAB & PSPICE Tools
- To generate various waveform signals and sequences
- To verify and simulate various electrical circuits using Mesh and Nodal Analysis
- To verify and simulate various theorems
- To determine self and mutual inductance of a magnetic circuit, parameters of a given coil.

Requirements

- ❖ Basics Knowledge of Electrical system
- ❖ Basics Knowledge of circuit Theory
- ❖ Basic computer knowledge.

Course Contents

- Chap 1. Generation of various signals and sequences, such as unit Impulse, Step, etc.
- Chap 2. Operations on signals and sequences such as Addition Multiplication, Scaling, Shifting, Folding and Average power
- Chap 3. Verification of Kirchhoff's current law and Voltage law using simulation tools.
- Chap 4. Determination of electrical parameters
1. Average Value,
 2. RMS value
 3. Form factor,
 4. Peak factor
- Chap 5. Develop Circuit Theorems
1. Super position theorem
 2. Reciprocity Theorem
 3. Thevenin's Theorem
 4. Maximum power transfer theorem.

List of Participated students

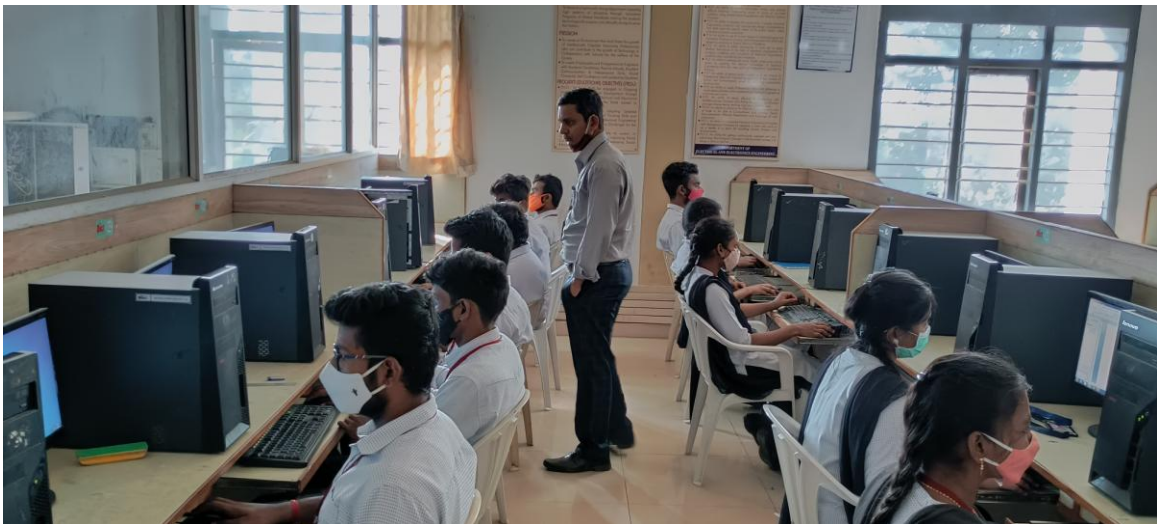
SNO	HT.No	StudentName
1	19U41A0201	GALLA DEEPTHI
2	19U41A0202	KONATHALA VENKATESH
3	19U41A0203	RAPETI JAYARAM
4	19U41A0204	ROTTA KURMA KAPOOR
5	19U41A0205	SINGAMPALLI VENKATA KALYANI
6	19U41A0206	THUMPALA AKSHAYA DEVI
7	19U41A0207	VIYYAPU NOOKA RAJU
8	19U41A0208	YATHIRAJYAM VENKATESWARA RA
9	19U41A0209	NAGULAPALLI VENKATA SAI TRI
10	19U41A0210	PILLA SAI KONDAYYA
11	20U45A0201	ALLA VIJAY KUMAR
12	20U45A0202	AMARAPINNI HEMANTH SAI KUMAR
13	20U45A0203	ANAKAPALLI NAGENDRA
14	20U45A0204	ARJILLI KONDA BABU
15	20U45A0205	BABBODI PAPINAIDU
16	20U45A0206	BENARJEE VAMSI BHEEMUNI
17	20U45A0207	BHEEMISSETTY SAI PRAVEEN
18	20U45A0208	BODDETI MUSILI NAIDU
19	20U45A0209	BOKKA LAKSHMI NARAYANA MANIKANTA
20	20U45A0210	CHIKKALA SAI SIRISHA
21	20U45A0211	DADI RAVITEJA
22	20U45A0212	DARLA MAHESH

23	20U45A0213	DASARI SAI
24	20U45A0214	DHANAKONDA SIVA
25	20U45A0215	DODDI SRITEJA
26	20U45A0216	DUNDURTHI ARUNA
27	20U45A0217	EEGALA KUMAR
28	20U45A0218	GANTA GOWTHAM KUMAR
29	20U45A0219	GARIKI LAKSHMAN
30	20U45A0220	JALLA VISWASWARA RAO
31	20U45A0221	UPPILI JAYANTH
32	20U45A0222	KAMPARA CHANDU
33	20U45A0223	KANDREGULA GANESH
34	20U45A0224	KANDREGULA SANJAY
35	20U45A0225	KARANAM PAVANI
36	20U45A0226	KAREDLA DILEEP KUMAR
37	20U45A0227	KARNAM SYAM KUMAR
38	20U45A0228	KARRI SWETHA
39	20U45A0229	KASIREDDI CHANDRIKA
40	20U45A0230	KOMMANAPALLI VENKATA CHAITANYA
41	20U45A0231	KONATALA MADHU
42	20U45A0232	LANKI VIJAY KUMAR
43	20U45A0233	LEKKALA TARUNKUMAR
44	20U45A0234	LOKIREDDY SUNIL
45	20U45A0235	MAJJI PREM KUMAR
46	20U45A0236	MALLA SAI
47	20U45A0237	MOTURI VENKATESH

48	20U45A0238	MUMMINA LOWKYA
49	20U45A0239	OBIREDDY LOKESH
50	20U45A0240	PAMALA LOHITH KUMAR
51	20U45A0242	PENTAKOTA MURARI
52	20U45A0243	PRAMUDULA PAVAN KUMAR
53	20U45A0244	RANGALA SATYA JAGADEESH
54	20U45A0245	SADI PADMANABAM
55	20U45A0246	SALAPU SAI GANESH
56	20U45A0247	SANKARLA SANTHOSH SANDEEP
57	20U45A0248	SEETHINI MOUNIKA
58	20U45A0249	SEERAM SAI
59	20U45A0250	SESETTI NARENDRA
60	20U45A0251	SETTI VENKATA PADMAVATHI
61	20U45A0252	SHAIK JALALUDDIN
62	20U45A0253	SIRASAPALLI SAI KUMAR
63	20U45A0254	SURISSETTI SOMESH MAHA LAKSHMI NAIDU
64	20U45A0255	SURISSETTY UDAY KIRAN
65	20U45A0256	VANAPALLI HARI SHANKAR DINESH
66	20U45A0257	VEERESWARAPU VARAHA VENKATA SATYA NARENDRA
67	20U45A0258	VEGI SYAM KUMAR
68	20U45A0259	VINDULA CHARAN SAI TEJA
69	20U45A0260	YEDDU ARAVIND KUMAR
70	20U45A0261	CHUKKA MANIKANTA
71	20U45A0262	KARRI KESAVA
72	20U45A0263	GORLI GANESH

73	20U45A0264	GANISETTY HARSHA VARDHAN
74	20U45A0265	KOYYA NAVEEN
75	20U45A0266	VIYYAPU SIRISHA
76	20U45A0267	SANDRANI BHANUSREE
77	20U45A0268	MUTYALA GANGESWARA RAO

Gallery



Certificate

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Course Completion Certificate



THIS CERTIFICATE IS GIVEN TO

K. Venkatesh

FOR SUCCESSFUL COMPLETION OF 4 WEEKS COURSE ON
DESIGN OF ELECTRICAL CIRCUITS USING S/W TOOLS FROM
03/01/2021 TO 31/01/2021

Dr. K. Sujatha
Coordinator

Prof. Dr. Ch. Narasimham
Principal

Sri Dadi Ratnakar
Chairman