



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for III B.Tech II semester (R16) Supplementary Examinations Nov-2019

College name: DADI INSTT. OF ENGG. & TECH., GAVARAPALEM, ANAKAPALLE:U4

Htno	Subcode	Subname	Grade	Credits
15U41A0421	R1632042	MICRO WAVE ENGINEERING	F	0
15U41A0421	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0106	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	C	3
16U41A0107	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	D	3
16U41A0112	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	D	3
16U41A0112	R1632012	GEOTECHNICAL ENGINEERING - I	F	0
16U41A0116	R1632014	WATER RESOURCE ENGINEERING -I	B	3
16U41A0202	R1632024	DATA STRUCTURES	D	3
16U41A0203	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
16U41A0203	R163202A	UNIX AND SHELL PROGRAMMING	F	0
16U41A0204	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	D	3
16U41A0207	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	D	3
16U41A0207	R163202A	UNIX AND SHELL PROGRAMMING	F	0
16U41A0402	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0402	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0402	R163204D	BIO-MEDICAL ENGINEERING	D	3
16U41A0408	R163204D	BIO-MEDICAL ENGINEERING	F	0
16U41A0409	R1632043	VLSI DESIGN	F	0
16U41A0410	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
16U41A0410	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0410	R1632043	VLSI DESIGN	F	0
16U41A0410	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0410	R163204D	BIO-MEDICAL ENGINEERING	F	0
16U41A0413	R1632042	MICRO WAVE ENGINEERING	A	3
16U41A0415	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0417	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0430	R163204D	BIO-MEDICAL ENGINEERING	B	3
16U41A0431	R1632044	DIGITAL SIGNAL PROCESSING	B	3
16U41A0440	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0440	R1632043	VLSI DESIGN	F	0
16U41A0440	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0440	R1632046	MICRO PROCESSORS & MICRO CONTROLLERS LAB	A	2
16U41A0440	R1632047	VLSI LAB	A	2
16U41A0440	R1632048	DIGITAL COMMUNICATIONS LAB	A	2
16U41A0446	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
16U41A0446	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0446	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0449	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
16U41A0449	R1632042	MICRO WAVE ENGINEERING	F	0
16U41A0449	R1632043	VLSI DESIGN	F	0
16U41A0449	R1632044	DIGITAL SIGNAL PROCESSING	F	0
16U41A0449	R1632046	MICRO PROCESSORS & MICRO CONTROLLERS LAB	A	2
16U41A0449	R1632047	VLSI LAB	A	2
16U41A0449	R1632048	DIGITAL COMMUNICATIONS LAB	A	2
16U41A0449	R163204D	BIO-MEDICAL ENGINEERING	D	3

Htno	Subcode	Subname	Grade	Credits
16U41A0452	R1632042	MICRO WAVE ENGINEERING	D	3
16U41A0503	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	D	3
16U41A0505	R163205C	CYBER SECURITY	C	3
16U41A0527	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	F	0
16U41A0531	R1632051	COMPUTER NETWORKS	D	3
16U41A0531	R1632052	DATA WAREHOUSING AND MINING	F	0
16U41A0531	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	F	0
16U41A0531	R1632054	SOFTWARE TESTING METHODOLOGIES	F	0
16U41A0531	R163205C	CYBER SECURITY	D	3
16U41A0542	R1632052	DATA WAREHOUSING AND MINING	F	0
16U41A0544	R1632054	SOFTWARE TESTING METHODOLOGIES	F	0
16U41A0544	R163205C	CYBER SECURITY	F	0
16U41A0545	R1632052	DATA WAREHOUSING AND MINING	F	0
16U41A0545	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	F	0
16U41A0545	R1632054	SOFTWARE TESTING METHODOLOGIES	F	0
16U41A0552	R1632051	COMPUTER NETWORKS	F	0
16U41A0552	R1632054	SOFTWARE TESTING METHODOLOGIES	F	0
16U41A0552	R163205C	CYBER SECURITY	F	0
16U41A0564	R1632051	COMPUTER NETWORKS	D	3
16U41A0569	R1632054	SOFTWARE TESTING METHODOLOGIES	D	3
16U41A0593	R1632054	SOFTWARE TESTING METHODOLOGIES	C	3
16U41A05A0	R1632051	COMPUTER NETWORKS	F	0
16U41A05A0	R1632052	DATA WAREHOUSING AND MINING	D	3
16U41A05A0	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	F	0
16U41A05A0	R1632054	SOFTWARE TESTING METHODOLOGIES	D	3
16U41A05A4	R163205C	CYBER SECURITY	C	3
16U41A05A6	R1632052	DATA WAREHOUSING AND MINING	B	3
16U41A05B7	R1632054	SOFTWARE TESTING METHODOLOGIES	F	0
16U41A05C0	R163205C	CYBER SECURITY	D	3
16U41A05C4	R163205C	CYBER SECURITY	D	3
16U41A05C8	R1632052	DATA WAREHOUSING AND MINING	C	3
16U41A05D0	R163205C	CYBER SECURITY	C	3
17U45A0106	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	C	3
17U45A0109	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	F	0
17U45A0109	R1632012	GEOTECHNICAL ENGINEERING - I	F	0
17U45A0115	R1632012	GEOTECHNICAL ENGINEERING - I	F	0
17U45A0115	R1632014	WATER RESOURCE ENGINEERING -I	F	0
17U45A0117	R1632012	GEOTECHNICAL ENGINEERING - I	C	3
17U45A0133	R1632012	GEOTECHNICAL ENGINEERING - I	D	3
17U45A0133	R1632013	ENVIRONMENTAL ENGINEERING -I	D	3
17U45A0133	R1632014	WATER RESOURCE ENGINEERING -I	F	0
17U45A0133	R163201D	WASTE WATER MANAGEMENT	D	3
17U45A0139	R163201D	WASTE WATER MANAGEMENT	C	3
17U45A0142	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	C	3
17U45A0146	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	F	0
17U45A0149	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	D	3
17U45A0151	R1632011	DESIGN & DRAWING OF STEEL STRUCTURES	F	0
17U45A0202	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	B	3
17U45A0207	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	B	3
17U45A0208	R1632022	POWER SYSTEM ANALYSIS	F	0
17U45A0209	R1632022	POWER SYSTEM ANALYSIS	F	0

Htno	Subcode	Subname	Grade	Credits
17U45A0209	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	C	3
17U45A0209	R1632024	DATA STRUCTURES	D	3
17U45A0214	R1632022	POWER SYSTEM ANALYSIS	F	0
17U45A0214	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	C	3
17U45A0217	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	D	3
17U45A0217	R1632022	POWER SYSTEM ANALYSIS	F	0
17U45A0217	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	F	0
17U45A0217	R1632024	DATA STRUCTURES	F	0
17U45A0217	R163202A	UNIX AND SHELL PROGRAMMING	F	0
17U45A0221	R1632022	POWER SYSTEM ANALYSIS	C	3
17U45A0227	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	B	3
17U45A0230	R1632022	POWER SYSTEM ANALYSIS	C	3
17U45A0233	R1632022	POWER SYSTEM ANALYSIS	F	0
17U45A0234	R1632022	POWER SYSTEM ANALYSIS	C	3
17U45A0237	R1632022	POWER SYSTEM ANALYSIS	D	3
17U45A0237	R163202A	UNIX AND SHELL PROGRAMMING	F	0
17U45A0239	R1632022	POWER SYSTEM ANALYSIS	F	0
17U45A0241	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	B	3
17U45A0242	R1632022	POWER SYSTEM ANALYSIS	C	3
17U45A0247	R1632021	POWER ELECTRONIC CONTROLLERS & DRIVES	B	3
17U45A0247	R1632022	POWER SYSTEM ANALYSIS	C	3
17U45A0247	R1632024	DATA STRUCTURES	B	3
17U45A0250	R1632022	POWER SYSTEM ANALYSIS	F	0
17U45A0253	R1632022	POWER SYSTEM ANALYSIS	F	0
17U45A0254	R1632022	POWER SYSTEM ANALYSIS	D	3
17U45A0254	R163202A	UNIX AND SHELL PROGRAMMING	C	3
17U45A0402	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	C	3
17U45A0402	R1632044	DIGITAL SIGNAL PROCESSING	B	3
17U45A0407	R1632042	MICRO WAVE ENGINEERING	D	3
17U45A0407	R1632044	DIGITAL SIGNAL PROCESSING	C	3
17U45A0410	R1632044	DIGITAL SIGNAL PROCESSING	C	3
17U45A0416	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	F	0
17U45A0416	R1632042	MICRO WAVE ENGINEERING	F	0
17U45A0416	R1632043	VLSI DESIGN	D	3
17U45A0416	R1632044	DIGITAL SIGNAL PROCESSING	D	3
17U45A0420	R1632043	VLSI DESIGN	D	3
17U45A0420	R163204D	BIO-MEDICAL ENGINEERING	D	3
17U45A0422	R1632042	MICRO WAVE ENGINEERING	F	0
17U45A0422	R1632044	DIGITAL SIGNAL PROCESSING	C	3
17U45A0422	R163204D	BIO-MEDICAL ENGINEERING	C	3
17U45A0423	R1632043	VLSI DESIGN	B	3
17U45A0423	R1632044	DIGITAL SIGNAL PROCESSING	F	0

\*\*Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 30-01-2020 ]

\*\* Note:\*\*

\* -1 in the filed of externals indicates student is absent for the respective subject.

\* -2 in the filed of externals indicates student result Withheld for the respective subject.

\* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.

*Robert A. Kelly*

Date:23.01.2020

Controller of Examinations