



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for II B.Tech I semester (R13/R10) Regular/Supplementary Examinations Nov 2020

College name: DADI INSTT. OF ENGG. & TECH., GAVARAPALEM, ANAKAPALLE:U4

Htno	Subcode	Subname	Internal	External	CREDITS
10U41A0421	R21042	NETWORK ANALYSIS	13	7	0
10U41A0421	R21043	PROBABILITY THEORY & STOCHASTIC PROCESSE	18	-1	0
11U41A0120	R21016	MATHEMATICS-III	11	-1	0
12U41A0451	R21026	ELECTRONIC DEVICES AND CIRCUITS	9	36	4
13U41A0503	RT21034	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	17	-1	0
14U41A0425	RT21044	SIGNALS AND SYSTEMS	16	33	3
14U41A0529	RT21051	OBJECT ORIENTED PROGRAMMING THROUGH C++	18	22	0
15U41A0101	RT21013	STRENGTH OF MATERIALS-I	16	29	3
15U41A0215	RT21022	THERMAL AND HYDRO LRIME MOVERS	8	0	0
15U41A0215	RT21025	ELECTRO MAGNETIC FIELDS	9	20	0
15U41A0215	RT21026	ELECTRICAL MACHINES-I	12	6	0
15U41A0226	RT21024	COMPLEX VARIABLES AND STATISTICAL METHOD	19	27	3
15U41A0226	RT21026	ELECTRICAL MACHINES-I	15	3	0
15U41A0403	RT21041	ELECTRONIC DEVICES AND CIRCUITS	15	47	3
15U41A0403	RT21044	SIGNALS AND SYSTEMS	12	30	3
15U41A0403	RT21045	ELECTRICAL TECHNOLOGY	19	4	0
15U41A0421	RT21034	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	5	4	0
15U41A0421	RT21041	ELECTRONIC DEVICES AND CIRCUITS	0	1	0
15U41A0421	RT21044	SIGNALS AND SYSTEMS	17	3	0
15U41A0421	RT21045	ELECTRICAL TECHNOLOGY	17	2	0
15U41A0428	RT21041	ELECTRONIC DEVICES AND CIRCUITS	17	0	0
15U41A0428	RT21044	SIGNALS AND SYSTEMS	21	28	3
15U41A0502	RT21052	MATHEMATICAL FOUNDATIONS OF COMLUTER SCI	11	10	0
15U41A0517	RT21042	DATA STRUCTURES	19	12	0
15U41A0526	RT21053	DIGITAL LOGIC DESIGN	21	39	3
15U41A0534	RT21052	MATHEMATICAL FOUNDATIONS OF COMLUTER SCI	19	7	0
15U41A0540	RT21053	DIGITAL LOGIC DESIGN	18	27	3
15U41A0544	RT21053	DIGITAL LOGIC DESIGN	17	-1	0
15U41A0547	RT21053	DIGITAL LOGIC DESIGN	10	34	3
15U41A0566	RT21042	DATA STRUCTURES	18	17	0
15U41A0566	RT21051	OBJECT ORIENTED PROGRAMMING THROUGH C++	17	31	3
15U41A0566	RT21052	MATHEMATICAL FOUNDATIONS OF COMLUTER SCI	9	31	3
15U41A0566	RT21053	DIGITAL LOGIC DESIGN	3	26	0
15U41A0570	RT21053	DIGITAL LOGIC DESIGN	19	26	3
15U41A0578	RT21053	DIGITAL LOGIC DESIGN	20	43	3
15U41A0599	RT21042	DATA STRUCTURES	14	20	0
15U41A0599	RT21051	OBJECT ORIENTED PROGRAMMING THROUGH C++	22	29	3
15U41A0599	RT21053	DIGITAL LOGIC DESIGN	16	19	0
15U41A05C4	RT21034	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	19	3	0
15U41A05C4	RT21051	OBJECT ORIENTED PROGRAMMING THROUGH C++	20	20	0
15U41A05C4	RT21052	MATHEMATICAL FOUNDATIONS OF COMLUTER SCI	21	8	0
15U41A05C4	RT21053	DIGITAL LOGIC DESIGN	21	12	0
15U45A0208	RT21024	COMPLEX VARIABLES AND STATISTICAL METHOD	18	-1	0
15U45A0208	RT21026	ELECTRICAL MACHINES-I	18	-1	0
15U45A0212	RT21024	COMPLEX VARIABLES AND STATISTICAL METHOD	22	-1	0

Htno	Subcode	Subname	Internal	External	CREDITS
15U45A0212	RT21025	ELECTRO MAGNETIC FIELDS	16	-1	0
15U45A0225	RT21024	COMPLEX VARIABLES AND STATISTICAL METHOD	17	24	3
15U45A0240	RT21024	COMPLEX VARIABLES AND STATISTICAL METHOD	18	29	3
15U45A0246	RT21026	ELECTRICAL MACHINES-I	10	33	3
15U45A0403	RT21044	SIGNALS AND SYSTEMS	15	35	3
16U45A0401	RT21044	SIGNALS AND SYSTEMS	16	33	3
16U45A0432	RT21044	SIGNALS AND SYSTEMS	17	38	3

**Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 06-01-2021]

** Note:**

* -1 in the filed of externals indicates student is absent for the respective subject.

* -2 in the filed of externals indicates student result Withheld for the respective subject.

* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.



Date:31.12.2020

Controller of Examinations