

AKINADA कर्मसु कीशल

Htno	Subcode	Subname	Internal	External	CREDITS
10U41A0421	R22023	SWITCHING THEORY & LOGIC DESIGN	19	25	0
11U41A0541	R22052	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	17	18	0
11U41A0541	R22056	PRINCIPLES OF PROGRAMMING LANGUAGES	14	16	0
12U41A0451	R22023	SWITCHING THEORY & LOGIC DESIGN	12	39	4
12U41A0451	R22042	EMWTL	15	36	4
12U41A0452	R22042	EMWTL	17	-1	0
12U41A0484	R22026	CONTROL SYSTEMS	12	-1	0
12U41A0484	R22042	EMWTL	4	-1	0
12U41A0491	R22042	EMWTL	13	-1	0
12U41A0521	R22054	COMPUTER ORGANIZATION	19	34	4
12U41A0521	R22055	FORMAL LANGUAGES AND AUTOMATA THEORY	25	33	4
12U41A0521	R22056	PRINCIPLES OF PROGRAMMING LANGUAGES	14	33	4
13U41A0503	RT22055	FORMAL LANGUAGES AND AUTOMATA THEORY	15	-1	0
14U41A0207	RT22026	CONTROL SYSTEMS	17	-1	0
14U41A0529	RT22052	JAVA PROGRAMMING	7	10	0
14U41A0529	RT22055	FORMAL LANGUAGES AND AUTOMATA THEORY	10	22	0
15U41A0225	RT22026	CONTROL SYSTEMS	18	66	3
15U41A0403	RT22022	SWITCHING THEORY AND LOGIC DESIGN	9	14	0
15U41A0403	RT22041	ELECTRONIC CIRCUIT ANALYSIS	12	0	0
15U41A0403	RT22042	RANDOM VARIABLES & STOCHASTIC PROCESSES	11	12	0
15U41A0403	RT22044	EM WAVES AND TRANSMISSION LINES	20	25	3
15U41A0403	RT22045	ANALOG COMMUNICATIONS	19	27	3
15U41A0428	RT22022	SWITCHING THEORY AND LOGIC DESIGN	16	-1	0
15U41A0428	RT22041	ELECTRONIC CIRCUIT ANALYSIS	17	0	0
15U41A0428	RT22042	RANDOM VARIABLES & STOCHASTIC PROCESSES	20	2	0
15U41A0428	RT22044	EM WAVES AND TRANSMISSION LINES	17	32	3
15U41A0428	RT22045	ANALOG COMMUNICATIONS	26	5	0
15U41A0442	RT22044	EM WAVES AND TRANSMISSION LINES	19	28	3
15U41A0452	RT22042	RANDOM VARIABLES & STOCHASTIC PROCESSES	18	-1	0
15U41A0502	RT22052	JAVA PROGRAMMING	19	29	3
15U41A0517	RT22051	PROBABILITY AND STATISTICS	22	8	0
15U41A0517	RT22052	JAVA PROGRAMMING	20	10	0
15U41A0517	RT22055	FORMAL LANGUAGES AND AUTOMATA THEORY	18	16	0
15U41A0525	RT22052	JAVA PROGRAMMING	17	-1	0
15U41A0534	RT22051	PROBABILITY AND STATISTICS	20	10	0
15U41A0534	RT22053	ADVANCED DATA STRUCTURES	25	24	3
15U41A0540	RT22051	PROBABILITY AND STATISTICS	23	12	0
15U41A0546	RT22052	JAVA PROGRAMMING	19	34	3
15U41A05B0	RT22055	FORMAL LANGUAGES AND AUTOMATA THEORY	16	28	3
15U45A0204	RT22022	SWITCHING THEORY AND LOGIC DESIGN	21	11	0
15U45A0208	RT22022	SWITCHING THEORY AND LOGIC DESIGN	12	-1	0
15U45A0208	RT22023	PULSE & DIGITAL CIRCUITS	10	-1	0
15U45A0208	RT22026	CONTROL SYSTEMS	13	-1	0
15U45A0212	RT22023	PULSE & DIGITAL CIRCUITS	16	-1	0
15U45A0212	RT22024	POWER SYSTEMS-I	18	-1	0

Htno	Subcode	Subname	Internal	External	CREDITS
15U45A0246	RT22026	CONTROL SYSTEMS	17	51	3
16U45A0246	RT22023	PULSE & DIGITAL CIRCUITS	23	2	0
16U45A0246	RT22025	ELECTRICAL MACHINES-II	22	36	3
16U45A0401	RT22042	RANDOM VARIABLES & STOCHASTIC PROCESSES	18	36	3

^{**}Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is: 14-02-2021]

Date:03.02.2021

Controller of Examinations

Oglest a. helle

^{**} Note:**

^{* -1} in the filed of externals indicates student is absent for the respective subject.

^{* -2} in the filed of externals indicates student result Withheld for the respective subject.

^{* -3} in the filed of externals indicates student involved in Malpractice for the respective subject.