

# **COURSE STRUCTURE AND SYLLABUS**

**For**

## **ELECTRONICS AND COMMUNICATION ENGINEERING**

*(Applicable for batches admitted from 2016-2017)*



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA**  
**KAKINADA - 533 003, Andhra Pradesh, India**

### III Year - I Semester

S.No.	Subjects	L	T	P	Credits
1	Computer Architecture and Organization	4	--	--	3
2	Linear I C Applications	4	--	--	3
3	Digital I C Applications	4	--	--	3
4	Digital Communications	4	--	--	3
5	Antenna and Wave Propagation	4	--	--	3
6	Pulse and Digital Circuits Lab	--	--	3	2
7	Linear I C Applications Lab	--	--	3	2
8	Digital I C Applications Lab	--	--	3	2
MC	Professional Ethics & Human Values	--	3	--	--
<b>Total Credits</b>					<b>21</b>

### III Year - II Semester

S.No.	Subjects	L	T	P	Credits
1	Micro Processors & Micro Controllers	4	--	--	3
2	Micro Wave Engineering	4	--	--	3
3	VLSI Design	4	--	--	3
4	Digital Signal Processing	4	--	--	3
5	<b>OPEN ELECTIVE</b> 1. OOPs through Java 2. Data Mining 3. Industrial Robotics 4. Power Electronics 5. Bio-Medical Engineering 6. Artificial Neural Networks	4	--	--	3
6	Micro Processors & Micro Controllers Lab	--	--	3	2
7	VLSI Lab	--	--	3	2
8	Digital Communications Lab	--	--	3	2
MC	IPR & Patents	--	2	--	--
<b>Total Credits</b>					<b>21</b>

#### IV Year - I Semester

S.No.	Subjects	L	T	P	Credits
1	Radar Systems	4	--	--	3
2	Digital Image Processing	4	--	--	3
3	Computer Networks	4	--	--	3
4	Optical Communications	4	--	--	3
5	<b>Elective I</b> 1. TV Engineering 2. Electronic Switching Systems 3. System Design through Verilog	4	--	--	3
6	<b>Elective II</b> 1.Embedded Systems 2. Analog IC Design 3.Network Security & Cryptography	4	--	--	3
7	Micro Wave Engineering & Optical Lab	--	--	2	2
8	Digital Signal Processing Lab	--	--	2	2
<b>Total Credits</b>					<b>22</b>

#### IV Year - II Semester

S.No.	Subjects	L	T	P	Credits
1	Cellular Mobile Communications	4	--	--	3
2	Electronic Measurements and Instrumentation	4	--	--	3
3	Satellite Communications	4	--	--	3
4	<b>Elective III</b> 1.Wireless sensors & Networks 2. Digital IC Design 3. Operating Systems	4	--	--	3
5	<b>Seminar</b>	--	3	--	2
6	<b>Project</b>	--	--	--	10
<b>Total Credits</b>					<b>24</b>

**Total Course Credits = 48+44 + 42 + 46 = 180**