



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by A.I.C.T.E. & Permanently Affiliated to JNTUK)

NAAC Accredited Institute & Recognized under Section 2(f) & 12(B) of UGC Act
An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle – 531002, Visakhapatnam, A.P.

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Date:10-02-2023

To
The Principal,
Dadi Institute of Engineering & Technology
Anakapalle, Visakhapatnam.

Sub: Request for Approval to conduct a event on “Motivational Session by Successful Innovators”
on 13-02-2023.

Respected Sir,

Department of Electronics and communication Engineering is interested to conduct a event on
“Motivational Session by Successful Innovators” on 13-02-2023 Program for the students. . We
request to give approval to conduct this event.

Thank You Sir,

Approved
Ravi Kumar
10/2/23

yours faithfully,

P. Poornima Rao
Department of ECE

Electronics & Communication Engg.
Dadi Institute of Engineering & Tech
Anakapalle - 531002

PRINCIPAL
Dadi Institute of
Engineering & Technology
ANAKAPALLE - 531002



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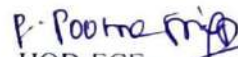
Circular

12-02-2023

This is to inform all the students that , Department of Electronics and communication engineering is interested to conduct a event on "Motivational Session by Successful Innovators". Programme on 13-02-2023.


PRINCIPAL
Dadi Institute of
Engineering & Technology
ANAKAPALLE - 531 002


12/2/23


HOD-ECE
Head of the Department
Electronics & Communication Engineering
Dadi Institute of Engg. & Tech
Anakapalle-531002



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

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Report on Motivational Session by Successful Innovators

Name of the Event: Motivational Session by Successful Innovators

Venue: Seminar Hall 2

Date: 13-02-2023

No. Of. Students participated: 23.

The poster features the DADI Institute of Engineering & Technology logo at the top left, including the tagline 'Build New World'. The main title 'Motivational Session by successful Innovators' is written in a large, white, sans-serif font against a dark purple background. Below the title, the speaker's name 'DR. P. POORNA PRIYA' and her title 'ASSOCIATE PROFESSOR, DIET' are listed. The venue 'VENUE: SEMINAR HALL 2' and date 'DATE AND TIME: 13-02-2023' are also provided. On the right side, there is a logo for the 'INSTITUTION'S INNOVATION COUNCIL (Ministry of IITD Initiative)'. The top section of the poster contains the institute's accreditation and contact information, identical to the header above.

The Institutes Innovations Council Design of Multiband patch antenna with Chebyshev distribution slotting techniques for IOT Applications is a patent applied as patent by speaker.

The following Technical matter discussed and she explained the procedure for patent application and the procedure to grant patent in successful manner.

A different kind of approach was employed in designing a multi-band antenna by using Chebyshev distribution to fulfil IOT Applications on FR4 substrate. The presented approach is based on combining a rectangular and an equilateral triangular patch with proper slots placed on each part and is simulated fabricated and tested. By changing certain feed position desired application within single antenna this type of antenna can be used in several IOT applications.

The whole system is being fed by a wire edge into the substrate with input impedance of 50Ω . Several results are verified to show the versatility of this antenna. The proposed antenna has many applications in IOT field applications. The concept of inserting slot arrays following a known Chebyshev distribution has proven to give remarkable versatility to an antenna design.

Numerous applications can subject for this newly designed antenna, it can be used for various broad band applications. By changing certain feed positions only we can achieve certain frequency of prescribed application. The s_{11} parameter for every resonant frequency are under -10dB . This new wideband operation of the antenna shares the presence of resonances at IOT application

This Idea will give unique antenna design for IOT applications using chebyshev distribution for slot design to reach different resonant frequencies for wide range.



Dr.P.Poorna Priya,Associate Professor,ECE Expressing her views

Dr.P.Poorna Priya , Associate professor explaining the procedure of innovation

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STUDENT PARTICIPATE:

S.No.	HT.No	STUDENT NAME	SIGNATURE OF THE STUDENT
1	21U41A0446	AGATHAMUDI MANOJ KUMAR	Manoj kumar
2	21U41A0448	BHEESETTI RUCHITHA	Ruchitha
3	21U41A0449	BODDETI UMA MALLESWARI	Uma
4	21U41A0450	CHANDANALA NAGA VENKATA SIVA KIRAN	Seeran
5	21U41A0451	CHIKKALA RUTHI SAMHITHA	Ramitha
6	21U41A0452	DADI BABITHA	D. Babitha
7	21U41A0453	DAILAPALLI ASHOK	Ashok
8	21U41A0454	DURGA BHARGAVI	Bhargavi
9	21U41A0456	GUDIVADA SHYAM PRASAD	Shyam Prasad
10	21U41A0458	KANDREGULA JYOSHNA	Jyoshna
11	21U41A0459	KUDUPUDI JYOTHI SINDHURA	K.S. Sindhura
12	21U41A0460	KUNDRAPU TEJASWINI	K. Tejaswini
13	21U41A0462	MADIMI BHAGYA SRI VENKATALAKSHMI	M.B Sri Venkata Lakshmi
14	21U41A0463	MADIREDDI HARIKRISHNA	M. Hari Krishna
15	21U41A0464	MD.FARHAN	MD. Farhan
16	21U41A0465	MEDISETTI CHANDRASEKHAR	M. Chandra Sekhar
17	21U41A0466	MOTURU REVATHI	Revathi
18	21U41A0467	MUTCHAKARLA GANESH	M. Ganesh
19	21U41A0468	NAMBARI POOJITHA	Poojitha
20	21U41A0469	NUNNA SAI VISWA	Viswa
21	21U41A0470	PEDIREDDY VENKATA SATYA	P. Venkata Satya
22	21U41A0471	PENTAKOTA AKANKSHA	P. Akanksha
23	21U41A0472	PONNADA TARUN KUMAR	Tarun Kumar