



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by AICTE & Permanently affiliated to JNTU GV

Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act

An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle - 531002, Visakhapatnam, A.P.

Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

Academic year: 2024-25

Anakapalle,

Dt: 15-10-2024

From,
Dr. ASLK .Gopalamma
HOD-EEE,
Dadi Institute of Engineering & Technology.

(Through Proper Channel)

To,

The Principal,
Dadi Institute of Engineering & Technology.

Sub: Permission for Conduction of **GATE (Graduate Aptitude Test in Engineering) Training Programme** for IV B. Tech EEE, Students-Reg.

Sir,

With due respect, here by stating that, I, on behalf of CSE Department request you for conducting of Conduction of **GATE Training Programme** for our IV - B. Tech EEE, Students who are under eligible criteria and are also interested. The **GATE Programme** schedule is planned on 15-10-2024 without hindering the regular class work. Arrangements are done without disturbing the regular class work schedule.

We, therefore, hope that you would be kind enough to permit us to conduct the National Conference. Kindly grant us the permission. A waiting anxiously for your reply.

Thanking you Sir,

Yours Sincerely,


Dr. ASLK .Gopalamma
HOD-EEE,

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Techn.
Anakapalle - 531 002



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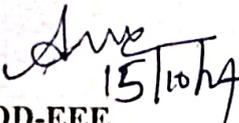
CIRCULAR

DATE: 15/10/2024

This is to inform all the IV-B.Tech EEE Students of Dadi Institute of Engineering and Technology that the Department of EEE is conducting **GATE(Graduate Aptitude Test in Engineering) Training Programme** on 15-10-2024. So Students of IV-B.Tech EEE are instructed to kindly make a best use of this opportunity.

Happy Learning & All the Best.

Venue: LH 42.


15/10/24
HOD-EEE

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Tech.
Anakapalle - 531 002


15/10/24
PRINCIPAL

PRINCIPAL
Dadi Institute of
Engineering & Technology
Autonomous
Anakapalle - 531 002.

GATE RECRUITMENT TRAINING

DADI INSTITUTE OF ENGINEERING AND TECHNOLOGY Department of EEE

GATE Training Report

To foster **employable, confident, dynamic, and skill-equipped students**, Dadi Institute of Engineering and Technology (DIET) has undertaken several initiatives to prepare its students for the competitive demands of the corporate world. The primary objective is to mold students who are not only industry-ready but also capable of embracing entrepreneurial ventures.

Objective:

To produce students who can effectively handle the operational norms of industries and commerce in both public and private sectors. The training also aims to instil entrepreneurial skills among students to encourage self-employment and start-up ventures.

Department of EEE

GATE HANDLING FACULTY 2024-2025

S.NO	SUBJECT	FACULTY
1	ELECTRICAL CIRCUITS	Dr AS LK GOPALAMMA
2	SIGNALS& SYSTEMS	Dr S RAMANA KUMAR JOGA
3	POWER SYSTEMS	Mr K.SRINIVASA RAO
4	ELECTROMAGNETIC FIELD THEORY	Mr V. SUDHAKAR
5	CONTROL SYSTEMS	Mr. G.JAGADEESH
6	ELECTRICAL MACHINES	Mr K.VIJAY KUMAR
7	POWER ELECTRONICS	Mrs K. Alfoni Jose

LIST OF STUDENTS

S.No.	HT.No	StudentName
1	21U41A0201	BHEEMARASETTI CHANDU
2	21U41A0202	BOTCHA SHRAVANI KUMARI
3	21U41A0203	KANTAM REDDY BALA
4	21U41A0204	MUDUNURU KALYAN
5	21U41A0205	PALAKA SOWMYA
6	21U41A0206	PAPPALA YASWANTH
7	21U41A0207	KAMBAPU SIDDARDHA REDDY
8	21U41A0208	POLAMARASETTI PURNA GANESH
9	21U41A0209	YEGIREDDY SIRISHA
10	22U45A0201	ANUBOTHU HARIKRISHNA
11	22U45A0202	BALASA DILESH MADHAV
12	22U45A0203	BANAİKANA SRAVANI
13	22U45A0204	BHUMARAJU BALAJI
14	22U45A0205	BOBBARI LAVANYA
15	22U45A0206	BOBBARI MOHAN SAI
16	22U45A0207	CHUKKA SUNEETHA
17	22U45A0208	DADI BHARGAV APOOR
18	22U45A0209	DASARI ARUNA
19	22U45A0210	EARUSU RAMYANKA
20	22U45A0211	EERLA ARCHANA
21	22U45A0212	GIRIJALA MOUNIKA
22	22U45A0214	GUDABANDI SRINIVAS
23	22U45A0215	JONNADA SWASTHIK KUMAR
24	22U45A0216	JUVVALA VIJAY KUMAR
25	22U45A0217	KAKUMANU V K VASANTHA CHARI
26	22U45A0218	KANCHUMURTY SHARAN SAI
27	22U45A0219	KANDULA TEJA
28	22U45A0220	KOLLI SRINIVAS
29	22U45A0222	KOPPISETTI BABY SAROJINI
30	22U45A0223	KOTLA SAI DEEPAK
31	22U45A0224	KOTTE DIVYA NAGA BALA SAI
32	22U45A0225	LOKANADHAM SAI KAMAL
33	22U45A0226	MADAKA BHARGAVI
34	22U45A0227	MOLLI ARUN KUMAR
35	22U45A0228	NAKKA VISHNU VARDHAN KUMAR
36	22U45A0229	NELAPARTHI PRAVEEN KUMAR
37	22U45A0230	NIDRABINGI GIRIJA
38	22U45A0231	SINGAMPALLI VISWAKANTH
39	22U45A0232	SURISSETTY JAGADEESH
40	22U45A0233	TANETI GNANA PRAKASH
41	22U45A0234	THUMMAGUNTA SATYA DHANUSH
42	22U45A0235	URITI RAVI

43	22U45A0236	VEGI DHANRAJ
44	22U45A0237	YENNI KARTHIK
45	22U45A0238	VASAMSETTI HARI SHANKAR GANESH
46	22U45A0239	DODDI CHAKRADHAR
47	22U45A0240	SENAPATHI PAVAN
48	22U45A0241	KOVIRI JITENDHRA SATEESH

SCHEDULE OF GATE

IV EEE

TIME TABLE

DADI INSTITUTE OF ENGINEERING AND TECHNOLOGY

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Department of Electrical and Electronics Engineering

EEE

B.Tech/ IV/ II Sec A

Academic Year - 2024-25

GATE TIME TABLE

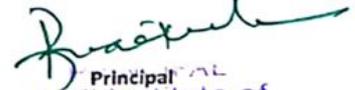
w.e.f 4/11/2024

Class Teacher - Mrs. K Alfoni Jose Total Strength- 48 ROOM NO :42

Day/ PERIOD	9:00-9:50 AM	9:50-10:40 AM	10:40-11 AM	11:00-11:50 AM	11:50-12:40 PM	12:40 - 1:30 PM	1:30-2:20 PM	2:20-3:10 PM	3:10- 4:00 PM	
MON			B R E A K			L U N C H				
TUE	GATE	GATE								
WED										
THU				GATE	GATE					
FRI										

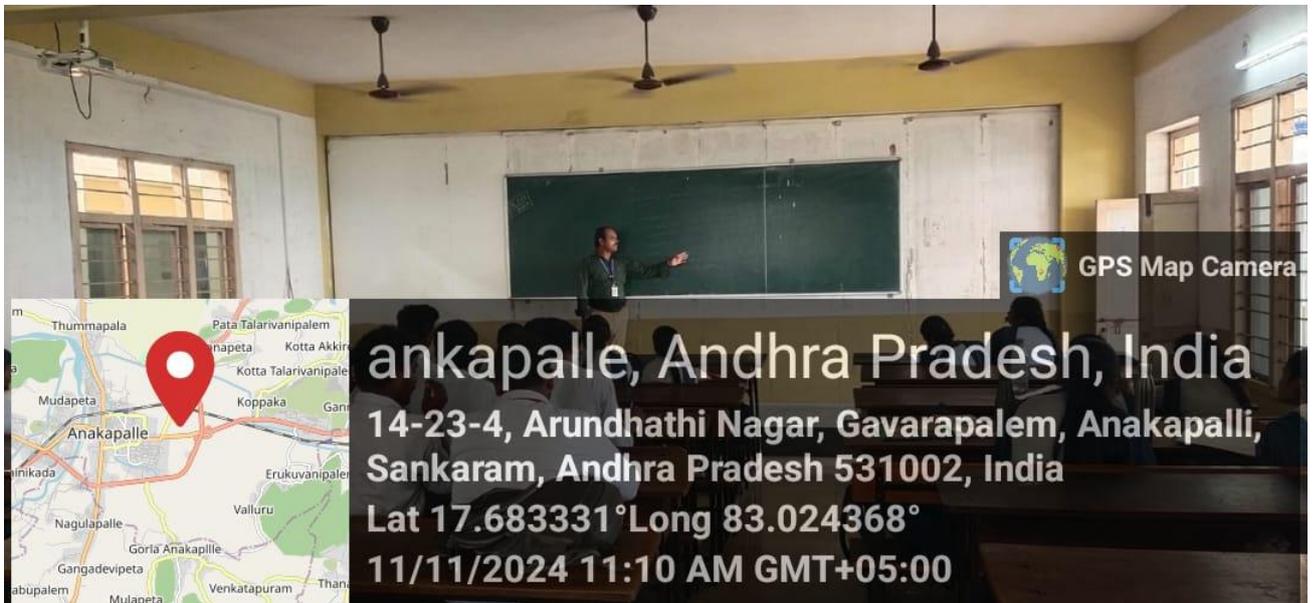

HoD, EEE

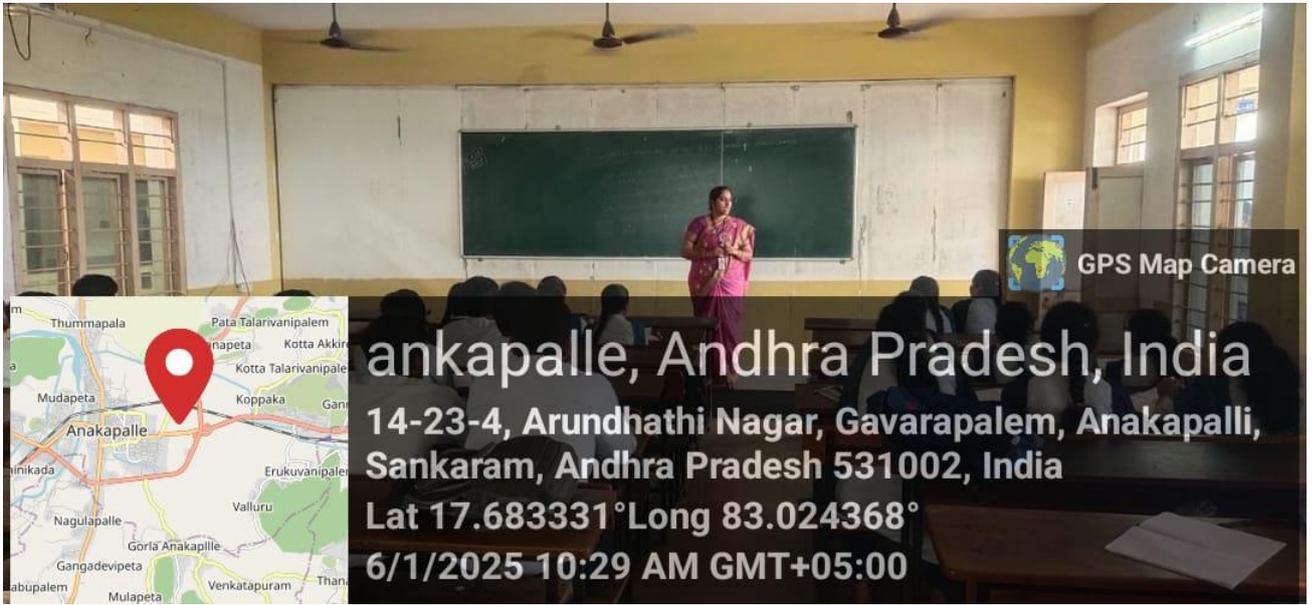
Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Techn.
Anakapalle - 531 002


Principal

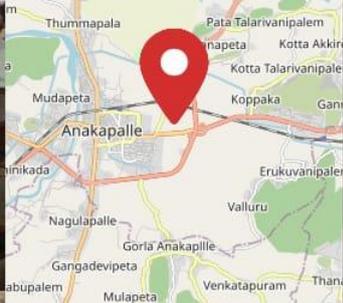
Dadi Institute of
Engineering & Technology
Autonomous
Anakapalle - 531 002.

Glimpses of GATE





GPS Map Camera



anakapalle, Andhra Pradesh, India
14-23-4, Arundhathi Nagar, Gavarapalem, Anakapalli,
Sankaram, Andhra Pradesh 531002, India
Lat 17.683331° Long 83.024368°
6/1/2025 10:29 AM GMT+05:00



GPS Map Camera



anakapalle, Andhra Pradesh, India
14-23-4, Arundhathi Nagar, Gavarapalem, Anakapalli,
Sankaram, Andhra Pradesh 531002, India
Lat 17.683331° Long 83.024368°
26/11/2024 11:35 AM GMT+05:00

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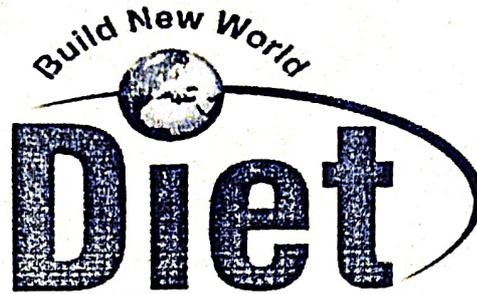
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ANAKAPALLE, VISAKHAPATNAM



B.Tech.

STUDENT ACADEMIC REGISTER

ACADEMIC YEAR 2024-25

REGULATIONS R20

NAME OF THE FACULTY 2024-25

BRANCH EEF YEAR IV SEM. II

SUBJECT GATE

DURATION FROM 5/11/2024 TO 4/2/2025

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

ANAKAPALLE

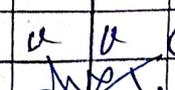
IV-II EEE

(2021 Admitted Batch)

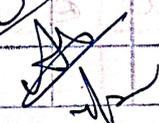
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48	22U45A0241	KOVIRI JITENDHIRA SATEESH

Dadi Institute of Engineering & Technology (A), Anakapalle.

	3/12	5/12	10/12	12/12	13/12	14/12	24/12	26/12	31/12	2/1	9/1	21/1	23/1	28/1	30/1
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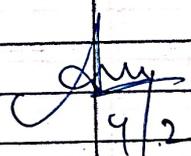

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SYLLABUS COVERAGE REPORT

Sl. No.	Date	No. of Periods engaged	Topic Covered	REMARKS
01	5/11	1	Network elements, Ideal current & Voltage Source R, L, C elements.	
02	07/11	1	KCL, KVL Node & Mesh Analysis Thevenin's theorem & Norton's theorem	
03	12/11	01	Superposition & Maximum power transfer theorem	
04	14/11	01	Transient response of D.C circuit	
05	19/11	01	Transient response of A.C circuit	
06	21/11	01	Balanced three phase system	
07	26/11	01	Star / Delta transformation	
08	28/11	01	Complex power & power factor of A.C circuit	
09	03/12	01	Transmission line Constants	
10	05/12	01	concept of GMR & GMD	
11	10/12	01	power System Transients	
12	12/12	01	Capacitance of a transmission line	

Sl. No.	Date	No. of Periods engaged	Topic Covered	REMARKS
	17/12	01	Corona, problem with corona	
	19/12	01	Factors affecting corona	
	24/12	01	Used Critical Disruptive Voltage	
	26/12	01	problem on corona loss	
	31/12	01	characteristics of SCR, mosfet & IGBT	
	7/1	01	1- ϕ 3- ϕ Rectifier using SCR	
	9/1	01	1- ϕ cyclo converter & AC drive	
	21/1	01	Speed control of DC motor fed from 1- ϕ fully controlled rectifier	
	23/1	01	3- ϕ Inverter	
	28/1	01	Power connection & line connection	
	30/1	01	Time Response	
	4/2	01	Steady state Error.	 4/2

EE Electrical Engineering

Section 1: Engineering Mathematics

Linear Algebra: Matrix Algebra, Systems of linear equations, Eigen values, Eigen vectors.

Calculus: Mean value theorems, Theorems of integral calculus, Evaluation of definite and improper integrals, Partial Derivatives, Maxima and minima, Multiple integrals, Fourier series, Vector identities, Directional derivatives, Line integral, Surface integral, Volume integral, Stokes's theorem, Gauss's theorem, Divergence theorem, Green's theorem.

Differential equations: First order equations (linear and nonlinear), Higher order linear differential equations with constant coefficients, Method of variation of parameters, Cauchy's equation, Euler's equation, Initial and boundary value problems, Partial Differential Equations, Method of separation of variables.

Complex variables: Analytic functions, Cauchy's integral theorem, Cauchy's integral formula, Taylor series, Laurent series, Residue theorem, Solution integrals.

Probability and Statistics: Sampling theorems, Conditional probability, Mean, Median, Mode, Standard Deviation, Random variables, Discrete and Continuous distributions, Poisson distribution, Normal distribution, Binomial distribution, Correlation analysis, Regression analysis.

Section 2: Electric circuits

Network elements: ideal voltage and current sources, dependent sources, R, L, C, M elements; **Network solution methods:** KCL, KVL, Node and Mesh analysis; **Network Theorems:** Thevenin's, Norton's, Superposition and Maximum Power Transfer theorem; **Transient response of dc and ac networks,** sinusoidal steady-state analysis, resonance, two port networks, balanced three phase circuits, star-delta transformation, complex power and power factor in ac circuits.

Section 3: Electromagnetic Fields

Coulomb's Law, Electric Field Intensity, Electric Flux Density, Gauss's Law, Divergence, Electric

field and potential due to point, line, plane and spherical charge distributions, Effect of dielectric medium, Capacitance of simple configurations, Biot-Savart's law, Ampere's law, Curl, Faraday's law, Lorentz force, Inductance, Magnetomotive force, Reluctance, Magnetic circuits, Self and Mutual inductance of simple configurations.

Section 4: Signals and Systems

Representation of continuous and discrete time signals, shifting and scaling properties, linear time invariant and causal systems, Fourier series

representation of continuous and discrete time periodic signals, sampling theorem, Applications of Fourier Transform for continuous and discrete time signals, Laplace Transform and Z transform. R.M.S. value, average value calculation for any general periodic waveform

Section 5: Electrical Machines

Single phase transformer: equivalent circuit, phasor diagram, open circuit and short circuit tests, regulation and efficiency; Three-phase transformers: connections, vector groups, parallel operation; Auto-transformer, Electromechanical energy conversion principles; DC machines: separately excited, series and shunt, motoring and generating mode of operation and their characteristics, speed control of dc motors; Three-phase induction machines: principle of operation, types, performance, torque-speed characteristics, no-load and blocked-rotor tests, equivalent circuit, starting and speed control; Operating principle of single-phase induction motors; Synchronous machines: cylindrical and salient pole machines, performance and characteristics, regulation and parallel operation of generators, starting of synchronous motors; Types of losses and efficiency calculations of electric machines

Section 6: Power Systems

Basic concepts of electrical power generation, ac and dc transmission concepts, Models and performance of transmission lines and cables, Economic Load Dispatch (with and without considering transmission losses), Series and shunt compensation, Electric field distribution and insulators, Distribution systems, Per-unit quantities, Bus admittance matrix, Gauss-Seidel and Newton-Raphson load flow methods, Voltage and Frequency control, Power factor correction, Symmetrical components, Symmetrical and unsymmetrical fault analysis, Principles of over-current, differential, directional and distance protection; Circuit breakers, System stability concepts, Equal area criterion.

Section 7: Control Systems

Mathematical modelling and representation of systems, Feedback principle, transfer function, Block diagrams and Signal flow graphs, Transient and Steady-state analysis of linear time invariant systems, Stability analysis using Routh-Hurwitz and Nyquist criteria, Bode plots, Root loci, Lag, Lead and Lead-Lag compensators; P, PI and PID controllers; State space model, Solution of state equations of LTI systems

Section 8: Electrical and Electronic Measurements

Bridges and Potentiometers, Measurement of voltage, current, power, energy and power factor; Instrument transformers, Digital voltmeters and multi-meters, Phase, Time and Frequency measurement; Oscilloscopes, Error analysis.

Section 9: Analog and Digital Electronics

Simple diode circuits: clipping, clamping, rectifiers; Amplifiers: biasing, equivalent circuit and frequency response; oscillators and feedback amplifiers; operational amplifiers: characteristics and applications; single stage active filters, Active Filters: Sallen Key, Butterworth, VCOs and timers, combinatorial and sequential logic circuits, multiplexers, demultiplexers, Schmitt triggers, sample and hold circuits, A/D and D/A converters.

Section 10: Power Electronics

Static V-I characteristics and firing/gating circuits for Thyristor, MOSFET, IGBT; DC to DC conversion: Buck, Boost and Buck-Boost Converters; Single and three-phase configuration of uncontrolled rectifiers; Voltage and Current commutated Thyristor based converters; Bidirectional ac to dc voltage source converters; Magnitude and Phase of line current harmonics for uncontrolled and thyristor based converters; Power factor and Distortion Factor of ac to dc converters; Single-phase and three-phase voltage and current source inverters, sinusoidal pulse width modulation.



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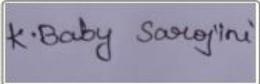
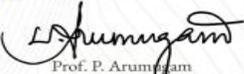
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Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

STUDENT ADMIT CARDS

	GRADUATE APTITUDE TEST IN ENGINEERING 2025 अभियांत्रिकी स्नातक अभिक्षमता परीक्षा २०२५	S4 (AN)
ADMIT CARD		
 	(Name) BABY SAROJINI KOPPISETTI	
	(Registration No.) EE25S46120283	(Paper Code/Name) EE : Electrical Engineering
	(Date) 2nd February 2025 Sunday	(Time) 2:30 PM to 5:30 PM
	Examination Centre: 6120 ION Digital Zone iDZ Sheela Nagar, Door No. 32-12-329/5 and 6, Icon Krishi Hospitals Road, Sheela Nagar, Visakhapatnam, Andhra Pradesh, PIN : 530012, India.	
 R227F59 264EA0ECD9D356C32F778E22DC790786	<i>Organising Institute</i> IIT Roorkee	 Prof. P. Arumugam Organising Chairperson, GATE 2025 (on behalf of NCB-GATE, for MoE)

	GRADUATE APTITUDE TEST IN ENGINEERING 2025 अभियांत्रिकी स्नातक अभिक्षमता परीक्षा २०२५	S4 (AN)
ADMIT CARD		
 	(Name) SHARAN SAI KANCHUMURTHY	
	(Registration No.) EE25S46121606	(Paper Code/Name) EE : Electrical Engineering
	(Date) 2nd February 2025 Sunday	(Time) 2:30 PM to 5:30 PM
	Examination Centre: 6121 ION Digital Zone iDZ 2 Chinamushidiwada, Do.No - 15-6-3 , Beside NRI College, Opp. ABN Church, Chinamushidiwada, Pendurthi Mandal, Visakhapatnam, Andhra Pradesh, PIN : 531173, India.	
 R228G91 7906B4C0E5D4F4635A9C139C41282ACD	<i>Organising Institute</i> IIT Roorkee	 Prof. P. Arumugam Organising Chairperson, GATE 2025 (on behalf of NCB-GATE, for MoE)



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Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

STUDENT SCORE CARD



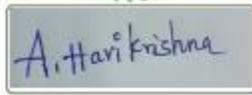
GRADUATE APTITUDE TEST IN ENGINEERING 2025
 अभियांत्रिकी स्नातक अभिक्षमता परीक्षा २०२५
 Organising Institute: INDIAN INSTITUTE OF TECHNOLOGY ROORKEE

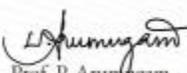


SCORE CARD

Name of the Candidate	ANUBOTHU HARIKRISHNA
Name of the Parent/Guardian	ANUBOTHU RAMANAMMA
Registration No.	EE25S46121471
Date of Birth	January 18, 2003
Test Paper	Electrical Engineering (EE)
Date of Examination	February 2, 2025
GATE Score	346
Marks out of 100	24.67

All India Rank (AIR) in the test paper: 8472	Qualifying Marks
Number of candidates appeared for the test paper: 67701	General: 25.0
	EWS/OBC-NCL: 22.5
	SC/ST/PwD: 16.6



Prof. P. Arunigam
Organising Chairperson, GATE 2025
On behalf of NCB-GATE
Ministry of Education (MoE)



A candidate is considered **qualified** if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this Score Card.

This Score Card is valid up to 31st March 2028.

2690386326111442a759b8b8485275

GATE SCORE COMPUTATION



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NII-16, Anakapalle - 531002, Visakhapatnam, A.P.
Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

Academic year: 2024-25

Anakapalle,
Dt:21-10-2024

From,
Dr.P.Poorna Priya,
HOD,ECE,
Dadi Institute of Engineering & Technology.

(Through Proper Channel)

To,

The Principal,
Dadi Institute of Engineering & Technology.

Sir,
Sub:Permission for Conduction of GATE Classesf or IV BTech ECE Students-Reg

With due respect, here by stating that, I, on behalf of ECE Department request you for conduction of GATE Classes for our ECE IV B Tech students who are under eligible criteria and are also interested. The GATE Classwork schedule is planned for 3 hours on every Wednesday and Thursday, the time table schedule will be shared a week prior to commencement.

We, therefore, hope that you would be kind enough to permit us to conduct the GATE Classes. Kindly grant us the permission. Awaiting anxiously for your reply.

Thanking you Sir,

Yours Sincerely,

Permitted

Quaited

P. Poorna Priya
Dr.P.PoornaPriya,
HOD,ECE,
Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech.
An Autonomous Institute
Anakapalle-531002



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Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

CIRCULAR

DATE:23/10/2024

This is to inform all the IV BTech ECE Students that the Department of ECE is Conducting GATE Classes for interested and eligible students on every Wednesday and Thursday from 30/10/2024 .So students of IV BTech ECE are instructed to kindly make the best use of this opportunity.The subject schedule and timetable will be shared accordingly.

HAPPY LEARNING & ALL THE VERY BEST!!

Venue:LH-27

P. Poornima
HODECE
Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech.
An Autonomous Institute
Anakapalle-531002

PRINCIPAL

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Dadi Institute of
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Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

DEPARTMENT OF ECE GATE HANDLING FACULTY-2024-25

S.NO.	SUBJECT	FACULTY
1	Network Analysis	R.SuneelKumar
2	Digital Electronics	Sk.Shabeena <i>Shabeena</i>
3	Signal and Systems	P.Amrutha <i>Amrutha</i>
4	Control Systems	Ch.Ramana Babu <i>ChR</i>
5	Communication Systems	K.Someswara Rao <i>KSR</i>
6	Analog Electronics	ArchanaBT <i>Archana</i>
7	Electronic Devices and Circuits	A.Sankara Rao <i>SAS</i>
8	Electromagnetic Theory	Dr.P.Poorna Priya <i>PP</i>

P. Poorna Priya
HOD ECE
Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech.
An Autonomous Institute
Anakapalle-531002

R. Suneel Kumar
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Dadi Institute of
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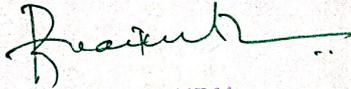
Dept. Name -ECE-A Course / Year / Sem -B.Tech/ IV/II Academic Year - 2024-25

w.e.f: 30-10-2024 Class Teacher - Dr.B.Anjanees Kumar Total Strength- 57 LH-27

Day/Time	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-3:00	3:00-4:00
Wednesday	DE	SS	EDC	LUNCH	CS	PROJ	PROJ
Thursday	CS	AE	EMT	LUNCH	COS	PROJ	PROJ

S.No	Subject Name	Faculty name	No: Of Periods
1	NetworkAnalysis(NA)	R.SuneelKumar	1
2	Digital Electronics(DE)	Sk.Shabeena	1
3	Signal andSystems(SS)	P.Amruha	1
4	Control Systems(CS)	Ch.Ramana Babu	1
5	Communication Systems(COS)	K.Someswara Rao	1
6	Analog Electronics(AE)	ArchanaBT	1
7	Electronic Devices and Circuits(EDC)	A.Sankara Rao	1
8	ElectromagneticTheory(EMT)	Dr.P.Poorna Priya	1
9	PROJECT WORK(PROJ)	Project Guides	4

P. Poorna Priya
Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech.
An Autonomous Institute
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Department of ECE FEEDBACK FORM ON GATE CLASSES 2024-25

1. Did the GATE Schedule attained its objectives

- Yes
- No

2. GATE Training was relevant to my needs

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

3. Instructions were clear and understandable

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

4. Content was well organised

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

5. Was the Duration of the training sufficient.

- Yes
- No

6. Resource persons were effective.

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

7. Queries were encouraged

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

8. Any additional remarks

9. Overall how would you rate this schedule.

- Excellent
- Very good
- Good
- Fair
- Poor

Section 1: Engineering Mathematics

Linear Algebra: Vector space, basis, linear dependence and independence, matrix algebra, eigenvalues and eigenvectors, rank, solution of linear equations- existence and uniqueness.

Calculus: Mean value theorems, theorems of integral calculus, evaluation of definite and improper integrals, partial derivatives, maxima and minima, multiple integrals, line, surface and volume integrals, Taylor series.

Differential Equations: First order equations (linear and nonlinear), higher order linear differential equations, Cauchy's and Euler's equations, methods of solution using variation of parameters, complementary function and particular integral, partial differential equations, variable separable method, initial and boundary value problems.

Vector Analysis: Vectors in plane and space, vector operations, gradient, divergence and curl, Gauss's, Green's and Stokes' theorems.

Complex Analysis: Analytic functions, Cauchy's integral theorem, Cauchy's integral formula, sequences, series, convergence tests, Taylor and Laurent series, residue theorem.

Probability and Statistics: Mean, median, mode, standard deviation, combinatorial probability, probability distributions, binomial distribution, Poisson distribution, exponential distribution, normal distribution, joint and conditional probability.

Section 2: Networks, Signals and Systems

Circuit analysis: Node and mesh analysis, superposition, Thevenin's theorem, Norton's theorem, reciprocity. Sinusoidal steady state analysis: phasors, complex power, maximum power transfer. Time and frequency domain analysis of linear circuits: RL, RC and RLC circuits, solution of network equations using Laplace transform.

Linear 2-port network parameters, wye-delta transformation.

Continuous-time signals: Fourier series and Fourier transform, sampling theorem and applications.

Discrete-time signals: DTFT, DFT, z-transform, discrete-time processing of continuous-time signals. LTI systems: definition and properties, causality, stability, impulse response, convolution, poles and zeroes, frequency response, group delay, phase delay.

Section 3: Electronic Devices

Energy bands in intrinsic and extrinsic semiconductors, equilibrium carrier concentration, direct and indirect band-gap semiconductors.

Carrier transport: diffusion current, drift current, mobility and resistivity, generation and recombination of carriers, Poisson and continuity equations.

P-N junction, Zener diode, BJT, MOS capacitor, MOSFET, LED, photo diode and solar cell.

Section 4: Analog Circuits

Diode circuits: clipping, clamping and rectifiers.

BJT and MOSFET amplifiers: biasing, ac coupling, small signal analysis, frequency response.

Current mirrors and differential amplifiers.

Op-amp circuits: Amplifiers, summers, differentiators, integrators, active filters, Schmitt triggers and oscillators.

Section 5: Digital Circuits

Number representations: binary, integer and floating-point- numbers. Combinatorial circuits: Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates and their static CMOS implementations, arithmetic circuits, code converters, multiplexers, decoders.

Sequential circuits: latches and flip-flops, counters, shift-registers, finite state machines, propagation delay, setup and hold time, critical path delay.

Data converters: sample and hold circuits, ADCs and DACs.

Semiconductor memories: ROM, SRAM, DRAM.

Computer organization: Machine instructions and addressing modes, ALU, data-path and control unit, instruction pipelining.

Section 6: Control Systems

Basic control system components; Feedback principle; Transfer function; Block diagram representation; Signal flow graph; Transient and steady-state analysis of LTI systems; Frequency response; Routh-Hurwitz and Nyquist stability criteria; Bode and root-locus plots; Lag, lead and lag-lead compensation; State variable model and solution of state equation of LTI systems.

Section 7: Communications

Random processes: autocorrelation and power spectral density, properties of white noise, filtering of random signals through LTI systems.

Analog communications: amplitude modulation and demodulation, angle modulation and demodulation, spectra of AM and FM, superheterodyne receivers.

Information theory: entropy, mutual information and channel capacity theorem.

Digital communications: PCM, DPCM, digital modulation schemes (ASK, PSK, FSK, QAM), bandwidth, inter-symbol interference, MAP, ML detection, matched filter receiver, SNR and BER.

Fundamentals of error correction, Hamming codes, CRC.

Section 8: Electromagnetics

Maxwell's equations: differential and integral forms and their interpretation, boundary conditions, wave equation, Poynting vector.

Plane waves and properties: reflection and refraction, polarization, phase and group velocity, propagation through various media, skin depth.

Transmission lines: equations, characteristic impedance, impedance matching, impedance transformation, S-parameters, Smith chart.

Rectangular and circular waveguides, light propagation in optical fibers, dipole and monopole antennas, linear antenna arrays.

ADMIT CARDS OF GATE REGISTERED ECE STUDENTS 2024-25

S.NO	ROLL NUMBER	NAME	GATE ADMIT CARD NUMBER	ADMIT CARD
1	21U41A0406	DAVALA ARUNA	EC24S76117600	https://drive.google.com/open?id=1K061xi0D88Tp-rXnCx1GnCqVzXIQLKts
2	21U41A0409	E.SAI KARTHIK	EC24S76117251	https://drive.google.com/open?id=1V33rHIh2U6rOyzltoZ3ax1VsJCMwu0V6
3	21U41A0411	G.RUPASRI	EC24S76115467	https://drive.google.com/open?id=1up2D-w9SUigDjvJyxv0NIG0VLLdlqJZ3
4	21U41A0412	J NAGA SAI	EC25S66122314	https://drive.google.com/open?id=1BrJHN5rnwe4fYB-U0E5rJ5SZFILu kimT
5	21U41A0413	JANAPAREDDI JAYALAKSHMI	EC24S76116394	https://drive.google.com/open?id=1xIsq6kELT6gYF-8pLVlp1FIqHx-6C1Oa
6	21U41A0415	K.HIMABINDU	EC24S76118267	https://drive.google.com/open?id=186Twlirjvxzd wio BPn90XIB5kEyihhF
7	21U41A0416	KUSUMA SURISSETTI	EC24S76118044	https://drive.google.com/open?id=1pPI8lxGVXp7

				JTHAqUGZftvR08iwyvqtI
8	21U41A0421	KULA VARDHAN MAJJI	EC24S76117290	https://drive.google.com/open?id=1rpHIKv1-VaZptyjwXwQvrZeVqL1FtC0Y
9	21U41A0422	MALLA ASWANI	EC24S76118370	https://drive.google.com/open?id=131HjUGT7VJ9hq9GbV4xbmTVZnCuDNUzk
10	21U41A0423	M.HARITHA	EC24S76118196	https://drive.google.com/open?id=13pyzNxdy-ldkO1x_YvHrqFYxXxAdngFp
11	21U41A0430	P SRAVAN KUMAR	EC25S66121973	https://drive.google.com/open?id=18rYhpYpA67V2ZtMNX3sGuXEWySmi_vlq
12	21U41A0439	R.GAYATHRI	EC24S76117280	https://drive.google.com/open?id=1TzYvkkn4g1TVrEGIp7CtDyP1pedWCI7g
13	21U41A0440	Y NAVYA	EC25S66125303	https://drive.google.com/open?id=11A9J6TdYgYeJx0jvFeIwVdAZiQ4ARcK4
14	21U41A0442	U. V. SATHVIK	EC24S76116478	https://drive.google.com/open?id=1DYlgDHig72dMVjLUAmN0bIi4J7400aS
15	21U41A0444	K KAVYA	EC25S66124158	https://drive.google.com/open?id=1-yYjIhNg4YgKFlrzZXWuB7sYjN xu7pZ4
16	21U41A0448	BHEESETTY.R UCHITHA	EC24S76115060	https://drive.google.com/open?id=1ROwYQ-fezIFi0Vyf_ToN0j_Nuz8tho2y

17	21U41A0452	DADI.BABITH A	EC24S76117720	https://drive.google.com/open?id=1mIOyUQiX-G0gGdtLzocZMAFEZYbidpr4
18	21U41A0454	BHARGAVI.D URGA	EC24S76115080	https://drive.google.com/open?id=1BuB0wX0VuUlpSjv42uWVLR5s6MOIj5h8
19	21U41A0459	KUDUPUDI JYOTHI SINDHURA	EC24S76116393	https://drive.google.com/open?id=1wbMhdi06hDPa75ZzWEyVYCMpjkIFme9y
20	21U41A0460	K TEJASWINI	EC24S76115248	https://drive.google.com/open?id=11Fo91NC7EhxCBEB-aCAEMDYjjcq33gcl
21	21U41A0462	M BHAGYA SRI VENKATA LAKSHMI	EC25S66125218	https://drive.google.com/open?id=1SLaE40shMrFUIbYY5q2Ht8q3Q-J9Gw3c
22	21U41A0465	CHANDRA SEKHAR	EC25S66104508	https://drive.google.com/open?id=1L_Mp9AxsB e6w7khCX_OzLjDT9lfqEiL
23	21U41A0470	P VENKATA SATYA	EC24S76117201	https://drive.google.com/open?id=18b0imhda0jCfZeTJnfQ_dwSgsqFp-GCV
24	21U41A0471	P. AKANKSHA	EC25S66122313	https://drive.google.com/open?id=1JAV1CuUzthhAqNtjvGA6x6AzBk0bLv3E
25	21U41A0474	RONGALI CHARMILA	EC24S76115115	https://drive.google.com/open?id=1YEQDtF1gOrHnrfJdujU-RjWeOMHHy965
26	21U41A0477	S. UDAY KIRAN	T136N96	https://drive.google.com/open?id=

				=15DgZBsofB-AmH035vXUjv6l2uKbxgDLj
27	21U41A0478	A AAKANKSHA	EC25S66125668	https://drive.google.com/open?id=1qfjmFCwaPkT0WhiNltKPL5hkYpYzYb7G
28	21U41A0481	T HARI PRIYA	EC25S66120486	https://drive.google.com/open?id=1ByMq6MUoKrm8i5KOJHfoIbKxBK5z_mYK
29	21U41A0482	V LAXMI VENKATA DURGA	EC25S66125468	https://drive.google.com/open?id=1vxDaOHh_1j2F2quAcdxPxo8H3O1hww4h
30	22U45A0401	ADARI SIVANAYAKI	EC24S76118167	https://drive.google.com/open?id=10lpE_DHBi0lDwCl0lvbdTDkGyN7BbrBm
31	22U45A0407	D.VASANTHA	EC25S66120442	https://drive.google.com/open?id=1CUGRMqfR0KmsNQ8Ik-wYef7arPgWc-s5
32	22U45A0408	GAVARA LIKHITHA	EC24S76118510	https://drive.google.com/open?id=1FfZAT15oKJ83rR8WhzN7PxenG_3YWbwi
33	22U45A0413	LAKKOJU DHANUSHYA	T124U32	https://drive.google.com/open?id=1Wg1-REN6wtgp6lkaCBPP7YcgKrHpNwfQ
34	22U45A0413	LAKKOJU DHANUSHYA	EC24S76115482	https://drive.google.com/open?id=1H9rxAKRegg1AuSO0TSv_lfUMV7dmyxsJ
35	22U45A0414	MALLEMPUDI LEELA SARANYA	EC24S76103102	https://drive.google.com/open?id=1H1JMGw0sA

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36	22U45A0423	VIYYAPU VAISHNAVI	EC24S76117532	https://drive.google.com/open?id=1q1cHGy9Fy3cQVzk5ujXocCQoZNIa9dqj

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ANAKAPALLE, VISAKHAPATNAM



B.Tech.

STUDENT ACADEMIC REGISTER

ACADEMIC YEAR 2024-25

REGULATIONS R20

NAME OF THE FACULTY _____

BRANCH ECE YEAR IV SEM. II

SUBJECT GATE - 2024

DURATION FROM 30/10/2024 TO 05/02/2025

Sl. No.	Roll No.	Name of the Student	DVM							
			1	2	3	4	5	6	7	8
1	21041A0403	B. Hiranmayee	4	8	12	16	20	24	28	32
2	21041A0406	D. Anura	4	8	A	12	16	20	24	28
3	21041A0410	G. Satish	4	8	12	16	20	24	28	32
4	21041A0411	G. Rupasri	4	8	12	16	A	20	24	28
5	21041A0413	J. Jayalakshmi	4	8	12	16	20	24	28	32
6	21041A0414	J. V. S. Srinivas	4	8	12	16	20	24	28	32
7	21041A0415	S. Kusuma	4	8	12	16	20	24	28	32
8	21041A0416	K. Hima Bindu	A	4	8	12	16	20	24	28
9	21041A0421	M. Kulavardhan	4	8	12	16	20	24	28	32
10	21041A0422	M. Anwari	4	8	12	16	20	24	28	32
11	21041A0423	M. Haritha	4	8	12	16	20	24	28	32
12	21041A0425	M. Chandu	4	8	12	16	20	24	28	32
13	21041A0428	N. Vaashini	4	8	12	16	20	24	28	32
14	21041A0430	P. Srevar Kumar	4	8	12	16	20	24	28	32
15	21041A0431	P. Hemant Kumar	4	8	12	16	20	24	28	32
16	21041A0433	P. Kumar	4	8	12	16	20	24	28	32
17	21041A0434	P. Divya Prasad	4	8	A	12	16	20	24	28
18	21041A0438	R. Kusuma	4	8	12	16	20	24	28	32
19	21041A0440	Y. Navya	4	8	12	16	20	A	28	32
20	21041A0442	V. V. S. Sathvik	4	8	12	16	20	24	28	32
21	21041A0444	K. Kavya	4	8	12	16	20	24	28	32
22	21041A0448	E. Ruchitha	4	8	A	12	16	20	28	32
23	21041A0449	B. Uma Malleswari	4	8	12	16	20	24	28	A
24	21041A0454	D. Bhargavi	4	8	12	16	20	24	28	32
25	21041A0459	K. Tyathri Sindhu	4	8	12	16	20	24	28	32
26	21041A0462	M. V. Venkata Lakshmi	4	8	12	16	A	20	28	32
27	21041A0463	M. Harikrishna	4	8	12	16	20	24	28	32
28	21041A0466	M. Revathi	4	8	12	16	20	24	28	32
29	21041A0468	N. Poojitha	4	8	12	16	20	24	28	32
30	21041A0470	P. Venkata Satya	4	8	12	A	16	20	28	32
31	21041A0471	P. Anankha	4	8	12	16	20	24	28	32
32	21041A0474	R. Charamila	4	8	12	16	20	24	28	32

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Sl. No.	DVM																						
	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23								
3	36	40	44	48	52	56	60	64	68	72	76	80	84	A	88								
6	32	36	40	44	48	52	56	60	64	A	68	72	76	80	84								
10	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
11	32	36	40	44	48	52	56	60	64	68	72	76	80	84	88								
13	36	40	44	A	48	52	56	60	64	68	72	76	80	84	88								
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15	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
16	32	36	40	A	48	52	56	60	64	68	72	76	80	84	88								
21	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
22	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
23	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
25	36	40	44	48	52	56	60	64	A	68	72	76	80	84	88								
28	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
30	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
31	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
33	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
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38	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
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44	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
48	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
49	32	36	40	44	48	52	56	60	64	68	72	76	80	84	88								
54	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
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62	36	40	44	48	52	56	60	64	68	72	76	80	84	88	92								
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70	36	40	44	48	52	56	A	60	64	68	72	76	80	84	88								
11	36	40	44	48	52	56	60	64	68	72	76	80	84	A	88								
14	36	40	44	48	52	56	60	64	68	72	76	80	84	A	88								

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Award Mark



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Sl. No.	Roll No.	Name of the Student	DVM							
			2010	2110	3110	4110	5110	6110	7110	2011
33	21041A0478	A. Aakanksha	4	8	A	12	16	20	24	28
34	21041A0484	B. Jothiha	A	4	8	12	16	20	24	28
35	22045A0402	A. Sandhya	4	8	12	16	20	24	28	A
36	22045A0403	B. Chaitanya	4	A	8	12	16	20	24	28
37	22045A0405	B. Swetha	4	8	12	16	20	A	24	28
38	22045A0406	C. Lakshmi Amulya	4	8	12	16	20	24	28	32
39	22045A0407	D. Vasantha	4	8	12	A	16	20	24	28
40	22045A0408	G. Lakitha	4	8	12	16	20	24	28	A
41	22045A0410	G. Kasi	4	8	12	16	20	24	28	32
42	22045A0411	J. Himassri	4	8	12	16	20	24	A	28
43	22045A0412	K. Sudha Rani	4	8	12	16	20	24	28	32
44	22045A0413	L. Dhanushya	4	8	12	16	20	24	28	32
45	22045A0414	M. Saranya	4	8	12	16	20	24	28	32
46	22045A0415	M. Anitha	4	8	12	16	20	24	28	32
47	22045A0416	P. Hari Krishna	4	8	12	16	20	24	28	32
48	22045A0419	S. Manikanta	4	8	12	16	20	24	28	32
49	22045A0422	V. Sujana	4	8	12	16	20	24	28	32
50	22045A0423	V. Vaishnavi	4	8	12	16	20	24	28	32

	26/11	27/11	30/11	4/12	10/12	11/12	17/12	18/12	24/12	31/12	7/01	8/01	21/01	22/01	28/01
9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	
32	36	40	44	48	52	56	60	64	A	68	72	76	80	84	
32	36	40	A	44	48	52	56	60	64	68	72	76	80	84	
32	36	40	44	48	52	56	60	64	68	72	76	80	84	88	
32	36	40	44	48	52	56	60	64	68	72	76	80	84	88	
32	36	40	44	48	A	52	56	60	64	68	72	76	80	84	
32	36	40	44	48	52	56	60	64	A	68	72	76	80	84	
36	40	44	48	52	56	60	64	68	72	76	80	84	88	92	
32	36	40	44	48	52	56	60	64	68	72	76	80	84	88	
32	36	40	44	48	52	56	60	64	68	72	76	80	84	88	
36	40	44	48	52	56	60	64	68	72	76	80	84	88	92	
32	36	40	A	44	48	52	56	60	64	68	72	76	80	84	88
36	40	44	48	52	56	A	60	64	68	72	76	80	84	88	
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36	40	44	48	52	56	60	64	68	72	76	80	84	88	92	
36	40	44	48	52	56	60	64	A	68	72	76	80	84	88	
A	56	40	44	48	52	56	60	64	68	72	76	80	84	88	

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Sl. No.	Date	No. of Periods engaged	Topic Covered	REMARKS
1.	30/10/24	DE	Binary and integer floating points	
		SS	Number systems :-	
		EDC	Node and mesh Analysis	
		CS	Energy band in intrinsic & extrinsic	
		CS	Basic Control System Components	
2.	31/10/24	CS	Feedback principle	
		AE	Diode Circuit :- Clipping	
		EMT	Differential and rectangular waveforms	
		COS	Random forest - autocorrelation and Power spectral density	
3.	25/11/24	DE	combinational circuits: Boolean Algebra	
		SS	Superposition	
		EDC	Equilibrium carrier concentration	
		CS	Transformation	
4.	26/11/24	CS	Block diagram representation	
		AE	clamping and rectifiers	
		EMT	boundary conditions	
		COS	Properties of white noise,	
5.	12/11/24	DE	minimization of functions using Boolean identities and Karnaugh map	
		SS	Thvenin's theorem	
		EDC	Direct & indirect band gap semiconductor	
		CS	Signal flow graph	
6.	13/11/24	CS	Transient & steady state analysis of LTI system	
		AE	BJT & MOSFET amplifier :- Biasing	
		EMT	wave equation	
		COS	filtering of random signals through LTI systems	
7.	19/11/24	DE	logic gates and their static CMOS implementation	
		SS	Norton's theorem	
		EDC	Diffusion current	
		CS	Frequency response	
8.	20/11/24	CS	Routh - Hurwitz and Nyquist stability criteria	
		AE	AC Coupling	
		EMT	Fourier Vectors	
		COS	Analogue communication - Amplitude Modulation and demodulation	
9.	26/11/24	DE	Arithmetic Circuits	
		SS	super-capacity	
		EDC	Drift Current	

Sl. No.	Date	No. of Periods engaged	Topic Covered	REMARKS
		CS	Bode and root-locus plots	
10.	27/11/24	CS	lag, lead and lag-lead compensation	
		AE	Small signal Analytic	
		EMT	Reflection and refraction	
		COS	Angle modulation and demodulation	
11.	03/12/24	DE	code converts, multiplexers,	
		SS	essential study analysis	
		EDC	Mobility and resistivity	
		CS	State variable model & solution of state equation	
12.	4/12/24	CS	System modelling	
		AE	frequency Response	
		EMT	Polarization	
		COS	Bandwidth Spectra of AM and FM	
13.	10/12/24	DE	Decoders	
		SS	Phasers	
		EDC	General & recombination of carrier	
		CS	stability analysis	
14.	11/12/24	CS	Controller design	
		AE	Current mirrors & differential amplifier	
		EMT	Phase and group velocity	
		COS	Super heterodyne Receivers	
15.	17/12/24	DE	latches and FlipFlops	
		SS	complex power	
		EDC	Poisson and Continuity Equations	
		CS	Time response analysis	
16.	18/12/24	CS	frequency response analysis	
		AE	Op-Amp Circuits :- Amplifiers	
		EMT	Propagation through various media	
		COS	Information theory :- Entropy	
17.	24/12/24	DE	Shift registers	
		SS	maximum power transfer	
		EDC	P.N junction	
		CS	State-space representation	
18.	31/12/24	CS	Digital control systems	
		AE	Summers	

Sl. No.	Date	No. of Periods engaged	Topic Covered	REMARKS
		FMT.	Reflection & Refraction	
		COS	Mutual information & channel capacity theory.	
19.	7/01/25	DE	finite state machines	f
		SS	time & frequency domain	Am
		EDC	Zener diode	
		CS	Non linear control system	chr
20	8/01/25	CS	finding controller that optimize performance	
		AE	differentiators	d
		FMT	differential & integral forms & interpaths	
		COS	Digital communication: PCM & DPCM	
21.	21/01/25	DE	propagation delay	f
		SS	RL, RC & RLC circuit	Am
		EDC	BJT	
		CS	Controller that adapt to changing system dynamics	chr
22.	22/01/25	CS	using Fuzzy logic for control decision	
		AE	Integrator	d
		FMT	boundary condition, Smith chart	
		COS	Digital Modulation Schemes: ASK, PSK, FSK, QAM	
23	28/01/25	DE	Setup and hold time	f
		SS	Solution of network equation	Am
		EDC	MOS Capacitor	
		CS	Neural network control	chr
24	29/01/25	CS	system identification	
		AE	Active filters	d
		FMT	wave equation	
		COS	Bandwidth	
25	04/02/25	DE	Critical path delay, sample and hold also	f
		SS	Laplace transform	Am
		EDC	MOSFET	
		CS	Model predictive control (MPC)	chr
26.	05/02/25	CS	Designing controllers for system with uncertainty	
		AE	Schmitt triggers & Oscillators.	d
		FMT	Pointing Vectors	Am
		COS	Intersymbol Interference.	

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