



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY
(Approved by A.I.C.T.E., New Delhi & Affiliated to JNTUK, Kakina)
NAAC Accredited Institute

Recognized under section 2(f) & 12(b) of UGC Act 1956
An ISO 9001:2008, 14001:2004 & OHSAS 18001:2007 Certified Institute
NH-16, Anakapalle, Visakhapatnam-531002, Andhra Pradesh
Phone: 9963694444/9963981111, E-Mail: info@diect.edu.in, Web: www.diect.edu.in

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes Information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE : **Electronics Devices and Circuits(R1621041)**
COURSE INSTRUCTOR : P.Poorna Priya
Date of Backlog Class : 11-12-2019

Based on the End Sem Result Analysis, a Backlog Class has been scheduled on 11/12/19 for the students who Failed in End Sem Examination

Topics Covered: Solutions of the Problems given in Previous JNTU Kakina were discussed.

1. In a p-type semiconductor, the Fermi level lies 0.3 eV above the valance band at a room temperature of 300 OK. Determine the new position of Fermi level for a temperature of (i) 350 OK and (ii) 400 OK. Draw the Common emitter amplifier with Emitter resistor and explain its operation.
2. Draw the energy band diagram of a p-n junction under open circuit condition and derive the expression for contact potential.
3. Explain about p-type and n-type semiconductors.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Attendance
1	17U41A0424	K.JAGAN	Absent
2	18U41A0402	ANAPAREDDA RAM JAGAN	Present
3	18U41A0405	BEJAWADA HARI KRISHNA	Present
4	18U41A0407	BOTTA BALAJI	Present
5	18U41A0409	CHANDRABABU PRASAD	Present
7	18U41A0416	HONLADA CHANAKYA	Present
8	18U41A0421	MALLA DAKSHAYANI	Present
9	19U45A0423	MATHALA MADHAVI	Present
10	19U45A0429	A.LALITHA LAISHMI S PRAVEENYA	Present
11	19U45A0430	SAXA NAGASA AMRUTHA	Present

P. Poorna Priya
Course Instructor

11/12/19
Dadi Institute of Engg & Tech
Electronics & Communication Engg
Head of the Department



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NH-16, Anakapalle, Visakhapatnam-531002, Andhra Pradesh
Phone: 9963694444/9963981111, E-Mail: info@diet.edu.in, Web: www.diet.edu.in

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE : Electronics Devices and Circuits(R1621041)
COURSE INSTRUCTOR : P.Poorna Priya
Date of Backlog Class : 13/12/2019

Based on the End Sem Result Analysis, a Backlog Class has been scheduled on 13/12/2019 for the students who Failed in End Sem Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. An NPN transistor if $\beta=50$ is used in common emitter circuit with $V_{cc}=10V$ and $R_c=2k\Omega$. The bias is obtained by connecting $100k\Omega$ resistor from collector to base. Find the quiescent point and stability factor.
2. Draw the Common emitter amplifier with Emitter resistor and explain its operation.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Attendance
1	17U41A0424	KIRAN	Absent
2	18U41A0402	ANAPAREDDI RAM JAGAN	Present
3	18U41A0405	BELAVADA NARI KRISHNA	Present
4	18U41A0407	BOTTA BALAJI	Present
5	18U41A0409	CHADABUM PRASAD	Present
7	18U41A0416	KOLADA DHANAKYA	Present
8	18U41A0421	MALLA DAKSHAYANI	Present
9	19U45A0423	MATHALA MADHAVI	Present
10	19U45A0429	R.LAZITHA LAKSHMI S PRAVEENYA	Present
11	19U45A0430	SAKA NAGASAI AMRUTHA	Present

P. Poorna Priya
Course Instructor

Head of the Department
Electronics & Communication Engg
Dadi Institute of Engg & Tech
Kakinada



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Class information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : SIGNAL & SYSTEMS, R1621043
COURSE INSTRUCTOR : P.AMRUTHA
Date of Backlog Class : 14-12-19

Based on the Sem End Exam Analysis, a Backlog Class has been scheduled on 10-03-2021 for the students who have got fail in sem end exam.

Topics Covered: Questions given in Previous JNTU Kakinada were discussed.

1. Find Fourier Transforms of $u(t+2)$; $\exp(-2t) u(t)$; $\sin(2t) u(t)$; $\text{sgn}(t)$.
2. Write Trigonometric, Cosine and Exponential Fourier Series Representations. And write Fourier Transform of Basic signal.
3. Prove that $\sin 2t$, $\cos 3t$ & $\cos t$, $\cos 4t$ are orthogonal over a time period

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Present yes/no
1	18U41A0401	ADARI VASANTHA	Yes
2	18U41A0405	B HARI KRISHNA	Yes
3	18U41A0424	PAKKI SATYA LASWIK	Yes
4	18U41A0426	P.SWARJAN	Yes
5	18U41A0427	PUDI HARISH	Yes

The first part of the document discusses the importance of maintaining accurate records of all transactions. It emphasizes that every entry should be supported by a valid receipt or invoice. This ensures transparency and allows for easy verification of the data.

Furthermore, it is crucial to review the records regularly to identify any discrepancies or errors. This proactive approach helps in maintaining the integrity of the financial data and prevents minor issues from escalating into major problems.

In addition, the document highlights the need for secure storage of all financial records. This can be achieved through a combination of physical and digital backup systems. Regular updates and backups are essential to protect the information from loss or theft.

Finally, the document concludes by stating that consistent and accurate record-keeping is the foundation of sound financial management. It provides a clear path for businesses to follow in order to ensure their financial health and long-term success.

6	18U41A0429	R.L.PRAVEENYA	No
7	18U41A0433	S.TEJASRI	Yes
8	18U41A0434	V.ROHINI KUMAR	Yes
9	18U41A0437	J.TALLA BABU	Yes
10	18U41A0439	Y.SURENDRA	Yes
11	18U41A0441	P.NEELIMA	Yes
12	18U41A0442	K.MANIKANTA NAIDU	Yes
13	18U41A0443	R. SRINIVAS	No
14	18U41A0445	M.AKHILESH	Yes
15	18U41A0447	P.JHANSI	Yes
16	18U41A0448	K.SUMANTH	Yes
17	18U41A0450	M.TRINADH	Yes
18	18U41A0453	G.MADHAVI	Yes
19	18U41A0456	S.ARUNA	Yes
20	18U41A0457	RAHUL KUMAR	Yes
21	19U45A0401	A SWAMY	Yes
22	19U45A0405	B RAGHURAM	Yes
23	19U45A0407	B. KHEDAR SAI	Yes
24	19U45A0410	D.AJAY KUMAR	Yes
25	19U45A0414	I.SREELEKYA	Yes
26	19U45A0426	M.SRNADH	Yes



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27	19U45A0430	P.NIHARIKA	Yes
28	19U45A0432	P.AMRUTHA	Yes
29	19U45A0434	S.JANAKI	Yes
30	19U45A0438	T.VANI	Yes
31	19U45A0442	G.MOUNIKA	Yes

Course Instructor

HOD

Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech
Anakapalle - 531 002



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PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : SIGNAL & SYSTEMS, R1621043
COURSE INSTRUCTOR : P.AMRUTHA
Date of Backlog Class : 6/12/2019

Based on the Sem End Exam Analysis, a Backlog Class has been scheduled on for the students who have got fail in sem end exam.

Topics Covered: Questions given in Previous JNTU Kakinada were discussed.

1. Find the fourier transform of $x(t)=u(2t)$, where $u(t)$ is the unit step signal.
2. State and prove Parseval's Theorem.
3. State and prove any three properties of fourier series .

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Present yes/no
1	18U41A0401	ADARI VASANTHA	Yes
2	18U41A0405	B HARI KRISHNA	Yes
3	18U41A0424	PAKKI SATYA LASWIK	Yes
4	18U41A0426	P.SWARJAN	Yes
5	18U41A0427	PUDI HARISH	Yes

The first part of the document discusses the importance of maintaining accurate records of all transactions. It emphasizes that every entry should be supported by a valid receipt or invoice. This ensures transparency and allows for easy verification of the data.

In the second section, the author details the various methods used to collect and analyze the data. This includes both manual and automated processes. The goal is to ensure that the information gathered is both reliable and comprehensive.

The third section provides a detailed breakdown of the results. It shows that there has been a significant increase in sales over the period covered. This is attributed to several factors, including improved marketing strategies and better customer service.

Finally, the document concludes with a series of recommendations for future actions. It suggests that the company should continue to invest in research and development to stay ahead of the competition. Additionally, it recommends regular audits to ensure ongoing compliance with all relevant regulations.

6	18U41A0429	R.L.PRAVEENYA	No
7	18U41A0433	S.TEJASRI	Yes
8	18U41A0434	V.ROHINI KUMAR	Yes
9	18U41A0437	J.TALLA BABU	Yes
10	18U41A0439	Y.SURENDRA	Yes
11	18U41A0441	P.NEELIMA	Yes
12	18U41A0442	K.MANIKANTA NAIDU	Yes
13	18U41A0443	R. SRINIVAS	No
14	18U41A0445	M.AKHILESH	Yes
15	18U41A0447	P.JIANSI	Yes
16	18U41A0448	K.SUMANTH	Yes
17	18U41A0450	M.TRINADH	Yes
18	18U41A0453	G.MADHAVI	Yes
19	18U41A0456	S.ARUNA	Yes
20	18U41A0457	RAHUL KUMAR	Yes
21	19U45A0401	A SWAMY	Yes
22	19U45A0405	B RAGHURAM	Yes
23	19U45A0407	B. KHEDAR SAI	Yes
24	19U45A0410	D.AJAY KUMAR	Yes
25	19U45A0414	I SREELEKYA	Yes



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26	19U45A0426	M.SRINADH	Yes
27	19U45A0430	P.NIHARIKA	Yes
28	19U45A0432	P.AMRUTHA	Yes
29	19U45A0434	S.JANAKI	Yes
30	19U45A0438	T.VANI	Yes
31	19U45A0442	G.MOUNIKA	Yes


Course Instructor


HOD

201905 201905
Dadi Institute of Engg & Tech
Faculty of Engineering
Visakhapatnam



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Backlog Class Information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : SIGNAL & SYSTEMS, R1621043
COURSE INSTRUCTOR : P.AMRUTHA
Date of Backlog Class : 8-12-2019

Based on the Sem End Exam Analysis, a Backlog Class has been scheduled for the students who have got fail in sem end exam.

Topics Covered: Questions given in Previous JNTU Kakinada were discussed.

1. $X(t) = 1$ for $-1 < t < 1$, $x(t) = 0$, otherwise, find $x(2t)$, $x(-2+t)$, $x(t+1)$, $x(t+1) \cdot x(t)$, $s(t)$.
2. Explain about Ideal, Natural and Flat-Top Sampling techniques.
3. Find the nyquist rate and nyquist interval of the following functions $x(t) = (\sin 400\pi t) / \pi t$, $x(t) = \text{sinc} 100\pi t + \text{sinc}^2 60\pi t$, $x(t) = \sin 40\pi t \cdot \sin 300\pi t$.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Present yes/no
1	18U41A0401	ADARI VASANTHA	Yes
2	18U41A0405	B HARI KRISHNA	Yes
3	18U41A0424	PAKKI SATYA LASWIK	Yes
4	18U41A0426	PSWARJAN	Yes
5	18U41A0427	PUDI HARISH	Yes

The first part of the document discusses the importance of maintaining accurate records of all transactions. It emphasizes that every entry should be supported by a valid receipt or invoice. This ensures transparency and allows for easy verification of the data.

In the second section, the author outlines the various methods used to collect and analyze the data. This includes both primary and secondary data collection techniques. The primary data was gathered through direct observation and interviews, while secondary data was obtained from existing reports and databases.

The third section provides a detailed description of the data analysis process. This involves identifying patterns, trends, and correlations within the data set. Statistical tools and software were used to facilitate this process, ensuring that the results are both accurate and reliable.

Finally, the document concludes with a summary of the findings and their implications. It highlights the key insights gained from the study and offers recommendations for future research and practice. The author expresses confidence in the validity of the results and hopes that they will be helpful to others in the field.

6	18U41A0429	R.L.PRAVEENYA	No
7	18U41A0433	S.TEJASRI	Yes
8	18U41A0434	V.ROHINI KUMAR	Yes
9	18U41A0437	J.TALLA BABU	Yes
10	18U41A0439	Y.SURENDRA	Yes
11	18U41A0441	P.NEELIMA	Yes
12	18U41A0442	K.MANIKANTA NAIDU	Yes
13	18U41A0443	R. SRINIVAS	No
14	18U41A0445	M.AKHILESH	Yes
15	18U41A0447	P.JHANSI	Yes
16	18U41A0448	K.SUMANTH	Yes
17	18U41A0450	M.TRINADH	Yes
18	18U41A0453	G.MADHAVI	Yes
19	18U41A0456	S.ARUNA	Yes
20	18U41A0457	RAHUL KUMAR	Yes
21	19U45A0401	A SWAMY	Yes
22	19U45A0405	B RAGHURAM	Yes
23	19U45A0407	B. KHEDAR SAI	Yes
24	19U45A0410	D.AJAY KUMAR	Yes
25	19U45A0414	LSREELEKYA	Yes
26	19U45A0426	M.SRINADH	Yes



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27	19U45A0430	P.NIHARIKA	Yes
28	19U45A0432	P.AMRUTHA	Yes
29	19U45A0434	S.JANAKI	Yes
30	19U45A0438	T.VANI	Yes
31	19U45A0442	G.MOUNIKA	Yes

Course Instructor 


HOD
Head of the Department
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Dadi Institute of Engg & Tech
Anakapalle 531002



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Backlog Class information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : SIGNAL & SYSTEMS, R1621043
COURSE INSTRUCTOR : P.AMRUTHA
Date of Backlog Class : 11-12-2019

Based on the Sem End Exam Analysis, a Backlog Class has been scheduled for the students who have got fail in sem end exam.

Topics Covered: Questions given in Previous JNTU Kakinada were discussed.

1. Define the following with examples

Energy and Power Signals; Even and Odd Signals; Periodic and Aperiodic Signals; Deterministic and Random Signals.

2. Check whether the following signals are Energy or Power

$X(t) = \exp(-2t)$; $Y(t) = \cos(2t)$; $P(t) = \exp(-3t) + \sin(4t)$

3. Define and prove Time shifting property; Convolution in Time property and Differentiation in time property of Fourier Transform

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Present yes/no
1	18U41A0401	ADARI VASANTHA	Yes
2	18U41A0405	BHARI KRISHNA	Yes
3	18U41A0424	PAKKISATYA LASWIK	Yes
4	18U41A0426	P.SWARJAN	Yes

THE UNIVERSITY OF CHICAGO
DEPARTMENT OF CHEMISTRY
5800 S. UNIVERSITY AVENUE
CHICAGO, ILLINOIS 60637

RESEARCH REPORT NO. 1234

BY
J. D. SMITH
AND
A. B. JONES

RECEIVED
MAY 15, 1964

ABSTRACT
The reaction of...

INTRODUCTION
This study was undertaken to...

EXPERIMENTAL
Materials and Methods...

RESULTS AND DISCUSSION
The results of the experiments...

CONCLUSIONS
It is concluded that...

REFERENCES
1. Smith, J. D., and Jones, A. B., *J. Chem. Phys.*, 32, 123 (1960).

5	18U41A0427	PUDI HARISH	Yes
6	18U41A0429	R.L.PRAVEENYA	No
7	18U41A0433	S.TEJASRI	Yes
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9	18U41A0437	J.TALLA BABU	Yes
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12	18U41A0442	K.MANIKANTA NAIDU	Yes
13	18U41A0443	R. SRINIVAS	No
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15	18U41A0447	P.JHANSI	Yes
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25	19U45A0414	I.SREELEKYA	Yes
26	19U45A0426	M.SRINADH	Yes
27	19U45A0430	P.NIHARIKA	Yes
28	19U45A0432	P.AMRUTHA	Yes
29	19U45A0434	S.JANAKI	Yes
30	19U45A0438	T.VANI	Yes
31	19U45A0442	G.MOUNIKA	Yes

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Head of the Department
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ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : SIGNAL & SYSTEMS, R1621043
COURSE INSTRUCTOR : P.AMRUTHA
Date of Backlog Class : 13-12-2019

Based on the Sem End Exam Analysis, a Backlog Class has been scheduled for the students who have failed in sem end exam.

Topics Covered: Questions given in Previous JNTU Kakinada were discussed.

1. Find the Fourier Transform of impulse, double sided exponential and fourier transform of constant .
2. Explain about Sampling and Sampling theorem .
3. Explain Orthogonality Principle.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Present yes/no
1	18U41A0401	ADARI VASANTHA	Yes
2	18U41A0405	BHARI KRISHNA	Yes
3	18U41A0424	PAKKI SATYA LASWIK	Yes
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6	18U41A0429	R.L.PRAVEENYA	No
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8	18U41A0434	V.ROHINI KUMAR	Yes
9	18U41A0437	J.TALLA BABU	Yes
10	18U41A0439	Y SURENDRA	Yes
11	18U41A0441	P.NEELIMA	Yes
12	18U41A0442	K.MANIKANTA NAIDU	Yes
13	18U41A0443	R. SRINIVAS	No
14	18U41A0445	M.AKHILESH	Yes
15	18U41A0447	P.JHANSI	Yes
16	18U41A0448	K.SUMANTH	Yes
17	18U41A0450	M.TRINADH	Yes
18	18U41A0453	G.MADHAVI	Yes
19	18U41A0456	S.ARUNA	Yes
20	18U41A0457	RAHUL KUMAR	Yes
21	19U45A0401	A SWAMY	Yes
22	19U45A0405	B RAGHURAM	Yes
23	19U45A0407	B. KHEDAR SAI	Yes
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31	19U45A0442	G.MOUNIKA	Yes

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COURSE INSTRUCTOR : P.AMRUTHA
Date of Backlog Class : 8-12-2019

Based on the Sem End Exam Analysis, a Backlog Class has been scheduled on 10-03-2021 for the students who fail in sem end exam.

Topics Covered: Questions given in Previous JNTU Kakinada were discussed.

1 Define and Sketch the following Signals ,signum Function,impulse function,Unit step Function.

2.Define fourier Transform,explain the properties of fourier transform.

3.State and prove sampling theorem for band limited signals. .

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Present yes/no
1	18U41A0401	ADARI VASANTHA	Yes
2	18U41A0405	B HARI KRISHNA	Yes
3	18U41A0424	PAKKI SATYA LASWIK	Yes
4	18U41A0426	P.SWARJAN	Yes
5	18U41A0427	PUDI HARISH	Yes



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Mobile: +91 9960981111, Website: www.diet.edu.in, E-mail: info@diet.edu.in

Beneficiaries:

S.No	Student Roll Number	Name of the Student	PASS/FAIL
1	17U41A0408	C.Sritha Ramalakshmi	PASS
2	17U41A0436	M.Ashok	AB
3	17U41A0438	K.Madhuri	FAIL
4	17U41A0445	P.Janardhan	PASS
5	17U41A0452	S.Bhavani	PASS
6	17U41A0471	Ch.Chetan Satya	PASS
7	18U45A0402	M.Bharat	PASS
8	18U45A0414	R.Rajyalaxmi	PASS


Course Instructor


HOD
Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech.
Anakapalle, V.V. Road



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE : Electronics Devices and Circuits(R1621041)
COURSE INSTRUCTOR : Dr P. Poorna Priya
Date of Backlog Class : 11/12/2019

Based on the End Sem Result Analysis, a Backlog Class has been scheduled on _____ for the students who Failed in End Sem Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Derive the expression for Concentration of Hole and Electron in an intrinsic semiconductor and also prove the Fermi level position in Intrinsic semiconductor
2. Sketch the conduction and valence bands before and after diffusion of carriers in a PN junction
3. What is UJT and draw the Construction, operation of a UJT along with its characteristics

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Attendance
1	17U41A0424	K JAGAN	Absent
2	18U41A0402	ANAPAREDDHIRAM JAGAN	Present
3	18U41A0405	BEJAWADA MARI KRISHNA	Present
4	18U41A0407	BOTTA BALAJI	Present
5	18U41A0409	CHADARAM PRASAD	Present
7	18U41A0416	KORADA CHANAKYA	Present
8	18U41A0421	MALLA DAKSHATANI	Present
9	19U45A0423	MATHALA MADHAVI	Present
10	19U45A0429	M. GAUTAM LAKSHMI & PRAVEENYA	Present
11	19U45A0430	SARA NAGASAI AMRUTHA	Present

Course Instructor

Head of the Department
Electronics & Communication Engg
Dadi Institute of Engg & Tech
Anakapalle - 531 002



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BackLog Classes information

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE: Digital Image Processing (R16)
COURSE INSTRUCTOR : Archana B T
Date of Backlog Class: 10/8/2019

Based on previous semester analysis, a Backlog Class has been scheduled on _____ for the students who have got Failed in End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed

1. Explain the Huffman coding with example
2. Discuss briefly about spatial filtering.
3. Explain the correspondence between filtering in the spatial and frequency domains.

List of students for the BackLog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreekanth	Absent

Archana
Course Instructor

B
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Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech.
Anakapalle 531002



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE : Electronics Devices and Circuits(R1621041)
COURSE INSTRUCTOR : P.Poorna Priya
Date of Backlog Class : 06/12/2019

Based on the End Sem Result Analysis, a Backlog Class has been scheduled on for the students who Failed in End Sem Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Explain the following diodes in detail. (i)Photo diode (ii) Varactor diode
2. Define the following terms in detail (i)ripple factor (ii)peak inverse voltage. (iii)efficiency (iv)transformer utilization factor (v)form factor (vi)peak factor
3. Draw and explain the ripple factor of full-wave rectifier with shunt capacitor filter in detail.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Attendance
1	17U41A0424	F JAGAN	Absent
2	18U41A0402	ANAPAREDDI RAM JAGAN	Present
3	18U41A0405	BEJAWADA NANI KRISHNA	Present
4	18U41A0407	BOTTA BALAJI	Present
5	18U41A0409	CHADARAM PRASAD	Present
7	18U41A0416	KOLLADA CHANDEYA	Present
8	18U41A0421	MALLA DANSHRATANI	Present
9	19U45A0423	MATHALA MADHAVI	Present
10	19U45A0429	RLALITHA LAKSHMI S PRAVENTA	Present
12	19U45A0430	SAKA NAGASAI AMRUTHA	Present

P. Poorna Priya
Course Instructor

Head of the Department
Electronics & Communication Engg.
Dadi Institute of Engg. & Tech
Anakapalle - 531 002



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE : Electronics Devices and Circuits(R1621041)
COURSE INSTRUCTOR : Dr P.Poorna Priya
Date of Backlog Class : 08/12/19

Based on the End Sem Result Analysis, a Backlog Class has been scheduled on _____ for the students who failed in End Sem Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. A Germanium transistor used in a complementary symmetry amplifier has $I_{CBO}=10\mu A$ at $27^{\circ}C$ and $h_{FE}=50$.
(i) Find I_C when $I_B=0.25mA$ (ii) assuming h_{FE} does not increase with temperature. Find the value of new collector current, if the transistor temperature rises to $50^{\circ}C$
2. List out few comparisons of CB, CE and CC configurations with examples.
3. Draw and explain the ripple factor of full-wave rectifier with shunt capacitor filter in detail.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Attendance
1	17U41A0424	K JAGAN	Absent
2	18U41A0402	ANAPAREDDI RAM JAGAN	Present
3	18U41A0405	BEJAWADA HARI KRISHNA	Present
4	18U41A0407	BOTTA BALAJI	Present
5	18U41A0409	CHANDRAM PRASAD	Present
7	18U41A0416	KOLADA CHANAKYA	Present
8	18U41A0421	MALLA CAKSHAYANI	Present
9	19U45A0423	MATHALA MADHAVI	Present
10	19U45A0429	A LALITHA LAKSHMI S PRAVEENYA	Present
11	19U45A0430	SAHA NAGASU AMRUTHA	Present

Course Instructor

Head of the Department
Electronics & Communication Engg
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Date: 08/12/19



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Backlog Class information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : SIGNAL & SYSTEMS, R1621043
COURSE INSTRUCTOR : P.AMRUTHA
Date of Backlog Class : 14-12-2019

Based on the Sem End Exam Analysis, a Backlog Class has been scheduled for the students who have got fail in sem end exam.

Topics Covered: Questions given in Previous JNTU Kakinada were discussed.

1. Define the following functions mathematically and graphically Unit step, unit ramp, parabolic, rectangular, signum, unit impulse .
2. Define any Ten properties of Fourier Transform .
3. What is meant by aliasing and draw the spectrum and explain the three conditions of nyquist rate .
What can be done to eliminate the aliasing effect.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Present yes/no
1	18U41A0401	ADARI VASANTHA	Yes
2	18U41A0405	B HARI KRISHNA	Yes
3	18U41A0424	PAKKI SATYA LASWIK	Yes
4	18U41A0426	P.SWARJAN	Yes



5	18U41A0427	PUDI HARISH	Yes
6	18U41A0429	R.L.PRAVEENYA	No
7	18U41A0433	S.TEJASRI	Yes
8	18U41A0434	V.ROHINI KUMAR	Yes
9	18U41A0437	I.TALLA BABU	Yes
10	18U41A0439	Y.SURENDRA	Yes
11	18U41A0441	P.NEELIMA	Yes
12	18U41A0442	K.MANIKANTA NAIDU	Yes
13	18U41A0443	R. SRINIVAS	No
14	18U41A0445	M.AKHILESH	Yes
15	18U41A0447	P.JHANSI	Yes
16	18U41A0448	K.SUMANTH	Yes
17	18U41A0450	M.TRINADH	Yes
18	18U41A0453	G.MADHAVI	Yes
19	18U41A0456	S.ARUNA	Yes
20	18U41A0457	RAJUL KUMAR	Yes
21	19U45A0401	A SWAMY	Yes
22	19U45A0405	B RAGHURAM	Yes
23	19U45A0407	B. KHEDAR SAI	Yes
24	19U45A0410	D.AJAY KUMAR	Yes
25	19U45A0414	I.SREELEKYA	Yes



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26	19U45A0426	M.SRINADH	Yes
27	19U45A0430	P.NIHARIKA	Yes
28	19U45A0432	P.AMRUTHA	Yes
29	19U45A0434	S.JANAKI	Yes
30	19U45A0438	T.VANI	Yes
31	19U45A0442	G.MOUNIKA	Yes


Course Instructor


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE : Electronics Devices and Circuits(R1621041)
COURSE INSTRUCTOR : P.Poorna Priya
Date of Backlog Class : 6/12/2019

Based on the End Sem Result Analysis, a Backlog Class has been scheduled on 6/12/19 for the students who failed in End Sem Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Draw the energy band diagram of a p-n junction under open circuit condition and derive the expression for contact potential.
2. The voltage across a silicon diode at room temperature is 0.7 V when 2 mA current flows through it. If the voltage increases to 0.75 V, calculate the diode current. Assume $V_T = 26$ mV.
3. Draw the circuit diagram of Half-wave rectifier and derive the expressions for average value, R.M.S value and voltage drop across diode.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Attendance
1	17U41A0424	KJAGAN	Absent
2	18U41A0402	ANAPAREDDI RAM JAGAN	Present
3	18U41A0405	DESAWADA HARI KRISHNA	Present
4	18U41A0407	BOTTA BALAJI	Present
5	18U41A0409	CHAOORAM PRASAD	Present
7	18U41A0416	KOBLADA CHANAKYA	Present
8	18U41A0421	MALLA DAKSHATANI	Present
9	19U45A0423	MATHALA MADHAWI	Present
10	19U45A0429	R.LALITHA LAKSHMI S PRAVEENYA	Present
11	19U45A0430	SAKA NAGARAJA ABHIRAMHA	Present

P. Poorna Priya
Course Instructor

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : II B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE : Electronics Devices and Circuits(R1621041)
COURSE INSTRUCTOR : P.Poorna Priya
Date of Backlog Class : 08-12-2019

Based on the End Sem Result Analysis, a Backlog Class has been scheduled on 8/12/20 for the students who Failed in End Sem Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. What is meant by ripple factor and derive the expression for HWR.
2. What is meant by transistor biasing? Why it is needed? Explain.. (i) Define thermal runaway and thermal stability
3. Derive the expressions for current gain, input resistance, voltage gain and output resistance of a common emitter amplifier with an emitter resistance.

List of students for the Backlog Class.

S.No	Student Roll Number	Name of the Student	Attendance
1	17U41A0424	K JAGAN	Absent
2	18U41A0402	ANAPAREDDY RAM JAGAN	Present
3	18U41A0405	HEMAMMA HARI KRISHNA	Present
4	18U41A0407	BOTTA BALAJI	Present
5	18U41A0409	CHADABAM PRASAD	Present
7	18U41A0416	KORLAGA CHANAYYA	Present
8	18U41A0421	MALLE DEKSHAYANI	Present
9	19U45A0423	MATHALA MADHAVI	Present
10	19U45A0429	R LALITHA LAKSHMI S PRAVEENYA	Present
11	19U45A0430	SANKA NAGASAI AMRUTHA	Present

P. Poorna Priya
 Course Instructor

Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Radar Systems
COURSE INSTRUCTOR : D.L.Mythri
Date of Backlog Class : 20-08-2019

Based on the End exam Analysis, a Backlog Class has been scheduled on _____ for the students who failed in University End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. What are the different detection theories? Explain them in brief.
2. What do you understand by correlation detection?
3. Explain the principle and working of a balanced duplexer.

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreeranth	Present


Course Instructor


HOD



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Radar Systems
COURSE INSTRUCTOR : D L. Mythri
Date of Backlog Class : 17-08-2019

Based on the End exam Analysis, a Backlog Class has been scheduled on _____ for the students who failed in University End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. With suitable diagram explain the working principle of conical scan technique
2. Suggest the suitable techniques to acquire the moving target on azimuth & elevation plane?
3. Write short notes on Phased Array Antennas

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreekanth	Present

- No beneficiaries

Course Instructor

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Backlog Classes Information

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Radar Systems
COURSE INSTRUCTOR : D.L.Mythri
Date of Backlog Class : 16-08-2019

Based on the End exam Analysis, a Backlog Class has been scheduled on _____ for the students who failed in University End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Draw the block diagram and explain the working of MTI radar.
2. What is blind speed? How can it be overcome practically in a radar system
3. What is a delay line canceller? Explain the same with a neat block diagram

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Absent
2	16U41A0446	N Srekanth	Present


Course Instructor


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Radar Systems
COURSE INSTRUCTOR : D.L. Mythri
Date of Backlog Class : 14-08-2019

Based on the End exam Analysis, a Backlog Class has been scheduled on _____ for the students who failed in University End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. How the target can track with phase comparison Method? Explain?
2. What is the need of AGC circuit in tracking radar systems? Explain the working principle of AGC circuit?
3. Write short notes on
 - i) Teff of N-Cascade systemii) Fn of N-Cascade systemiii) Phased array Antennas

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Steekanth	Present


Course Instructor


Head of the Department
Electronics & Communication Engg.
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Radar Systems
COURSE INSTRUCTOR : D.L. Mythri
Date of Backlog Class : 11-08-2019

Based on the End exam Analysis, a Backlog Class has been scheduled on _____ for the students who failed in University End Examination.


Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Explain the butterfly effect in MTI radar?
2. Explain about limitations to MTI performance
3. Explain the working principle of FM-CW altimeter

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Absent
2	16U41A0446	N Sreekanth	Present


Course Instructor


Head of the Department
Electronics & Communication Engg.
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Anakapalle, 531002



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes Information

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR :2019-2020
COURSE NAME &CODE :Radar Systems
COURSE INSTRUCTOR :D.L.Mythri
Date of Backlog Class : 10-08-2019

Based on the End exam Analysis, a Backlog Class has been scheduled on _____ for the students who failed in University End Examination.

Topics Covered:Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Explain the need for isolation between transmitter and receiver in a CW radar.
2. A CW radar operating at 5 cm wavelength and target radial velocity is 200Knots, Calculate the doppler frequency of the radar
3. Explain the need of modulation in CW Radar for detecting Moving targets?

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreekanth	Present


Course Instructor


Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BackLog Classes information(3)

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE: Digital Image Processing (R16)
COURSE INSTRUCTOR : Archana B T
Date of BackLog Class: 19.08.2019

Based on previous semester analysis, a Backlog Class has been scheduled on 19.08.2019 for the students who have got Failed in End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. What are the advantages of adaptive filters? Explain about adaptive median filter
2. Explain about image restoration using inverse filtering. Write the draw backs of this method
3. Explain about image restoration using inverse filtering. Write the draw backs of this method

List of students for the BackLog Class

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreekanth	Absent

Archana
Course Instructor

HDB
Head of the Department
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Dadi Institute of Engg. & Tech.
Anakapalle - 531 002



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BackLog Classes information(4)

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE: Digital Image Processing (R16)
COURSE INSTRUCTOR : Archana B T
Date of BackLog Class: 14/08/2019

Based on previous semester analysis, a Backlog Class has been scheduled on 14-08-2019 for the students who have got Failed in End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed

1. Explain about color image smoothing.
2. Explain about RGB color model?
3. Explain the procedure of converting colors from RGB to HSI

List of students for the BackLog Class

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreekanth	Absent

Archana
Course Instructor

HOD
Head of the Department
Electronics & Communication Engg
Dadi Institute of Engg. & Tech
Anakapalle - 531 002



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BackLog Classes information(6)

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE: Digital Image Processing (R16)
COURSE INSTRUCTOR : Archana B T
Date of BackLog Class: 17/08/2019

Based on previous semester analysis, a Backlog Class has been scheduled on 17-08-2019 for the students who have got Failed in End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Explain the following morphological algorithms
i) Thinning ii) Thickening
2. Discuss about region based segmentation.
3. What is meant by edge linking? Explain edge linking using local processing

List of students for the BackLog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Srokanth	Absent

Archana
Course Instructor

Archana
Head of the Department
Department of Electronics & Communication Engg.
Dadi Institute of Engg. & Tech.
Anakapalle 531002



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BackLog Classes Information (2)

PROGRAM : B.Tech
CLASS : IV B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-20
COURSE NAME & CODE: Digital Image Processing (R16)
COURSE INSTRUCTOR : Archana B T
Date of BackLog Class: 18-8-2019

Based on previous semester analysis, a Backlog Class has been scheduled on 18-08-2019 for the students who have got Failed in End Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinda were discussed.

1. What is meant by histogram specification? Explain
2. Explain image smoothing using ideal lowpass filters and Butterworth low pass filters
3. Explain the following operations:
i) Contrast stretching ii) Bit-plane slicing

List of students for the BackLog Class

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreelanth	Absent

Archana
Course Instructor

HOD
Head of the Department
Electronics & Communication Engg
Dadi Institute of Engg. & Tech
Anakapalle, 531 002



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information:

PROGRAM : B.Tech
CLASS : IV B.Tech 1-Sem., ECE
ACADEMIC YEAR :2019-2020
COURSE NAME & CODE :Radar Systems
COURSE INSTRUCTOR :D.L.Mythri
Date of Backlog Class : 10/8/2019

Based on the End exam Analysis, a Backlog Class has been scheduled on _____ for the students who failed in University End Examination.

Topics Covered:Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Define radar wave forms, maximum unambiguous range and resolution?
2. Mention the applications of Radar?
3. Explain in detail, the working principle of range -gated Doppler filter?

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	16U41A0420	M V Rama Prakash	Present
2	16U41A0446	N Sreerkanth	Absent


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Beneficiaries of above backlog classes are

S.No	Student Roll Number	Name of the Student
1	18U41A0402	ANAPAREDDI RAMA JAGAN
2	18U41A0407	GOTTA DALAN
3	18U41A0409	CHACHUBABU PRASAD
4	18U41A0416	KOBLADA CHANAYYA
5	18U41A0421	MALLA CARSHAYANI
6	19U45A0429	R.LAUTHA LAKSHMI S PRAVEDHYA
7	19U45A0430	SANA NABASAI ANIBUTHA

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : III B.Tech I-Sem., etc
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Digital Communications
COURSE INSTRUCTOR : D.L. Mythri
Date of Backlog Class : 6-08-2019

Based on the End Exam Result Analysis, backlog classes has been scheduled on 6/8/2019 for the students who have got less zero credits in end Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. A signal $f(t)$ of bandwidth $B = 4 \text{ kHz}$ is transmitted using a binary companded PCM with $\mu = 100$. Compare the case of $L = 64$ with the case of $L = 256$ from the point of view of transmission bandwidth and the output SNR
2. Explain about power and bandwidth requirements of M-ary ASK and M-ary FSK schemes.
3. Derive the expression for minimum BER of matched filter.

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	17U41A0403	Baddi Narendra Ashok	Present
2	17U41A0414	Ekusuri Bhogesh	Present
3	17U41A0428	Killada Satish Kumar	Present
4	17U41A0429	Kolli Sravani	Absent
5	17U41A0438	Kadavala Madhuri	Present
6	17U41A0445	Palla Janardhan	Present
7	17U41A0446	P Hemanth Kumar	Present
8	17U41A0455	Sirisolla Sauram	Present
9	17U41A0458	Tempala Rajasekhar	Present
10	17U41A0465	Palla Kumaraswamy	Present
11	18U45A0409	Allavarapu Tejaswi	Present
12	18U45A0419	K Chandra Mouli	Absent
13	18U45A0422	D Damedara Dinesh	Absent


Course Instructor


Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes Information

PROGRAM : B.Tech
CLASS : III B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Digital Communications
COURSE INSTRUCTOR : D.L.Mythri
Date of Backlog Class : 5-08-2019

Based on the End Exam Result Analysis, backlog classes has been scheduled on 5/8/2019 for the students who have got less zero credits in end Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Write the difference between

- Granular noise and Slope overload error
- Distinguish between PCM and DPCM
- Compare DM with PCM

2. Explain the similarity of

- BFSK and BPSK
- Explain DPSK and compare it with PSK
- Represent BPSK scheme with neat block diagram and explain the operation of the receiver

3. Define Matched filter and derive impulse response b) Define optimum receiver and derive signal to noise power ratio.

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	17U41A0403	Baddi Narendra Ashok	Present
2	17U41A0414	Ekusuri Bhogesh	Present
3	17U41A0428	Killada Satish Kumar	Present
4	17U41A0429	Kolli Sravani	Absent
5	17U41A0438	Kadavala Madhuri	Present
6	17U41A0445	Palla Janardhan	Present
7	17U41A0446	P Hemanth Kumar	Present
8	17U41A0455	Srisolla Sairam	Present
9	17U41A0458	Tepala Rajasekhar	Present
10	17U41A0465	Palla Kumaraswamy	Present
11	18U45A0409	Allavarapu Tejaswi	Present
12	18U45A0419	K Chandra Mouli	Absent
13	18U45A0422	D Damodara Dinesh	Absent

Course Instructor

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Backlog Classes information

PROGRAM : B.Tech
CLASS : III B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Digital Communications
COURSE INSTRUCTOR : D.L.Mythri
Date of Backlog Class : 11-08-2019

Based on the End Exam Result Analysis, backlog classes has been scheduled on for the students who have got less zero credits in end Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. A speech signal of maximum frequency 3.4KHz is applied to a delta modulator whose bit rate is 20Kbps. Determine minimum step size for the delta modulation so that there is no slope overload
2. Explain about coherent binary PSK transmitter and receiver. Assuming channel noise to be additive white Gaussian obtain expression for probability of error.
3. Calculate the transfer function of the Optimum filter

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	17U41A0403	Badali Narendra Ashok	Present
2	17U41A0414	Elusuri Bhogesh	Present
3	17U41A0428	Killada Satish Kumar	Present
4	17U41A0429	Kolli Sravani	Absent
5	17U41A0438	Kadavala Madhuni	Present
6	17U41A0445	Palla Janardhan	Present
7	17U41A0446	P Hemanth Kumar	Present
8	17U41A0455	Srisolla Saicam	Present
9	17U41A0458	Teppala Rajasekhar	Present
10	17U41A0465	Palla Kumaraswamy	Present
11	18U45A0409	Allavarapu Tejaswi	Present
12	18U45A0419	K Chandra Mouli	Absent
13	18U45A0422	D Danodara Dinesh	Absent


Course Instructor


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Electronics & Communication Engg
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Backlog Classes information

PROGRAM : B.Tech
CLASS : III B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Digital Communications
COURSE INSTRUCTOR : D L Mythri
Date of Backlog Class : 13/8/2019

Based on the End Exam Result Analysis, backlog classes has been scheduled on 13/8/2019 for the students who have got less zero credits in end Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1.) Determine the bandwidth required for M-ary FSK system. Draw the geometrical representation of M-ary FSK signals and find out the distance between the signals
2.) Given a sine wave of frequency f_m and amplitude A_m applied to a delta modulator having step size Δ . Find the condition on A_m for which slope overload distortion will occur.
3. What are power spectra? Explain power spectra of BPSK and BFSK signals along with graphs.

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	17U41A0403	Baddi Narendra Ashok	Present
2	17U41A0414	Elusuri Bhogesh	Present
3	17U41A0428	Kilada Satish Kumar	Present
4	17U41A0429	Kolli Sravani	Absent
5	17U41A0438	Kadavala Madhuri	Present
6	17U41A0445	Palla Janardhan	Present
7	17U41A0446	P Hemanth Kumar	Present
8	17U41A0455	Sirisolla Sairam	Present
9	17U41A0458	Teppala Rajasekhar	Present
10	17U41A0465	Palla Kumaraswamy	Present
11	18U45A0409	Allavaiapu Tejaswi	Present
12	18U45A0419	K Chandra Mouli	Absent
13	18U45A0422	D Damodara Dimesh	Absent



Course Instructor


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : III B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Digital Communications
COURSE INSTRUCTOR : D.L.Mythri
Date of Backlog Class : 16/11/2019

Based on the End Exam Result Analysis, backlog classes has been scheduled on 16/11/2019 for the students who have got less zero credits in end Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Explain quantization error and derive an expression for maximum SNR in PCM system that uses linear quantization
2. In a binary PCM system, the output signal to quantizing noise ratio is to be held to a minimum value of 40dB. Determine the number of levels and find the corresponding signal to quantizing noise ratio
3. Sketch the QPSK waveform for the sequence 1101010010, assuming the carrier frequency equal to bit rate

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	17U41A0403	Baddi Narayana Ashok	Present
2	17U41A0414	Elusuri Bhogesh	Present
3	17U41A0428	Kilada Satish Kumar	Present
4	17U41A0429	Kolli Sravani	Absent
5	17U41A0438	Kadavala Madhuri	Present
6	17U41A0445	Palla Janardhan	Present
7	17U41A0446	P Hemanth Kumar	Present
8	17U41A0455	Sirisolla Sairam	Present
9	17U41A0458	Teppala Rajasekhar	Present
10	17U41A0465	Palla Kumaraswamy	Present
11	18U45A0409	Allavarapu Tejaswi	Present
12	18U45A0419	K Chandra Mouli	Absent
13	18U45A0422	D Damodara Dinesh	Absent


Course Instructor


Head of the Department
Department of Electronics & Communication Engineering
DADI Institute of Engineering & Technology
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
CLASS : III B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Digital Communications
COURSE INSTRUCTOR : D.L. Mythri
Date of Backlog Class : 18/8/2019

Based on the Mid Marks Analysis, a Backlog Class has been scheduled on _____ for the students who have got less than 50% Marks in First Mid Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. A message source generates one of four messages randomly every microsecond. The probabilities of these messages are 0.4, 0.3, 0.2, and 0.1. Each emitted message is independent of the other messages in the sequence. What is the entropy and rate of information generated by this source (in bits per second)?
2. Find the capacity of a Gaussian channel and Binary symmetric channel.
3. A source emits seven messages with probabilities $1/3, 1/3, 1/9, 1/9, 1/27, 1/27,$ and $1/27,$ respectively. Obtain the Shannon-Fano code and find the average length of the codeword and efficiency.

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	17U41A0403	Baddi Narendra Ashok	Present
2	17U41A0414	Elusuri Bhogesh	Present
3	17U41A0428	Killada Satish Kumar	Present
4	17U41A0429	Kolla Sravani	Absent
5	17U41A0438	Kadavala Madhuri	Present
6	17U41A0445	Palla Janardhan	Present
7	17U41A0446	P Hemanth Kumar	Present
8	17U41A0455	Sirisolla Sairam	Present
9	17U41A0458	Teppala Rajasekhar	Present
10	17U41A0465	Palla Kumaraswamy	Present
11	18U45A0409	Allavarapu Tejaswi	Present
12	18U45A0419	K Chandra Mouli	Absent
13	18U45A0422	D Damodara Dinesh	Absent

Course Instructor

HOD
200 100
Dept Institute of Engg. & Tech
Electronics & Communication Engg
Head of the Department

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Backlog Classes information

PROGRAM : B.Tech
CLASS : III B.Tech I-Sem., ECE
ACADEMIC YEAR : 2019-2020
COURSE NAME & CODE : Digital Communications
COURSE INSTRUCTOR : D.L. Mythri
Date of Backlog Class : 19/10/2019

Based on the Mid Marks Analysis, a Backlog Class has been scheduled on _____ for the students who have got less than 50% Marks in First Mid Examination.

Topics Covered: Solutions of the Problems given in Previous JNTU Kakinada were discussed.

1. Draw the QAM modulator and demodulator block diagram. Explain briefly
2. Explain the process of generating DM wave
3. Give the concept of Information with suitable examples and state its properties.

List of students for the Backlog Class.

S.No	Roll Number	Name of the Student	Attendance
1	17U41A0403	Baddi Narendra Ashok	Present
2	17U41A0414	Elusuri Bhogesh	Present
3	17U41A0428	Killada Satish Kumar	Present
4	17U41A0429	Kolli Sroveni	Absent
5	17U41A0438	Kadavala Madhuri	Present
6	17U41A0445	Palla Janardhan	Present
7	17U41A0446	P Hemanth Kumar	Present
8	17U41A0455	Srivolla Sairam	Present
9	17U41A0458	Teppala Rajasekhar	Present
10	17U41A0465	Palla Kumaraswamy	Present
11	18U45A0409	Allavarapu Tejaswi	Present
12	18U45A0419	K Chandra Mouli	Absent
13	18U45A0422	D Damodara Dinesh	Absent


Course Instructor


2019-2020
Department of Electronics & Communication Engineering
JNTU Kakinada

LIST OF BENEFICIARIES

S.No	Roll Number	Name of the Student
1	17U41A0428	Killada Satish Kumar
2	17U41A0429	Kolli Sravani
3	17U41A0445	Palla Janardhan
4	17U41A0446	P Hemanth Kumar
5	17U41A0455	Sirisolla Sakam
6	17U41A0458	Teppala Rajasekhar
7	17U41A0465	Palla Kumaraswamy
8	18U45A0409	Allavaramu Tejaswi