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Approved by A.I.C.T.E & Permanently affiliated to JNTU GV Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle – 531002, Visakhapatnam, A.P.

Website: www.diet.edu.in, 9963993229 E-mail:, principal@diet.edu.in

CRITERIA-2

2.6.2 - Attainment of Programme Outcomes and Course Outcomes as evaluated by the institution 2023-24 Institute Academic Calendar,

- 1. Process of Direct Assessment
- 2. Process of Indirect Assessment
- 3. Sample Copy





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Algorithm for CO-PO Attainment process

- **1.** With the help of mid answer scripts attainment level of each CO is calculated and converted into the 3 point scale.
- **2.** Using Quiz and assignment marks attainment level of each CO is calculated and converted into the 3 point scale.
- **3.** Average of Mid Exam, Quiz and Assignment Attainment level is considered as internal exam attainment.
- 4. Using following rubrics Semester end exam attainment calculated

Table 2 Rubrics for Semester end exams

End exam Results	Grade allocation
≥80%	3
60-79%	2
	1
50-59%	

5. Attainment of COs calculated using following formula

70% of semester end exam+30% internal exam

6. Attainment of CO-PO calculated by using following formula

(Attainment Level of Individual CO × mapping level of CO-PO)

3

7. Overall Direct Attainment is equal to Average of each PO attainment level of all COs



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(Indirect assessment)

1. Assessment Tools

Alumni feedback

Faculty feedback

2. Feedback percentage is calculated using formula

(Number of excellent responses+ Number of good responses+ Number of Average responses)÷ Total Responses

3. Rubrics for feedback percentage

Feedback	Grade allocation
percentage	
≥80%	3
60-	2
79.9%	
	1
50-	
59.9%	

- **3.** 10% of weightage given to each feedback
- **4.** Indirect Attainment=

(Alumni feedback Grade× 0.1+Faculty feedback Grade× 0.1)

Overall attainment is calculated using following formula (80% of Direct attainment)+(20% of Indirect attainment)



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Action Plan AY 2023-24

Subject: Electronic Devices and Circuits Faculty Name:Dr.P.Poorna Priya

Based on the review report of previous course instructor The following points focussed on the action plan to implement the lecture plan effectively to enhance the attainment level.

- 1. Mapped the course outcome with possible outcomes.
- 2. Incorporate project based study in lecture plan.
- 3. Adopted latest technologies in teaching learning process.
- 4. Implementation of Lecture plan without deviations.
- 5. Implement Advanced learning strategy by experiential & Participatory learning and activity based education.

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Course Instructor HOD



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	DADI INSTITUTE OF ENGINEERING & TECHNOLOGY										
		COURSE	MARKS AI	NALYSIS SH	IEET						
						ELECTR					
						ONIC					
						DEVICE					
						S AND					
						CIRCUIT					
Acade	mic Year :	2023-24		ubject Cod	e & Title:	S					
	Semester:	II		Course I	nstructor:	Dr.P.Poor	na Priya				
	Branch	ECE			R-20						
C021.1	Understand	d the format	ion of p-n ji	unction and	how it can l	be used as a	p-n junctio				
C021.2	2.Know th	ne construc	tion, work	ing princip	le of rectif	iers with ar	nd				
C021.3	3.Understa	nd the const	ruction, pri	nciple of op	eration of t	ransistors, E	BJT and FET	with their	V-I charact		
C021.4	Know the r	need of trans	sistor biasin	g, various b	iasing techr	niques for B	JT and FET	and stabiliz	ation conc		
C021.5	Perform the	e analysis of	f small sign	al low frequ	ency transis	stor amplifie	er circuits us	sing BJT and	d FET in di		

Course Ou	Test	Quiz & Ass	Average	End Exam	Attainmen
C021.1	2.455	1.74	2.10	2	2.02
C021.2	2.455	1.74	2.10	2	2.02
C021.3	2.455	1.74	2.10	2	2.02
C021.4	2.455	2.00	2.23	2	2.05
C021.5	2.455	2.00	2.23	2	2.05

ATTAINMENT = END EXAM 80 %+INTERNAL 20 % CO-PO MAPPING

CO	PO 1	PO2	PO 3	PO 4	PO5	PO 6	PO 7	PO 8	PO 9
C211.1	3	3	3						
C211.2	3	3	3			2			
C211.3	3	3	3						
C211.4	3	3	3				2		
C211.5	3	3	3	2					





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CO-PO ATTAINMENT

CO	PO 1	PO 2	PO3	PO 4	PO 5	PO 6	PO 7	PO8	PO 9
C211.1	2.02	2.02	2.02						
C211.2	2.02	2.02	2.02			1.76			
C211.3	2.02	2.02	2.02						
C211.4	2.05	2.05	2.05				1.83		
C211.5	2.05	2.05	2.05	1.83					
Avg	2.03	2.03	2.03	1.83		1.76	1.83		

Direct Att	1.92	
irect attain	1.53	
n direct atta	inment= 3*	0.6
<u>rall</u> attainm	2.13	



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Action Taken Report AY 2023-24

Subject: Electronic Devices and Circuits Faculty Name: Dr.P.Poorna Priya

The Attainment of the Subject is 2.13 /3. The following points are important to impose in next Lecture plan to enrich the teaching learning process.

- 1. Mapping of course outcome with maximum possible outcomes needs to improve.
- 2. Incorporate project based study in lecture plan
- 3. Adopt latest technologies in teaching learning process.
- 4. Avoid deviations in implementation of Lecture plan.
- 5. Implement Advanced learning strategy by experiential & Participatory learning and activity based education.

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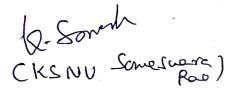
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CO-PO ATTAINMENT ACADEMIC YEAR 2023-24

	SNO		0-10 71	LAINM	HENT A	CADI	EMIC	YEAR	2023-	24					
		COURSE NAME	POI	non			1					54340	19/211	PO12	
	-	Database Management systems(CSD)	2.81	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	POII	1012	
		computer organization	1.26	1,39	2.62	•	2.79							1.05	
	3	Compiler Design	1.24	1.24	1.64	-						-	-	1.24	
	4	Big Data and Analytics	2.95	2.75	1.7	1.56	1.29	•		1.18	1.77	C.58	-	1./13	-
	5	Computer Networks	2.57	2.37	1.77	1.58	1.98	0.99	0.99		0.92		-	2.22	-
	0	Design Analysis of Algorithms	2.47	2.31	2.57	-	1.98		0.99		0.57			2.11	-
ŀ	7	Social Networking Analysis	2.81	2.58	2.46	-	2		0.91	0.91	0.91	1.23	6 31	1.4	۲
ŀ	8 1	Mathematical Foundations of Compute	2.59	2.39	1.55	1.83	1.83	0.91			0.52		+	1.5	-
-	, I	Database Management systems(CSM)	2,76		1.33	1.75		•	·		<u> </u>		-		-
-	10 N	lathematical Foundations as C		2.76	2.56	-	2.68	-	-				 	1,19	-
-	11 C	Computer organization		2.38	1.32	1.73	•	•	·	<u> </u>			-	1.64	
-	12 C	ompiler Design	1.39	1.4	1.63	-	-	-	•					- 	
L		omputer Networks	1.36	1.49	1.87	1.73	1.41	-	-	· • ·	-	<u> </u>		. 1.39	
			2.82	2.82	2.62	g -	2.74	-	-	<u> -</u>	ļ · ·	1			
	15 Na	esign Analysis of Algorithms atural Language Processing	2.52	2.53	2.52	. 	1.98	-	0.95	-	0.95		_	2.1	7.
			2.96	2.76	1.77	1.58	1.98	0.99	0.99	1.18	1.77	0.98	_	1.4	9
-	,	ectronic Devices and Circuits	1.93	1.93	1.93	1.83	_	1.76	1.83	_			٠, .	_	_
1	Sig	nals and Systems	1.22	1.31	1,23	_	1.02		_	_		- · · ·	· ·		_
18	8 Ele	ctronic circuit Analysis	1.99	1.99	1,99	_	1.99		_	72.7	· ·	+			_ <u>:</u> _
19	Ana	log Communications	1.92	1.77	1.93	_	1.6				+	+ -	- -		- :
20	Digi	tal IC Design	2.68	2.3	2.44		2.39	_	_	_	 -	+-			_
21	Anal	log ICs and Applications	2.77	2.34	2.77		2.31		<u> </u>	<u>-</u>	-	 -			<u>-</u>
22	Digit	a! Communications	1.99	1.83	1.94	-	1.65	_	_	_		<u> </u>	\dashv	-	_
23	Mobi	le Cellular communications	2.73	2.36	2.81	-	2.35	2.56	_	-	-	-	_	_	_
24	Opera	ating Systems	2.74	2.36	2.52	_	2.45	_	-	_	·	-	— - -	-	
25	Electr	oMagnetic waves and Transmission	1.98	1.98	1.98	1.83	1	1.76	1.83	-			-	_	
26	Micro	processors and Micro controllers	2.01	1.83	2.04	-	1.65	_	-	i -	-		-	-	-
27	Digital	Signal Processing	2.1	2.1	2.1	1.83	1	1.76	1.83	-			_	-	
8	Interne	et of Things	2.87	2.49	2.81	-	2.35	, <u> </u>	_	-		-	-	_	2
,	Image I	Processing	2.84	2.46	2.81	-	2.35	-	-	-		-		-	_
-	Remote	sensing Geographical information	2.87	2.49	2.81	_	2.35	-	-	-		-	-	-	_
s	Satellite	communications	2.73	2.36	2.81	-	2.35	2.56		-			-	-	-
L	Utilizati	on of Electrical Energy	2.14	2.14	2	1.9	1.91	_	-	-		_	_		-
P	ower S	ystem Operation & Control	2.35	2.35	2.02	2.18	2.18	_	-	-				2.35	
1			2.26	2.45	2.45	2.49	2.64	1				j-			-

				-									-
35	Basic Signals and systems	2.95	2.95	2.36	2.55	2.56	-	-		-		2.65	
36	Hybrid Electrical Vehicle	2.52	2.58	2.16	2.64	2.34	-		-	-	<u> </u>	2.03	
37	Flexible AC Transmission lines	2.64	2.38	2.7	-	2.2	-	-	-	-		-	
38	Industrial Electronics	2.31	2.51	2.51	2.54	2.7	1	-	-	-	-	-	-
39	Artificial intelligence Tools and technique	1.99	1.99	1.99	1.99	1.99	-	-	-	-	-	-	1.99
40		2.67	2.48	2.67	1.82	1.91	_		_	_	2.31		2.4799
41	Power systems	2.72	2.53	2.72	1.82	1.94			_	_	2.35	_	2.4799
42	Electro Magnetic Fields	2.359	2.359	2.228	2.102	2.082		_	-	_ '		-	-
43	Electronic Devices and Circuits	2.74	1.83	2.74	0.91	0.91	0.91	0.91	2.74	2.18	0.91	0.91	-
44	Electrical Circuit Analysis	1.88	1.75	1.88	1.46	1.35							1.98392
45	Power system Analysis	1.94	1.94	1.94	1.94	1.94	_	-	-	_	-	-	1.94
46	Electrical Drives	2.26	2.45	2.45	2.49	2.64	-	_	-	-		-	-
47	Electrical Measurements Instrumentation	2.52	2.51	2.15	2.33	2.33	_	-,	-		-	2.51	
48	Microprocessors and Micro controllers	1.74	1.89	1.89	2.02	2.04	_	_		-		-	1-
49	Power systems-I	2.8	2.8	2.62	2.26	2.41	_	_	_	_		-	-
50	Python Programming	2.81	2.37	2.79	-	2.34	_	-		_	-	-8	
51	ManagerialEconomics and Financial Ana	2.86	2.86	2.67	-	2.89	-	-	-	-	-	-	-
52	Induction and Synchronous Machines	2.44	2.44	2.09	2.27	2.27				•		2.54	
53	Digital Electronics	2.74	1.82	2.74	0.91	0.91	2.18	0.91	0.91	0.91	0.91	0.91	2.74
54	JAVA Programming	2.67	2.67	1.9	1.67	0.95	_	<u>_</u>	-0	_	0.95	_	1.71
55	Mathematical Foundations of Computer 5	2.48	2.29	1.27	1.68	_	_	-	-	-	_		1.44
_56	OOPS through C++	2.5	2.3	0	1.69	-	_	-	_	<u>-</u>	, –		1.45
57	Formal Languages and Automata Theory	2.67	2.67	1.9	1.67	0.95	-	_	-	_	0.95	-	1.71
58	Data Base Management System	2.76	2.76	1.97	1.72	0.99	,-,	_	_	_	0.99	-	1.77
59	Compiler Design	2.72	2.72	2.54		2.72	_	_	-	_	_	-	_
60	Crypto graphy&Network security	2.95	2.95	2.76		2.92	_	-	_	-	-	_	_
61	Social Networks and Semantic Web	2.95	2.95	2.76		2.92	_	_	_	-	-	-	-
62	Computer Networks	2.03	1.17	2.26	1.48	1.56	0.91	0.94	0.91	_	_	-	-
	AVERAGE	2.42	2.29	2.19	1.88	2.06	1.57	1.17	1.31	1.30	1.32	1.83	1.78



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Anakagalle - 531 002.