



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by AICTE, New Delhi & Permanently Affiliated to JNTU GV
Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act
An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.
NII-16, Anakapalle - 531002, Visakhapatnam, A.P.
Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

Academic Year 2024-25

Circular

Date: 16/06/2025

From,
HOD, ECE
Dadi Institute of Engineering and Technology,
Anakapalle.

This is to inform all the III-II B.Tech ECE Students that the Department of ECE is Conducting Backlog Classes to students from 18/06/2025 to 30/06/2025. In this regard, students of III-II B.Tech ECE are instructed to make the best use of this opportunity. The subject schedule and time table will be shared accordingly.

HAPPY LEARNING & ALL THE VERY BEST!!

P. Poornima
HOD ECE

Head of the Department
Electronics & Communication Engg
Dadi Institute of Engg. & Tech.
An Autonomous Institute
Anakapalle-531002

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY



(An Autonomous Institute)
 Approved by AICTE & Permanently Affiliated to JNTU GV
 Accredited by NAAC with 'A' Grade and Inclusion w/s 2(f) & 12(B) of UGC Act
 An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute
 NH-16, Anakapalle - 531002, Visakhapatnam, A.P.
 www.diet.edu.in, Mobile 9963994444, E-Mail: academics@diet.edu.in

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Backlog Classes information

PROGRAM : B.Tech
 CLASS : III-IV/VI th Sem ECE-A & B
 ACADEMIC YEAR : 2024-2025
 COURSE NAME & CODE : DSP((R2031042)

COURSE INSTRUCTOR :S.Veerraju

Discrete Time Signals, Stability of LTI Systems, Invertibility, Response of arbitrary Inputs, Solution of Linear Constant coefficient Differential equations, Frequency domain representation of DTS., Review of Z-Transform, solutions of Differential equations using Z-Transform.

DFS Representation of Periodic Signals, Decimation and Interpolation, Radix-2, Twiddle factors DFT and FFT DIT and DIF Butterfly Diagrams

IIR Filters, IIR Filters Design Techniques and Examples

List of students for the Backlog Classes.

S.No	Roll Number	Name of the Student	Attendance (Dates)					
			19/6/25	23/6/25	24/6/25	26/6/25	30/6/25	
1	22U41A0401	BALLA MEHER KARTHIK	P	AB	P	P	P	Karthik
2	22U41A0407	BYRI LEELA SURYA KALA	P	P	AB	P	P	B. leela surya
3	22U41A0410	DUKKA VARSHITH	AB	P	P	P	P	Varshith
4	22U41A0412	GAIPURI GOWTHAMI	P	P	P	P	AB	Gowthami
5	22U41A0413	GORLE RESHMA	P	P	P	P	AB	Reshma
6	22U41A0420	JILAGAM M V SAI DEVI	P	P	P	P	P	Sai Devi
7	22U41A0423	KANDULA N MURTHY	AB	P	P	P	P	Murthy
8	22U41A0427	KOONA SRINU	P		P	P	P	Srinu
9	22U41A0431	MAMIDI HARSHITHA	AB	P	P	AB		Harshitha
10	22U41A0433	MARISSETY ROSHINI	P	P	P	P	P	Roshini
11	22U41A0434	MITTIREDDY KUSUMA	P	P	P	P	P	Kusuma
12	22U41A0441	P.M.S.D. RAJEEV	P	P	P	P	AB	Rajeev
13	22U41A0446	P. G. TIRUMAL	P	AB	P	P	P	Tirumal
14	22U41A0450	S. S V YASASWINI	P	AB	P	P	P	Yasaswini
15	22U41A0452	SIRAPURAPU GANDHI	P	AB	P	P	P	Gandhi
16	22U41A0456	THOTADA NANI BABU	P	P	P	AB	P	Nani
17	22U41A0461	B. ANNAPOORNA	P	AB	P	P	P	Annapoorna
18	22U41A0465	DEERGASI UDAY KIRAN	P	P	P	P	AB	Uday Kiran
19	22U41A0467	D. Y KAVYA	P	P	P	P	P	Kavya
20	22U41A0470	K. R R S G CHARAN	AB	P	P	P	P	K. charan
21	22U41A0471	LALAM MAHESH	P	P	P	P	AB	L. Mahesh

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY



(An Autonomous Institute)

Approved by AICTE & Permanently Affiliated to JNTU GV
 Accredited by NAAC 'A' Grade and Institution's 2(F) & 12(B) of UGC Act
 An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute
 NRI-06, Anakapalle - 531002, Visakhapatnam, A.P.
 www.diet.edu.in, Mobile: 9607994444, E-Mail: academics@diet.edu.in

22	22U41A0472	M.CH KRISHNA	P	AC	P	P	P	P	Kumar P. Jagadeesh Balaji Shankar Shastri Venkatesh Balaji T. Venkatesh P. Jagadeesh V. Venkatesh
23	22U41A0474	P. JAGADEESWAR RAO	P	P	AB	P	P	P	
24	22U41A0475	POLAMARASETTY SAI	AC	P	P	P	P	P	
25	22U41A0477	SESETTI PRANITH SAI	P	AB	P	P	P	P	
26	22U41A0478	SHAIK SHAFIUDDIN	P	AB	P	AB	P	P	
27	22U41A0479	S. VENKATESH	P	P	AB	P	P	P	
28	22U41A0482	UPPADA BALAJI	P	AB	P	P	P	P	
29	22U41A0484	TOLAPI VENU BABU	AC	P	P	AB	P	P	
30	23U45A0416	PRAGADA JAGADEESH	P	P	P	AD	P	P	
31	23U45A0422	VUJAPU VENKATESH	P	P	P	P	AB	P	

Course Instructor

P. Poornima
HOD

Head of the Department
 Electronics & Communication Engg.
 Dadi Institute of Engg. & Tech.
 An Autonomous Institute
 Anakapalle-531002



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by AICTE, New Delhi & Permanently Affiliated to JNTU GV
Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act
An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.
NH-16, Anakapalle - 531002, Visakhapatnam, A.P.
Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

2024-25

Circular

Date: 20/09/2024

From,
HOD, EEE
Dadi Institute of Engineering and Technology,
Anakapalle.

This is to inform all the II-II & III-II B.Tech EEE Students that the Department of EEE is conducting Backlog Classes to students from 23/09/2024. In this regard, students of III-I & IV -I B.Tech EEE are instructed to make the best use of this opportunity. The subject schedule and time table will be shared accordingly.

HAPPY LEARNING & ALL THE VERY BEST!!


HOD EEE 20/9/24

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Tech.
Anakapalle - 531 002

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by A.I.C.T.E & Permanently affiliated to JNTU GV

Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act

An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle - 531002, Visakhapatnam, A.P.

Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in,



BACKLOG CLASSES

DEPARTMENT OF EEF

PROGRAM: B.Tech

YEAR/SEMESTER: II - II

SECTION: EEE

REGULATION: R-20

SUBJECT NAME: Digital Electronics

FACULTY NAME: M. Hemabha

Back log classes schedule for the students failed in end semester examinations

S.NO	DATE	TOPICS
1	23/9/24	Hamming code & Gray code & Demorgan's theorem
2	24/9/24	Flip flops of RS & JK
3	25/9/24	mul, 8:1 & Decoder 3:8
4	26/9/24	Synchronous & Asynchronous counters
5	27/9/24	Explain mealy & more function state machine

Backlog classes attendance sheet

SNO	ROLL NO	Name of the student	23/9	24/9	25/9	26/9	27/9
1	22V41A0205	P. Vanga	P	P	A	P	P
2	22V41A0206	D. Suresh Babu	P	P	P	P	P
3	22V41A0207	G. Raghav	A	P	P	A	P
4	22V41A0214	N. Thani	P	P	P	P	P
5	22V41A0220	S. Ramesh Kumar	P	P	P	A	A
6	22V41A0204	A. Yeshwanth	P	A	A	P	P


FACULTY


HEAD OF THE DEPARTMENT
Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Tech.
Anakapalle - 531002

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by A.I.C.T.E & Permanently affiliated to JNTU GV

Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act

An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle - 531002, Visakhapatnam, A.P.

Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in,



BACKLOG CLASSES

DEPARTMENT OF EEE

PROGRAM: B.Tech

YEAR/SEMESTER: I-II

SECTION: EEE

REGULATION: DR23

SUBJECT NAME: ECA-1

FACULTY NAME: DR.A.S.L.K Gopalamma

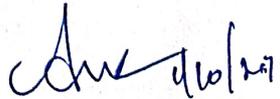
Back log classes schedule for the students failed in end semester examinations

S.NO	DATE	TOPICS
1	17/9/2024	Network reduction technique
2	20/9/2024	coupling and composite ch
3	24/9/2024	steady state analysis
4	27/9/2024	RL-RC
5	1/10/2024	theorem

Backlog classes attendance sheet

S NO	ROLL NO	Name of the student	17/9	20/9	24/9	27/9	1/10
1	2304/A0203	D.Chaitanya	✓	✓	✓	✓	✓
2	2304/A0210	P.Praveen	✓	A	✓	✓	✓
3	2304/A0212	T.Vinay	✓	✓	✓	✓	✓
4	2304/A0204	G.Gulshu	✓	✓	✓	A	✓


FACULTY


HEAD OF THE DEPARTMENT

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Tech.
Anakapalle - 531 002

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by A.I.C.T.E & Permanently affiliated to JNTU GV

Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act

An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle - 531002, Visakhapatnam, A.P.

Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in,



BACKLOG CLASSES

DEPARTMENT OF Electrical & Electronics Engineering

PROGRAM: B.Tech.

YEAR/SEMESTER: III / II

SECTION: 6 -

REGULATION: R-20

SUBJECT NAME: MPMC

FACULTY NAME: Ch. Ramana Babu

Back log classes schedule for the students failed in end semester examinations

S.NO	DATE	TOPICS
1	23/9/24	8086 - Architecture
2	24/9/24	8086 - Minimum Mode
3	25/9/24	8086 - Interrupts - 8255
4	26/9/24	8086 - Interrupts Stepped mode
5	27/9/24	8051 - Pin Diagram
6	1/10/24	8051 - Register organization

Backlog classes attendance sheet

SNO	ROLL NO	Name of the student	23/9	24/9	25/9	26/9	27/9
1	22U45A0214	G. Srinivas	✓	✓	✓	✓	✓
2	219	K. Teja	A	✓	✓	✓	✓
3	224	K. Bala	✓	✓	✓	✓	✓
4	225	L. Saikamal	✓	✓	A	✓	✓
5	233	T. Anam Prasad	✓	✓	✓	✓	✓
6	238	V.S. Ganesh	✓	✓	✓	✓	✓
7	240	S. Pawan.	✓	A	✓	✓	✓

Ch. Ramana Babu
FACULTY

Ch. Ramana Babu
HEAD OF THE DEPARTMENT

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Tech.
Anakapalle - 531 002

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by A.I.C.T.E & Permanently affiliated to JNTU GV

Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act

An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle - 531002, Visakhapatnam, A.P.

Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in,



BACKLOG CLASSES

DEPARTMENT OF EEE

PROGRAM: B.Tech

YEAR/SEMESTER: III-II

SECTION: EEE

REGULATION: R20

SUBJECT NAME: IOT

FACULTY NAME: G. Jagadeesh

Back log classes schedule for the students failed in end semester examinations

S.NO	DATE	TOPICS
1	23/9/24	Discuss Raspberry Pi board
2	24/9/24	Difference b/w M2M and IOT
3	25/9/24	What is meant by cloud computing
4	26/9/24	IOT design methodology steps
5	27/9/24	everything as a xaaS

Backlog classes attendance sheet

SNO	ROLL NO	Name of the student	23/9	24/9	25/9	26/9	27/9
1	22045A0214	G. Srinivas	✓	✓	✓	✓	✓
2	22045A0225	J. Sai Kamal	A	✓	✓	✓	✓
3	22045A0233	T.G. Prakash	✓	✓	A	✓	✓
4	22045A0234	T. Satya Dharm	✓	✓	✓	✓	✓
5	22045A0239	D.C. Halkadhar	✓	✓	✓	✓	✓
6	221145A0240	S. Pavan	✓	✓	✓	✓	✓

G. Jagadeesh

FACULTY

[Signature] 23/9/24
HEAD OF THE DEPARTMENT

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Tech.
Anakapalle - 531 002



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by AICTE, New Delhi & Permanently Affiliated to JNTU GV
Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act
An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.
NH-16, Anakapalle - 531002, Visakhapatnam, A.P.
Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in

2024-25

Circular

Date: 11/02/2025

From,
HOD, EEE
Dadi Institute of Engineering and Technology,
Anakapalle.

This is to inform all the I-I, II-I & III-I B.Tech EEE Students that the Department of EEE is conducting Backlog Classes to students from 14/02/2025. In this regard, students of I-II, II-II & III-II B.Tech EEE are instructed to make the best use of this opportunity. The subject schedule and time table will be shared accordingly.

HAPPY LEARNING & ALL THE VERY BEST!!


HOD EEE 11/2/25

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Tech.
Anakapalle - 531 002

DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institute

Approved by A.I.C.T.E & Permanently affiliated to JNTU GV

Accredited by NAAC with 'A' Grade and Inclusion u/s 2(f) & 12(B) of UGC Act

An ISO 9001:2015, ISO 14001:2015 & ISO 45001:2018 Certified Institute.

NH-16, Anakapalle - 531002, Visakhapatnam, A.P.

Website: www.diet.edu.in, 9963993229 E-mail: principal@diet.edu.in,



BACKLOG CLASSES

DEPARTMENT OF EEE

PROGRAM: B.Tech

YEAR/SEMESTER: I-I

SECTION: -

REGULATION: DR23

SUBJECT NAME: BEEE

FACULTY NAME: M. Anurag

Back log classes schedule for the students failed in end semester examinations

S.NO	DATE	TOPICS
1	14/2/25	working of Dc generator & motor
2	18/2/25	Bill calculation
3	21/2/25	CE configuration
4	25/2/25	RC coupled amplifier
5	4/3/25	Regulator and Synchronous motor

Backlog classes attendance sheet

S NO	ROLL NO	Name of the student	14/2	18/2	21/2	25/2	4/3
1	241241A0204	B.H.S.G. Kumar	P	P	P	P	P
2	241241A0206	N. B. Kiran	A	P	P	P	P
3	241241A0232	P. Sai Vinay	P	A	P	P	P
4	241241A0236	R. Dhansujun	P	A	P	A	P

FACULTY M. Anurag

HEAD OF THE DEPARTMENT

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. Techn.
Anakapalle - 531 002

