



DADI INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by A.I.C.T.E., New Delhi & Affiliated to JNTU, Kakinada)

NAAC Accredited Institute

An ISO 9001:2008: ISO 14001:2004 & OHSAS 18001:2007 Certified Institution

NH-5, Anakapalle – 531002, Visakhapatnam, A.P.

Phone: 08924-221111 / 221122/9963981111, e-mail: info@diet.edu.in

Date: 09-07-2018

Anakapalle

To
The Principal,
Dadi Institute of Engineering & Technology
Anakapalle, Visakhapatnam.

Sub: Request for Approval to conduct Six Day Workshop On **Embedded systems & Sci labs** –
Reg

Respected Sir,

The Department of EEE is proposing to conduct a Six Day Workshop On **Embedded systems & Sci labs**. In this regard, we request you to give approval to conduct this event from 16-07-2018 to 21-06-2018.

Thank You Sir,

Yours faithfully

HOD EEE

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. & Techn.
Anakapalle - 531 002

PRINCIPAL
Dadi Institute of
Engineering & Technolog
ANAKAPALLE - 531 002



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Circular

This is to inform all the students that the Department of EEE is conducting a Six Day Workshop On **Embedded systems & Sci labs** during 16th July to 21st July 2018. Interested Faculty can participate.

HOD EEE

Head of the Department
Electrical & Electronics Engg.
Dadi Institute of Engg. & Tech.
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A One Week Workshop On

SIX DAY WORKSHOP ON EMBEDDED SYSTEMS & SCI LABS
SIX DAY WORKSHOP ON EMBEDDED SYSTEMS & SCI LABS

(16th July – 21st July 2018)

Organized

by

Department of Electrical & Electronics Engineering

In Association with Andhra Pradesh State Skill Development Corporation

Report on one week workshop on Embedded systems & Sci labs

About workshop:

In pursuit of excellence in technical education **Dadi Institute of Engineering & Technology**, Visakhapatnam in association with Andhra Pradesh State Skill Development Corporation organized a one week workshop on **Embedded systems & Sci labs** for the IV year students of **Department of Electrical & Electronics Engineering** during **16th July – 21th July 2018**. This workshop aims to enhance and strengthen the skills among the students on Embedded systems & Sci labs. In order to give hands-on based exposure on Embedded systems & Sci labs, this workshop has been organized for 7 days in Association with APSSDC under CM Skill Excellency Center. As everyone knows that Embedded systems & Sci labs play an important role in Designing and analysis of circuits, which is in full demand and there is a lot of research work and project work going on this concept. Students have a wide scope of opportunities in Embedded systems & Sci labs. The participants were very much satisfied with this workshop. All 91 participants received certificates from APSSDC. This certificate course comprises 2 internal courses. Participants have completed all 2 courses in Embedded systems & Sci labs.

Andhra Pradesh State Skill Development Corporation:

Andhra Pradesh State Skill Development Corporation provides specialized training to the Students of Polytechnic, Engineering colleges etc., in various disciplines in different courses by conducting workshops and certification programmes.

Inauguration

The inauguration of the Workshop was done in the Seminar hall - 1 (AC), DIET Campus on 14-05-2018 at 9.30 AM with lightening of the lamp. The dignitaries of the Inaugural Function included Sri Dadi Ratnakar, Secretary-DIET , Prof. Dr. Ch.S. Naga Prasad, Principal, Vice Principal Dr. K. S. Eswar Rao and Prof.K.Vijay kumar, HOD EEE, HOD's of other departments and Trainers from APSSDC , other faculty members and 91 participants attended the inaugural function.



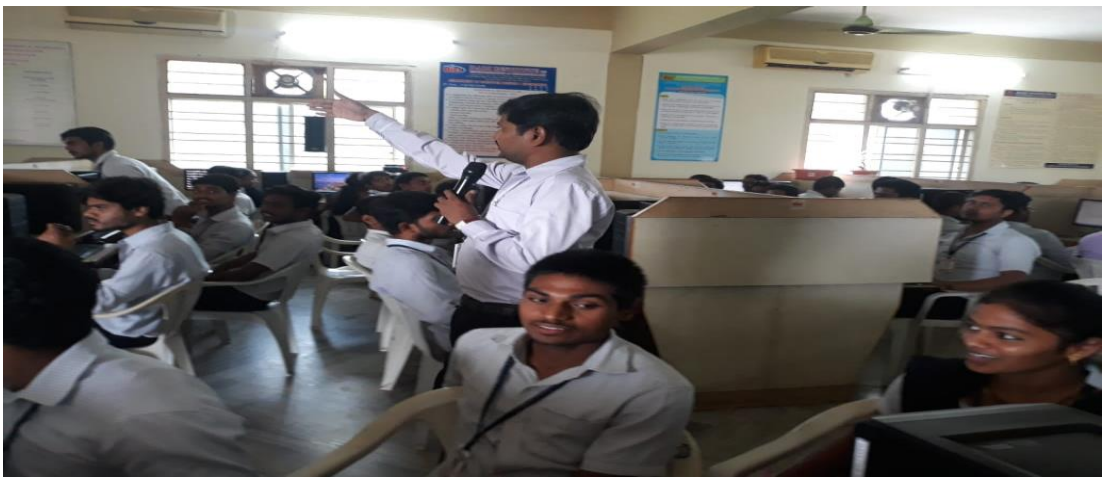
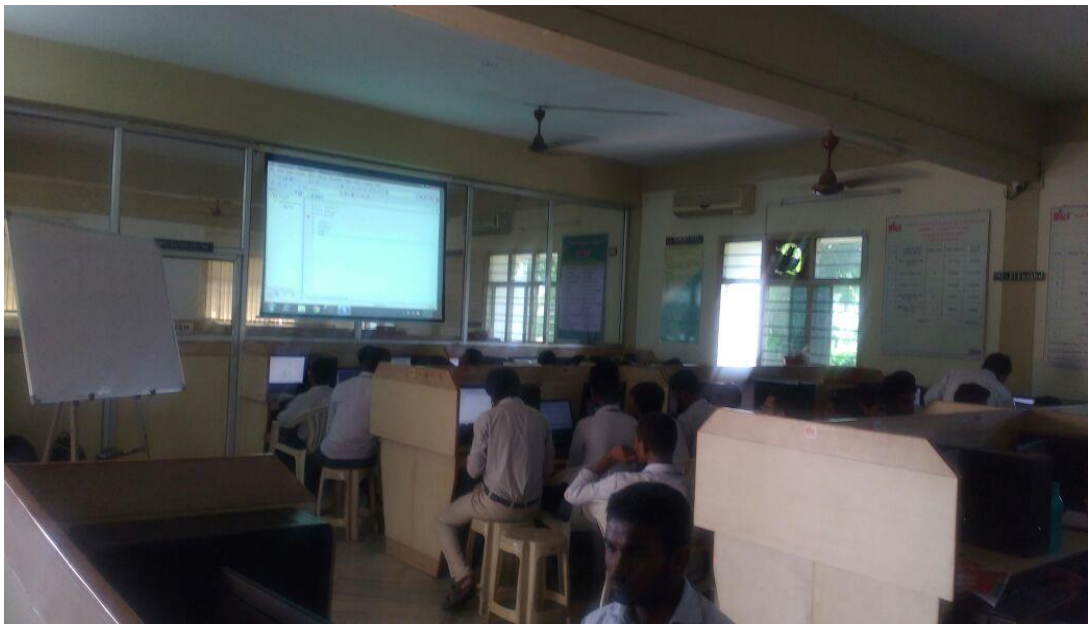
FIG 1 : Hon'ble principal Prof. Dr. Ch.S. Naga Prasad Inaugurating the One Week workshop on Embedded systems & Sci labs by Lightening the Lamp



FIG 2 : Hon'ble principal Prof. Dr. Ch.S. Naga Prasad, addressing the gathering at the one week workshop on Embedded systems & Sci labs.



Mr. G. Chandra Sekhar, Resource Person, APSSDC, addressing the gathering at the one week workshop on Embedded systems & Sci labs



VALEDICTORY FUNCTION:

Sri G. Satyanarayana, Divisional Electrical Engineer, APEPDCL, Anakapalle Division, is the Chief Guest for the Valedictory Function. Prof. Dr. Ch.S. Naga Prasad, Principal, Prof. K. Vijay Kumar, HoD, EEE, HoDs, Staff and Students participated in the Valedictory Session conducted on 21st Jul 2018 at 3:30 pm in Seminar Hall-1 (AC)



Sri G. Satyanarayana, Divisional Electrical Engineer, APEPDCL, Anakapalle Division, addressing the gathering at the one week workshop on Embedded systems & Sci labs



DIET Principal Prof. Dr. Ch.S. Naga Prasad felicitating the Chief Guest



Department of EEE

Feedback for Embedded systems & Sci labs

1. Has the workshop attained its objectives

- Yes
- No

2. Workshop was relevant to my needs

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

3. Instructions were clear and understandable

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

4. Classes was well organized

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

5. Was the Duration of the training sufficient.

- Yes
- No

6. Resource persons were effective.

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree

7. Queries were encouraged

- Strongly agree
- Agree
- Neutral
- Disagree
- Strongly disagree