



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for III B.Tech I Semester Regular/Supplementary Examination Nov-2017

College: DADI INSTT. OF ENGG. & TECH.,GAVARAPALEM, ANAKAPALLE:U4

Htno	Subcode	Subname	Internal	External	Credits
10U41A0421	R31042	DIGITAL IC APPLICATIONS	18	-1	0
10U41A0421	R31044	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	20	-1	0
10U41A0421	R31045	ANTENNAS AND WAVE PROPAGATION	18	-1	0
10U41A0466	R31042	DIGITAL IC APPLICATIONS	14	-1	0
10U41A0466	R31046	DIGITAL COMMUNICATIONS	16	-1	0
10U41A1209	R31054	COMPUTER GRAPHICS	8	26	0
10U41A1218	R31054	COMPUTER GRAPHICS	7	28	0
11U41A0525	R31056	OPERATIONG SYSTEMS	11	7	0
11U45A0209	R31021	COMPLEX VARIABLEDS AND STATISTICAL METHODS	6	0	0
11U45A0209	R31022	ELECTRICAL MEASUREMENT	12	-1	0
12U41A0133	R31015	STRUCTURAL ANALYSIS-II	23	24	0
12U41A0205	R31021	COMPLEX VARIABLEDS AND STATISTICAL METHODS	16	-1	0
12U41A0205	R31022	ELECTRICAL MEASUREMENT	13	21	0
12U41A0205	R31025	ELECTRICAL MACHINES-III	10	7	0
12U41A0209	R31021	COMPLEX VARIABLEDS AND STATISTICAL METHODS	18	-1	0
12U41A0219	R31021	COMPLEX VARIABLEDS AND STATISTICAL METHODS	15	-1	0
12U41A0219	R31026	LINEAR & DIGITAL IC APPLICATION	15	-1	0
12U41A0414	R31041	COMPUTER ARCHITECTURE & ORGANIZATION	22	-1	0
12U41A0419	R31045	ANTENNAS AND WAVE PROPAGATION	21	19	0
12U41A0452	R31042	DIGITAL IC APPLICATIONS	17	10	0
12U41A0452	R31045	ANTENNAS AND WAVE PROPAGATION	15	0	0
12U41A0452	R31046	DIGITAL COMMUNICATIONS	16	21	0
12U41A0456	R31041	COMPUTER ARCHITECTURE & ORGANIZATION	19	-1	0
12U41A0456	R31044	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	20	-1	0
12U41A0456	R31045	ANTENNAS AND WAVE PROPAGATION	14	-1	0
12U41A0456	R31046	DIGITAL COMMUNICATIONS	18	-1	0
12U41A0459	R31042	DIGITAL IC APPLICATIONS	10	17	0
12U41A0459	R31045	ANTENNAS AND WAVE PROPAGATION	8	21	0
12U41A0459	R31046	DIGITAL COMMUNICATIONS	3	11	0
12U41A0487	R31041	COMPUTER ARCHITECTURE & ORGANIZATION	10	-1	0
12U41A0487	R31042	DIGITAL IC APPLICATIONS	18	20	0
12U41A0487	R31043	LINEAR IC APPLICATIONS	16	11	0
12U41A0487	R31044	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	15	0	0
12U41A0487	R31045	ANTENNAS AND WAVE PROPAGATION	9	0	0
12U41A0487	R31046	DIGITAL COMMUNICATIONS	5	-1	0
12U41A0491	R31043	LINEAR IC APPLICATIONS	5	10	0
12U41A0491	R31045	ANTENNAS AND WAVE PROPAGATION	5	21	0
12U41A0503	R31052	COMPUTER NETWORKS	10	15	0
12U41A0503	R31054	COMPUTER GRAPHICS	15	2	0
12U41A0503	R31055	MICRO PROCESSORS AND MULTICORE SYSTEMS	6	6	0
12U41A0503	R31056	OPERATIONG SYSTEMS	10	-1	0
12U41A0547	R31051	COMPILER DESIGN	23	31	4
12U41A0553	R31056	OPERATIONG SYSTEMS	19	0	0
12U45A0419	R31046	DIGITAL COMMUNICATIONS	24	-1	0

Htno	Subcode	Subname	Internal	External	Credits
13U41A0104	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	23	54	3
13U41A0106	RT31011	GEOTECHNICAL ENGINEERING-I	5	13	0
13U41A0510	RT31054	DATABASE MANAGEMENT SYSTEMS	18	2	0
13U41A0510	RT31055	OPERATING SYSTEMS	13	-1	0
13U45A0208	R31021	COMPLEX VARIABLES AND STATISTICAL METHODS	20	20	0
13U45A0212	R31026	LINEAR & DIGITAL IC APPLICATION	17	32	4
13U45A0225	R31021	COMPLEX VARIABLES AND STATISTICAL METHODS	24	-1	0
13U45A0225	R31023	POWER SYSTEMS-II	16	28	4
13U45A0225	R31026	LINEAR & DIGITAL IC APPLICATION	10	16	0
13U45A0241	R31021	COMPLEX VARIABLES AND STATISTICAL METHODS	13	13	0
14HN1A0213	RT31025	POWER ELECTRONICS	19	32	3
14U41A0104	RT31014	ENGINEERING GEOLOGY	4	4	0
14U41A0104	RT31015	TRANSPORTATION ENGINEERING-I	16	37	3
14U41A0108	RT31011	GEOTECHNICAL ENGINEERING-I	20	38	3
14U41A0203	RT31025	POWER ELECTRONICS	19	-1	0
14U41A0203	RT31027	ELECTRICAL MACHINES-II LAB	12	29	2
14U41A0205	RT31023	POWER SYSTEMS-II	14	33	3
14U41A0205	RT31025	POWER ELECTRONICS	19	27	3
14U41A0205	RT31026	LINEAR & DIGITAL IC APPLICATIONS	12	34	3
14U41A0206	RT31024	ELECTRICAL MACHINES-III	17	6	0
14U41A0211	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	15	0
14U41A0218	RT31027	ELECTRICAL MACHINES-II LAB	12	34	2
14U41A0218	RT31028	CONTROL SYSTEMS LAB	12	39	2
14U41A0403	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	16	17	0
14U41A0403	RT31044	DIGITAL SYSTEM DESIGN & DICA	24	24	3
14U41A0405	RT31044	DIGITAL SYSTEM DESIGN & DICA	15	9	0
14U41A0405	RT31045	ANTENNAS AND WAVE PROPAGATION	21	19	0
14U41A0409	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	24	37	3
14U41A0409	RT31045	ANTENNAS AND WAVE PROPAGATION	27	27	3
14U41A0411	RT31041	PULSE & DIGITAL CIRCUITS	22	24	3
14U41A0411	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	29	3
14U41A0411	RT31043	CONTROL SYSTEMS	25	33	3
14U41A0411	RT31044	DIGITAL SYSTEM DESIGN & DICA	18	19	0
14U41A0413	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	19	33	3
14U41A0413	RT31044	DIGITAL SYSTEM DESIGN & DICA	14	46	3
14U41A0414	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	14	0
14U41A0416	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	16	13	0
14U41A0416	RT31043	CONTROL SYSTEMS	25	5	0
14U41A0416	RT31044	DIGITAL SYSTEM DESIGN & DICA	11	15	0
14U41A0416	RT31045	ANTENNAS AND WAVE PROPAGATION	24	15	0
14U41A0418	RT31016	IPR & PATENTS	19	15	0
14U41A0418	RT31041	PULSE & DIGITAL CIRCUITS	13	9	0
14U41A0418	RT31043	CONTROL SYSTEMS	15	16	0
14U41A0418	RT31044	DIGITAL SYSTEM DESIGN & DICA	12	16	0
14U41A0418	RT31045	ANTENNAS AND WAVE PROPAGATION	16	0	0
14U41A0425	RT31041	PULSE & DIGITAL CIRCUITS	16	33	3
14U41A0425	RT31043	CONTROL SYSTEMS	20	16	0
14U41A0503	RT31051	COMPILER DESIGN	10	28	0
14U41A0503	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	10	20	0
14U41A0503	RT31054	DATABASE MANAGEMENT SYSTEMS	19	11	0

Htno	Subcode	Subname	Internal	External	Credits
14U41A0518	RT31051	COMPILER DESIGN	23	5	0
14U41A0518	RT31055	OPERATING SYSTEMS	18	25	3
14U41A0522	RT31051	COMPILER DESIGN	22	19	0
14U41A0522	RT31054	DATABASE MANAGEMENT SYSTEMS	20	31	3
14U41A0522	RT31055	OPERATING SYSTEMS	23	17	0
14U41A0526	RT31051	COMPILER DESIGN	18	3	0
14U41A0526	RT31054	DATABASE MANAGEMENT SYSTEMS	18	6	0
14U41A0526	RT31055	OPERATING SYSTEMS	21	35	3
14U41A0529	RT31051	COMPILER DESIGN	15	13	0
14U41A0529	RT31054	DATABASE MANAGEMENT SYSTEMS	18	19	0
14U41A0529	RT31055	OPERATING SYSTEMS	20	9	0
14U41A0538	RT31051	COMPILER DESIGN	24	16	0
14U45A0115	RT31011	GEOTECHNICAL ENGINEERING-I	12	-1	0
14U45A0115	RT31014	ENGINEERING GEOLOGY	13	-1	0
14U45A0115	RT31015	TRANSPORTATION ENGINEERING-I	13	-1	0
14U45A0417	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	17	5	0
15U41A0101	RT31011	GEOTECHNICAL ENGINEERING-I	19	16	0
15U41A0101	RT31012	STRUCTURAL ANALYSIS-II	16	32	3
15U41A0101	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	17	22	0
15U41A0101	RT31014	ENGINEERING GEOLOGY	26	26	3
15U41A0101	RT31015	TRANSPORTATION ENGINEERING-I	22	30	3
15U41A0101	RT31016	IPR & PATENTS	23	24	2
15U41A0101	RT31017	GEOTECHNICAL ENGINEERING LAB	13	30	2
15U41A0101	RT31018	ENGINEERING GEOLOGY LAB	15	35	2
15U41A0102	RT31011	GEOTECHNICAL ENGINEERING-I	18	28	3
15U41A0102	RT31012	STRUCTURAL ANALYSIS-II	19	42	3
15U41A0102	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	15	30	3
15U41A0102	RT31014	ENGINEERING GEOLOGY	22	29	3
15U41A0102	RT31015	TRANSPORTATION ENGINEERING-I	21	39	3
15U41A0102	RT31016	IPR & PATENTS	20	25	2
15U41A0102	RT31017	GEOTECHNICAL ENGINEERING LAB	24	34	2
15U41A0102	RT31018	ENGINEERING GEOLOGY LAB	20	40	2
15U41A0103	RT31011	GEOTECHNICAL ENGINEERING-I	21	14	0
15U41A0103	RT31012	STRUCTURAL ANALYSIS-II	22	41	3
15U41A0103	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	24	24	3
15U41A0103	RT31014	ENGINEERING GEOLOGY	23	24	3
15U41A0103	RT31015	TRANSPORTATION ENGINEERING-I	23	32	3
15U41A0103	RT31016	IPR & PATENTS	22	29	2
15U41A0103	RT31017	GEOTECHNICAL ENGINEERING LAB	24	32	2
15U41A0103	RT31018	ENGINEERING GEOLOGY LAB	19	40	2
15U41A0104	RT31011	GEOTECHNICAL ENGINEERING-I	25	59	3
15U41A0104	RT31012	STRUCTURAL ANALYSIS-II	25	42	3
15U41A0104	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	29	3
15U41A0104	RT31014	ENGINEERING GEOLOGY	24	27	3
15U41A0104	RT31015	TRANSPORTATION ENGINEERING-I	25	58	3
15U41A0104	RT31016	IPR & PATENTS	24	52	2
15U41A0104	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
15U41A0104	RT31018	ENGINEERING GEOLOGY LAB	24	45	2
15U41A0105	RT31011	GEOTECHNICAL ENGINEERING-I	15	20	0
15U41A0105	RT31012	STRUCTURAL ANALYSIS-II	10	30	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0105	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	8	23	0
15U41A0105	RT31014	ENGINEERING GEOLOGY	13	27	3
15U41A0105	RT31015	TRANSPORTATION ENGINEERING-I	16	34	3
15U41A0105	RT31016	IPR & PATENTS	13	14	0
15U41A0105	RT31017	GEOTECHNICAL ENGINEERING LAB	22	30	2
15U41A0105	RT31018	ENGINEERING GEOLOGY LAB	15	30	2
15U41A0106	RT31011	GEOTECHNICAL ENGINEERING-I	16	30	3
15U41A0106	RT31012	STRUCTURAL ANALYSIS-II	14	16	0
15U41A0106	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	34	3
15U41A0106	RT31014	ENGINEERING GEOLOGY	15	31	3
15U41A0106	RT31015	TRANSPORTATION ENGINEERING-I	20	41	3
15U41A0106	RT31016	IPR & PATENTS	11	30	2
15U41A0106	RT31017	GEOTECHNICAL ENGINEERING LAB	22	30	2
15U41A0106	RT31018	ENGINEERING GEOLOGY LAB	17	-1	0
15U41A0107	RT31011	GEOTECHNICAL ENGINEERING-I	16	16	0
15U41A0107	RT31012	STRUCTURAL ANALYSIS-II	20	39	3
15U41A0107	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	10	12	0
15U41A0107	RT31014	ENGINEERING GEOLOGY	22	8	0
15U41A0107	RT31015	TRANSPORTATION ENGINEERING-I	20	31	3
15U41A0107	RT31016	IPR & PATENTS	19	12	0
15U41A0107	RT31017	GEOTECHNICAL ENGINEERING LAB	23	29	2
15U41A0107	RT31018	ENGINEERING GEOLOGY LAB	17	38	2
15U41A0108	RT31011	GEOTECHNICAL ENGINEERING-I	21	35	3
15U41A0108	RT31012	STRUCTURAL ANALYSIS-II	20	31	3
15U41A0108	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	21	21	0
15U41A0108	RT31014	ENGINEERING GEOLOGY	22	26	3
15U41A0108	RT31015	TRANSPORTATION ENGINEERING-I	22	46	3
15U41A0108	RT31016	IPR & PATENTS	20	56	2
15U41A0108	RT31017	GEOTECHNICAL ENGINEERING LAB	25	42	2
15U41A0108	RT31018	ENGINEERING GEOLOGY LAB	22	45	2
15U41A0109	RT31011	GEOTECHNICAL ENGINEERING-I	29	29	3
15U41A0109	RT31012	STRUCTURAL ANALYSIS-II	20	36	3
15U41A0109	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	55	3
15U41A0109	RT31014	ENGINEERING GEOLOGY	28	38	3
15U41A0109	RT31015	TRANSPORTATION ENGINEERING-I	29	49	3
15U41A0109	RT31016	IPR & PATENTS	28	10	0
15U41A0109	RT31017	GEOTECHNICAL ENGINEERING LAB	25	40	2
15U41A0109	RT31018	ENGINEERING GEOLOGY LAB	20	41	2
15U41A0110	RT31011	GEOTECHNICAL ENGINEERING-I	22	28	3
15U41A0110	RT31012	STRUCTURAL ANALYSIS-II	18	33	3
15U41A0110	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	42	3
15U41A0110	RT31014	ENGINEERING GEOLOGY	24	24	3
15U41A0110	RT31015	TRANSPORTATION ENGINEERING-I	24	31	3
15U41A0110	RT31016	IPR & PATENTS	22	43	2
15U41A0110	RT31017	GEOTECHNICAL ENGINEERING LAB	25	45	2
15U41A0110	RT31018	ENGINEERING GEOLOGY LAB	23	46	2
15U41A0111	RT31011	GEOTECHNICAL ENGINEERING-I	13	13	0
15U41A0111	RT31012	STRUCTURAL ANALYSIS-II	10	0	0
15U41A0111	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	9	16	0
15U41A0111	RT31014	ENGINEERING GEOLOGY	16	19	0

Htno	Subcode	Subname	Internal	External	Credits
15U41A0111	RT31015	TRANSPORTATION ENGINEERING-I	15	23	0
15U41A0111	RT31016	IPR & PATENTS	12	18	0
15U41A0111	RT31017	GEOTECHNICAL ENGINEERING LAB	17	29	2
15U41A0111	RT31018	ENGINEERING GEOLOGY LAB	17	44	2
15U41A0112	RT31011	GEOTECHNICAL ENGINEERING-I	22	38	3
15U41A0112	RT31012	STRUCTURAL ANALYSIS-II	23	42	3
15U41A0112	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	20	31	3
15U41A0112	RT31014	ENGINEERING GEOLOGY	25	48	3
15U41A0112	RT31015	TRANSPORTATION ENGINEERING-I	23	64	3
15U41A0112	RT31016	IPR & PATENTS	26	50	2
15U41A0112	RT31017	GEOTECHNICAL ENGINEERING LAB	25	48	2
15U41A0112	RT31018	ENGINEERING GEOLOGY LAB	23	47	2
15U41A0113	RT31011	GEOTECHNICAL ENGINEERING-I	23	54	3
15U41A0113	RT31012	STRUCTURAL ANALYSIS-II	23	46	3
15U41A0113	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	25	32	3
15U41A0113	RT31014	ENGINEERING GEOLOGY	25	38	3
15U41A0113	RT31015	TRANSPORTATION ENGINEERING-I	25	43	3
15U41A0113	RT31016	IPR & PATENTS	23	24	2
15U41A0113	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
15U41A0113	RT31018	ENGINEERING GEOLOGY LAB	24	44	2
15U41A0114	RT31011	GEOTECHNICAL ENGINEERING-I	25	28	3
15U41A0114	RT31012	STRUCTURAL ANALYSIS-II	23	31	3
15U41A0114	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	32	3
15U41A0114	RT31014	ENGINEERING GEOLOGY	24	28	3
15U41A0114	RT31015	TRANSPORTATION ENGINEERING-I	18	35	3
15U41A0114	RT31016	IPR & PATENTS	24	38	2
15U41A0114	RT31017	GEOTECHNICAL ENGINEERING LAB	24	29	2
15U41A0114	RT31018	ENGINEERING GEOLOGY LAB	21	40	2
15U41A0115	RT31011	GEOTECHNICAL ENGINEERING-I	19	16	0
15U41A0115	RT31012	STRUCTURAL ANALYSIS-II	17	46	3
15U41A0115	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	31	3
15U41A0115	RT31014	ENGINEERING GEOLOGY	23	24	3
15U41A0115	RT31015	TRANSPORTATION ENGINEERING-I	17	44	3
15U41A0115	RT31016	IPR & PATENTS	20	17	0
15U41A0115	RT31017	GEOTECHNICAL ENGINEERING LAB	18	30	2
15U41A0115	RT31018	ENGINEERING GEOLOGY LAB	21	40	2
15U41A0116	RT31011	GEOTECHNICAL ENGINEERING-I	16	37	3
15U41A0116	RT31012	STRUCTURAL ANALYSIS-II	24	38	3
15U41A0116	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	17	39	3
15U41A0116	RT31014	ENGINEERING GEOLOGY	23	31	3
15U41A0116	RT31015	TRANSPORTATION ENGINEERING-I	23	57	3
15U41A0116	RT31016	IPR & PATENTS	21	45	2
15U41A0116	RT31017	GEOTECHNICAL ENGINEERING LAB	23	47	2
15U41A0116	RT31018	ENGINEERING GEOLOGY LAB	22	41	2
15U41A0118	RT31011	GEOTECHNICAL ENGINEERING-I	26	35	3
15U41A0118	RT31012	STRUCTURAL ANALYSIS-II	22	41	3
15U41A0118	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	32	3
15U41A0118	RT31014	ENGINEERING GEOLOGY	27	30	3
15U41A0118	RT31015	TRANSPORTATION ENGINEERING-I	17	34	3
15U41A0118	RT31016	IPR & PATENTS	24	15	0

Htno	Subcode	Subname	Internal	External	Credits
15U41A0118	RT31017	GEOTECHNICAL ENGINEERING LAB	21	47	2
15U41A0118	RT31018	ENGINEERING GEOLOGY LAB	20	39	2
15U41A0119	RT31011	GEOTECHNICAL ENGINEERING-I	25	33	3
15U41A0119	RT31012	STRUCTURAL ANALYSIS-II	29	35	3
15U41A0119	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	29	45	3
15U41A0119	RT31014	ENGINEERING GEOLOGY	26	26	3
15U41A0119	RT31015	TRANSPORTATION ENGINEERING-I	23	37	3
15U41A0119	RT31016	IPR & PATENTS	22	40	2
15U41A0119	RT31017	GEOTECHNICAL ENGINEERING LAB	23	49	2
15U41A0119	RT31018	ENGINEERING GEOLOGY LAB	23	49	2
15U41A0120	RT31011	GEOTECHNICAL ENGINEERING-I	22	24	3
15U41A0120	RT31012	STRUCTURAL ANALYSIS-II	25	61	3
15U41A0120	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	20	36	3
15U41A0120	RT31014	ENGINEERING GEOLOGY	28	38	3
15U41A0120	RT31015	TRANSPORTATION ENGINEERING-I	26	41	3
15U41A0120	RT31016	IPR & PATENTS	22	29	2
15U41A0120	RT31017	GEOTECHNICAL ENGINEERING LAB	24	46	2
15U41A0120	RT31018	ENGINEERING GEOLOGY LAB	24	48	2
15U41A0121	RT31011	GEOTECHNICAL ENGINEERING-I	19	43	3
15U41A0121	RT31012	STRUCTURAL ANALYSIS-II	22	42	3
15U41A0121	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	20	40	3
15U41A0121	RT31014	ENGINEERING GEOLOGY	26	55	3
15U41A0121	RT31015	TRANSPORTATION ENGINEERING-I	27	59	3
15U41A0121	RT31016	IPR & PATENTS	19	40	2
15U41A0121	RT31017	GEOTECHNICAL ENGINEERING LAB	24	35	2
15U41A0121	RT31018	ENGINEERING GEOLOGY LAB	20	45	2
15U41A0122	RT31011	GEOTECHNICAL ENGINEERING-I	16	24	3
15U41A0122	RT31012	STRUCTURAL ANALYSIS-II	16	17	0
15U41A0122	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	19	22	0
15U41A0122	RT31014	ENGINEERING GEOLOGY	19	17	0
15U41A0122	RT31015	TRANSPORTATION ENGINEERING-I	14	13	0
15U41A0122	RT31016	IPR & PATENTS	15	25	2
15U41A0122	RT31017	GEOTECHNICAL ENGINEERING LAB	20	32	2
15U41A0122	RT31018	ENGINEERING GEOLOGY LAB	15	39	2
15U41A0123	RT31011	GEOTECHNICAL ENGINEERING-I	19	31	3
15U41A0123	RT31012	STRUCTURAL ANALYSIS-II	22	30	3
15U41A0123	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	24	40	3
15U41A0123	RT31014	ENGINEERING GEOLOGY	25	26	3
15U41A0123	RT31015	TRANSPORTATION ENGINEERING-I	20	32	3
15U41A0123	RT31016	IPR & PATENTS	23	34	2
15U41A0123	RT31017	GEOTECHNICAL ENGINEERING LAB	24	39	2
15U41A0123	RT31018	ENGINEERING GEOLOGY LAB	22	45	2
15U41A0124	RT31011	GEOTECHNICAL ENGINEERING-I	22	30	3
15U41A0124	RT31012	STRUCTURAL ANALYSIS-II	20	66	3
15U41A0124	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	33	3
15U41A0124	RT31014	ENGINEERING GEOLOGY	24	59	3
15U41A0124	RT31015	TRANSPORTATION ENGINEERING-I	24	41	3
15U41A0124	RT31016	IPR & PATENTS	20	29	2
15U41A0124	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
15U41A0124	RT31018	ENGINEERING GEOLOGY LAB	23	42	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0125	RT31011	GEOTECHNICAL ENGINEERING-I	28	51	3
15U41A0125	RT31012	STRUCTURAL ANALYSIS-II	23	41	3
15U41A0125	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	26	3
15U41A0125	RT31014	ENGINEERING GEOLOGY	25	49	3
15U41A0125	RT31015	TRANSPORTATION ENGINEERING-I	24	54	3
15U41A0125	RT31016	IPR & PATENTS	22	37	2
15U41A0125	RT31017	GEOTECHNICAL ENGINEERING LAB	25	49	2
15U41A0125	RT31018	ENGINEERING GEOLOGY LAB	21	41	2
15U41A0126	RT31011	GEOTECHNICAL ENGINEERING-I	24	39	3
15U41A0126	RT31012	STRUCTURAL ANALYSIS-II	24	32	3
15U41A0126	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	27	35	3
15U41A0126	RT31014	ENGINEERING GEOLOGY	29	33	3
15U41A0126	RT31015	TRANSPORTATION ENGINEERING-I	25	30	3
15U41A0126	RT31016	IPR & PATENTS	26	24	2
15U41A0126	RT31017	GEOTECHNICAL ENGINEERING LAB	24	49	2
15U41A0126	RT31018	ENGINEERING GEOLOGY LAB	19	38	2
15U41A0127	RT31011	GEOTECHNICAL ENGINEERING-I	15	32	3
15U41A0127	RT31012	STRUCTURAL ANALYSIS-II	11	33	3
15U41A0127	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	10	46	3
15U41A0127	RT31014	ENGINEERING GEOLOGY	23	30	3
15U41A0127	RT31015	TRANSPORTATION ENGINEERING-I	22	40	3
15U41A0127	RT31016	IPR & PATENTS	21	38	2
15U41A0127	RT31017	GEOTECHNICAL ENGINEERING LAB	15	45	2
15U41A0127	RT31018	ENGINEERING GEOLOGY LAB	19	38	2
15U41A0128	RT31011	GEOTECHNICAL ENGINEERING-I	20	17	0
15U41A0128	RT31012	STRUCTURAL ANALYSIS-II	20	43	3
15U41A0128	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	34	3
15U41A0128	RT31014	ENGINEERING GEOLOGY	21	46	3
15U41A0128	RT31015	TRANSPORTATION ENGINEERING-I	19	38	3
15U41A0128	RT31016	IPR & PATENTS	23	32	2
15U41A0128	RT31017	GEOTECHNICAL ENGINEERING LAB	19	30	2
15U41A0128	RT31018	ENGINEERING GEOLOGY LAB	18	37	2
15U41A0130	RT31011	GEOTECHNICAL ENGINEERING-I	26	42	3
15U41A0130	RT31012	STRUCTURAL ANALYSIS-II	28	51	3
15U41A0130	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	30	38	3
15U41A0130	RT31014	ENGINEERING GEOLOGY	27	51	3
15U41A0130	RT31015	TRANSPORTATION ENGINEERING-I	27	60	3
15U41A0130	RT31016	IPR & PATENTS	25	34	2
15U41A0130	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
15U41A0130	RT31018	ENGINEERING GEOLOGY LAB	23	50	2
15U41A0131	RT31011	GEOTECHNICAL ENGINEERING-I	20	38	3
15U41A0131	RT31012	STRUCTURAL ANALYSIS-II	25	25	3
15U41A0131	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	25	21	0
15U41A0131	RT31014	ENGINEERING GEOLOGY	23	26	3
15U41A0131	RT31015	TRANSPORTATION ENGINEERING-I	22	24	3
15U41A0131	RT31016	IPR & PATENTS	20	16	0
15U41A0131	RT31017	GEOTECHNICAL ENGINEERING LAB	24	43	2
15U41A0131	RT31018	ENGINEERING GEOLOGY LAB	17	43	2
15U41A0201	RT31016	IPR & PATENTS	20	40	2
15U41A0201	RT31021	ELECTRICAL MEASUREMENTS	18	29	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0201	RT31022	MEFA	26	36	3
15U41A0201	RT31023	POWER SYSTEMS-II	20	14	0
15U41A0201	RT31024	ELECTRICAL MACHINES-III	16	28	3
15U41A0201	RT31025	POWER ELECTRONICS	17	9	0
15U41A0201	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	8	0
15U41A0201	RT31027	ELECTRICAL MACHINES-II LAB	21	36	2
15U41A0201	RT31028	CONTROL SYSTEMS LAB	24	40	2
15U41A0202	RT31016	IPR & PATENTS	21	37	2
15U41A0202	RT31021	ELECTRICAL MEASUREMENTS	22	46	3
15U41A0202	RT31022	MEFA	25	52	3
15U41A0202	RT31023	POWER SYSTEMS-II	17	37	3
15U41A0202	RT31024	ELECTRICAL MACHINES-III	21	45	3
15U41A0202	RT31025	POWER ELECTRONICS	17	34	3
15U41A0202	RT31026	LINEAR & DIGITAL IC APPLICATIONS	19	32	3
15U41A0202	RT31027	ELECTRICAL MACHINES-II LAB	21	33	2
15U41A0202	RT31028	CONTROL SYSTEMS LAB	24	43	2
15U41A0203	RT31016	IPR & PATENTS	26	26	2
15U41A0203	RT31021	ELECTRICAL MEASUREMENTS	20	24	3
15U41A0203	RT31022	MEFA	20	46	3
15U41A0203	RT31023	POWER SYSTEMS-II	26	24	3
15U41A0203	RT31024	ELECTRICAL MACHINES-III	19	21	0
15U41A0203	RT31025	POWER ELECTRONICS	21	36	3
15U41A0203	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	24	3
15U41A0203	RT31027	ELECTRICAL MACHINES-II LAB	13	28	2
15U41A0203	RT31028	CONTROL SYSTEMS LAB	23	42	2
15U41A0204	RT31016	IPR & PATENTS	19	24	2
15U41A0204	RT31021	ELECTRICAL MEASUREMENTS	13	7	0
15U41A0204	RT31022	MEFA	21	25	3
15U41A0204	RT31023	POWER SYSTEMS-II	15	25	3
15U41A0204	RT31024	ELECTRICAL MACHINES-III	17	2	0
15U41A0204	RT31025	POWER ELECTRONICS	16	36	3
15U41A0204	RT31026	LINEAR & DIGITAL IC APPLICATIONS	13	7	0
15U41A0204	RT31027	ELECTRICAL MACHINES-II LAB	21	33	2
15U41A0204	RT31028	CONTROL SYSTEMS LAB	23	40	2
15U41A0205	RT31016	IPR & PATENTS	21	41	2
15U41A0205	RT31021	ELECTRICAL MEASUREMENTS	16	28	3
15U41A0205	RT31022	MEFA	18	29	3
15U41A0205	RT31023	POWER SYSTEMS-II	16	24	3
15U41A0205	RT31024	ELECTRICAL MACHINES-III	21	16	0
15U41A0205	RT31025	POWER ELECTRONICS	18	6	0
15U41A0205	RT31026	LINEAR & DIGITAL IC APPLICATIONS	19	24	3
15U41A0205	RT31027	ELECTRICAL MACHINES-II LAB	13	33	2
15U41A0205	RT31028	CONTROL SYSTEMS LAB	22	40	2
15U41A0207	RT31016	IPR & PATENTS	22	33	2
15U41A0207	RT31021	ELECTRICAL MEASUREMENTS	24	37	3
15U41A0207	RT31022	MEFA	26	43	3
15U41A0207	RT31023	POWER SYSTEMS-II	25	24	3
15U41A0207	RT31024	ELECTRICAL MACHINES-III	26	36	3
15U41A0207	RT31025	POWER ELECTRONICS	22	24	3
15U41A0207	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	25	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0207	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
15U41A0207	RT31028	CONTROL SYSTEMS LAB	25	49	2
15U41A0209	RT31016	IPR & PATENTS	24	29	2
15U41A0209	RT31021	ELECTRICAL MEASUREMENTS	30	26	3
15U41A0209	RT31022	MEFA	25	53	3
15U41A0209	RT31023	POWER SYSTEMS-II	29	34	3
15U41A0209	RT31024	ELECTRICAL MACHINES-III	28	31	3
15U41A0209	RT31025	POWER ELECTRONICS	30	46	3
15U41A0209	RT31026	LINEAR & DIGITAL IC APPLICATIONS	28	38	3
15U41A0209	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
15U41A0209	RT31028	CONTROL SYSTEMS LAB	24	49	2
15U41A0210	RT31016	IPR & PATENTS	25	35	2
15U41A0210	RT31021	ELECTRICAL MEASUREMENTS	21	26	3
15U41A0210	RT31022	MEFA	27	41	3
15U41A0210	RT31023	POWER SYSTEMS-II	26	53	3
15U41A0210	RT31024	ELECTRICAL MACHINES-III	25	17	0
15U41A0210	RT31025	POWER ELECTRONICS	25	50	3
15U41A0210	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	29	3
15U41A0210	RT31027	ELECTRICAL MACHINES-II LAB	24	48	2
15U41A0210	RT31028	CONTROL SYSTEMS LAB	23	45	2
15U41A0211	RT31016	IPR & PATENTS	23	47	2
15U41A0211	RT31021	ELECTRICAL MEASUREMENTS	21	8	0
15U41A0211	RT31022	MEFA	23	24	3
15U41A0211	RT31023	POWER SYSTEMS-II	21	18	0
15U41A0211	RT31024	ELECTRICAL MACHINES-III	19	26	3
15U41A0211	RT31025	POWER ELECTRONICS	18	6	0
15U41A0211	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	9	0
15U41A0211	RT31027	ELECTRICAL MACHINES-II LAB	21	38	2
15U41A0211	RT31028	CONTROL SYSTEMS LAB	20	36	2
15U41A0213	RT31016	IPR & PATENTS	24	38	2
15U41A0213	RT31021	ELECTRICAL MEASUREMENTS	22	31	3
15U41A0213	RT31022	MEFA	23	47	3
15U41A0213	RT31023	POWER SYSTEMS-II	24	29	3
15U41A0213	RT31024	ELECTRICAL MACHINES-III	21	24	3
15U41A0213	RT31025	POWER ELECTRONICS	17	32	3
15U41A0213	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	17	0
15U41A0213	RT31027	ELECTRICAL MACHINES-II LAB	25	48	2
15U41A0213	RT31028	CONTROL SYSTEMS LAB	21	40	2
15U41A0216	RT31016	IPR & PATENTS	23	28	2
15U41A0216	RT31021	ELECTRICAL MEASUREMENTS	15	9	0
15U41A0216	RT31022	MEFA	17	56	3
15U41A0216	RT31023	POWER SYSTEMS-II	17	37	3
15U41A0216	RT31024	ELECTRICAL MACHINES-III	18	29	3
15U41A0216	RT31025	POWER ELECTRONICS	14	44	3
15U41A0216	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	24	3
15U41A0216	RT31027	ELECTRICAL MACHINES-II LAB	20	35	2
15U41A0216	RT31028	CONTROL SYSTEMS LAB	24	47	2
15U41A0217	RT31016	IPR & PATENTS	20	24	2
15U41A0217	RT31021	ELECTRICAL MEASUREMENTS	22	28	3
15U41A0217	RT31022	MEFA	26	31	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0217	RT31023	POWER SYSTEMS-II	28	51	3
15U41A0217	RT31024	ELECTRICAL MACHINES-III	22	24	3
15U41A0217	RT31025	POWER ELECTRONICS	21	44	3
15U41A0217	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	11	0
15U41A0217	RT31027	ELECTRICAL MACHINES-II LAB	18	32	2
15U41A0217	RT31028	CONTROL SYSTEMS LAB	24	44	2
15U41A0218	RT31016	IPR & PATENTS	26	46	2
15U41A0218	RT31021	ELECTRICAL MEASUREMENTS	21	31	3
15U41A0218	RT31022	MEFA	22	39	3
15U41A0218	RT31023	POWER SYSTEMS-II	13	27	3
15U41A0218	RT31024	ELECTRICAL MACHINES-III	23	30	3
15U41A0218	RT31025	POWER ELECTRONICS	19	5	0
15U41A0218	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	34	3
15U41A0218	RT31027	ELECTRICAL MACHINES-II LAB	14	32	2
15U41A0218	RT31028	CONTROL SYSTEMS LAB	24	45	2
15U41A0219	RT31016	IPR & PATENTS	24	37	2
15U41A0219	RT31021	ELECTRICAL MEASUREMENTS	19	50	3
15U41A0219	RT31022	MEFA	19	49	3
15U41A0219	RT31023	POWER SYSTEMS-II	21	24	3
15U41A0219	RT31024	ELECTRICAL MACHINES-III	16	35	3
15U41A0219	RT31025	POWER ELECTRONICS	16	36	3
15U41A0219	RT31026	LINEAR & DIGITAL IC APPLICATIONS	14	7	0
15U41A0219	RT31027	ELECTRICAL MACHINES-II LAB	15	32	2
15U41A0219	RT31028	CONTROL SYSTEMS LAB	19	46	2
15U41A0220	RT31016	IPR & PATENTS	26	28	2
15U41A0220	RT31021	ELECTRICAL MEASUREMENTS	21	10	0
15U41A0220	RT31022	MEFA	25	49	3
15U41A0220	RT31023	POWER SYSTEMS-II	21	31	3
15U41A0220	RT31024	ELECTRICAL MACHINES-III	24	25	3
15U41A0220	RT31025	POWER ELECTRONICS	22	48	3
15U41A0220	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	34	3
15U41A0220	RT31027	ELECTRICAL MACHINES-II LAB	17	34	2
15U41A0220	RT31028	CONTROL SYSTEMS LAB	24	47	2
15U41A0221	RT31016	IPR & PATENTS	25	28	2
15U41A0221	RT31021	ELECTRICAL MEASUREMENTS	21	30	3
15U41A0221	RT31022	MEFA	24	33	3
15U41A0221	RT31023	POWER SYSTEMS-II	20	35	3
15U41A0221	RT31024	ELECTRICAL MACHINES-III	16	24	3
15U41A0221	RT31025	POWER ELECTRONICS	20	47	3
15U41A0221	RT31026	LINEAR & DIGITAL IC APPLICATIONS	14	8	0
15U41A0221	RT31027	ELECTRICAL MACHINES-II LAB	19	43	2
15U41A0221	RT31028	CONTROL SYSTEMS LAB	25	47	2
15U41A0222	RT31016	IPR & PATENTS	25	42	2
15U41A0222	RT31021	ELECTRICAL MEASUREMENTS	29	29	3
15U41A0222	RT31022	MEFA	27	36	3
15U41A0222	RT31023	POWER SYSTEMS-II	24	48	3
15U41A0222	RT31024	ELECTRICAL MACHINES-III	24	38	3
15U41A0222	RT31025	POWER ELECTRONICS	24	24	3
15U41A0222	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	39	3
15U41A0222	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0222	RT31028	CONTROL SYSTEMS LAB	25	49	2
15U41A0223	RT31016	IPR & PATENTS	23	37	2
15U41A0223	RT31021	ELECTRICAL MEASUREMENTS	20	47	3
15U41A0223	RT31022	MEFA	23	54	3
15U41A0223	RT31023	POWER SYSTEMS-II	21	28	3
15U41A0223	RT31024	ELECTRICAL MACHINES-III	22	42	3
15U41A0223	RT31025	POWER ELECTRONICS	19	36	3
15U41A0223	RT31026	LINEAR & DIGITAL IC APPLICATIONS	19	15	0
15U41A0223	RT31027	ELECTRICAL MACHINES-II LAB	22	39	2
15U41A0223	RT31028	CONTROL SYSTEMS LAB	22	43	2
15U41A0224	RT31016	IPR & PATENTS	20	27	2
15U41A0224	RT31021	ELECTRICAL MEASUREMENTS	16	26	3
15U41A0224	RT31022	MEFA	23	42	3
15U41A0224	RT31023	POWER SYSTEMS-II	23	42	3
15U41A0224	RT31024	ELECTRICAL MACHINES-III	20	15	0
15U41A0224	RT31025	POWER ELECTRONICS	20	44	3
15U41A0224	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	26	3
15U41A0224	RT31027	ELECTRICAL MACHINES-II LAB	15	34	2
15U41A0224	RT31028	CONTROL SYSTEMS LAB	24	44	2
15U41A0225	RT31016	IPR & PATENTS	20	28	2
15U41A0225	RT31021	ELECTRICAL MEASUREMENTS	16	27	3
15U41A0225	RT31022	MEFA	25	38	3
15U41A0225	RT31023	POWER SYSTEMS-II	22	34	3
15U41A0225	RT31024	ELECTRICAL MACHINES-III	17	12	0
15U41A0225	RT31025	POWER ELECTRONICS	19	40	3
15U41A0225	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	16	0
15U41A0225	RT31027	ELECTRICAL MACHINES-II LAB	15	43	2
15U41A0225	RT31028	CONTROL SYSTEMS LAB	20	45	2
15U41A0226	RT31016	IPR & PATENTS	21	48	2
15U41A0226	RT31021	ELECTRICAL MEASUREMENTS	15	29	3
15U41A0226	RT31022	MEFA	24	33	3
15U41A0226	RT31023	POWER SYSTEMS-II	11	3	0
15U41A0226	RT31024	ELECTRICAL MACHINES-III	14	18	0
15U41A0226	RT31025	POWER ELECTRONICS	17	8	0
15U41A0226	RT31026	LINEAR & DIGITAL IC APPLICATIONS	12	16	0
15U41A0226	RT31027	ELECTRICAL MACHINES-II LAB	18	36	2
15U41A0226	RT31028	CONTROL SYSTEMS LAB	23	41	2
15U41A0402	RT31016	IPR & PATENTS	22	28	2
15U41A0402	RT31041	PULSE & DIGITAL CIRCUITS	20	24	3
15U41A0402	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	16	15	0
15U41A0402	RT31043	CONTROL SYSTEMS	12	14	0
15U41A0402	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	27	3
15U41A0402	RT31045	ANTENNAS AND WAVE PROPAGATION	16	9	0
15U41A0402	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	38	2
15U41A0402	RT31048	LICA LAB	15	30	2
15U41A0402	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	10	27	2
15U41A0403	RT31016	IPR & PATENTS	26	26	2
15U41A0403	RT31041	PULSE & DIGITAL CIRCUITS	10	0	0
15U41A0403	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	19	0	0
15U41A0403	RT31043	CONTROL SYSTEMS	17	19	0

Htno	Subcode	Subname	Internal	External	Credits
15U41A0403	RT31044	DIGITAL SYSTEM DESIGN & DICA	12	3	0
15U41A0403	RT31045	ANTENNAS AND WAVE PROPAGATION	10	2	0
15U41A0403	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	38	2
15U41A0403	RT31048	LICA LAB	22	35	2
15U41A0403	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	19	27	2
15U41A0404	RT31016	IPR & PATENTS	29	31	2
15U41A0404	RT31041	PULSE & DIGITAL CIRCUITS	26	30	3
15U41A0404	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	25	42	3
15U41A0404	RT31043	CONTROL SYSTEMS	22	36	3
15U41A0404	RT31044	DIGITAL SYSTEM DESIGN & DICA	18	25	3
15U41A0404	RT31045	ANTENNAS AND WAVE PROPAGATION	24	27	3
15U41A0404	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	45	2
15U41A0404	RT31048	LICA LAB	25	40	2
15U41A0404	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	35	2
15U41A0405	RT31016	IPR & PATENTS	26	48	2
15U41A0405	RT31041	PULSE & DIGITAL CIRCUITS	29	32	3
15U41A0405	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	48	3
15U41A0405	RT31043	CONTROL SYSTEMS	25	27	3
15U41A0405	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	48	3
15U41A0405	RT31045	ANTENNAS AND WAVE PROPAGATION	25	7	0
15U41A0405	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	45	2
15U41A0405	RT31048	LICA LAB	24	44	2
15U41A0405	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	38	2
15U41A0406	RT31016	IPR & PATENTS	28	47	2
15U41A0406	RT31041	PULSE & DIGITAL CIRCUITS	28	49	3
15U41A0406	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	42	3
15U41A0406	RT31043	CONTROL SYSTEMS	27	35	3
15U41A0406	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	47	3
15U41A0406	RT31045	ANTENNAS AND WAVE PROPAGATION	27	56	3
15U41A0406	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	47	2
15U41A0406	RT31048	LICA LAB	25	47	2
15U41A0406	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	48	2
15U41A0407	RT31016	IPR & PATENTS	23	34	2
15U41A0407	RT31041	PULSE & DIGITAL CIRCUITS	27	33	3
15U41A0407	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	36	3
15U41A0407	RT31043	CONTROL SYSTEMS	26	30	3
15U41A0407	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	29	3
15U41A0407	RT31045	ANTENNAS AND WAVE PROPAGATION	25	27	3
15U41A0407	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	40	2
15U41A0407	RT31048	LICA LAB	23	42	2
15U41A0407	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	20	35	2
15U41A0408	RT31016	IPR & PATENTS	29	29	2
15U41A0408	RT31041	PULSE & DIGITAL CIRCUITS	26	28	3
15U41A0408	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	31	3
15U41A0408	RT31043	CONTROL SYSTEMS	26	37	3
15U41A0408	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	29	3
15U41A0408	RT31045	ANTENNAS AND WAVE PROPAGATION	24	27	3
15U41A0408	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	44	2
15U41A0408	RT31048	LICA LAB	25	43	2
15U41A0408	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	42	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0410	RT31016	IPR & PATENTS	29	48	2
15U41A0410	RT31041	PULSE & DIGITAL CIRCUITS	29	38	3
15U41A0410	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	40	3
15U41A0410	RT31043	CONTROL SYSTEMS	28	32	3
15U41A0410	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	58	3
15U41A0410	RT31045	ANTENNAS AND WAVE PROPAGATION	26	15	0
15U41A0410	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	49	2
15U41A0410	RT31048	LICA LAB	25	50	2
15U41A0410	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	47	2
15U41A0411	RT31016	IPR & PATENTS	29	31	2
15U41A0411	RT31041	PULSE & DIGITAL CIRCUITS	28	43	3
15U41A0411	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	25	34	3
15U41A0411	RT31043	CONTROL SYSTEMS	29	28	3
15U41A0411	RT31044	DIGITAL SYSTEM DESIGN & DICA	26	35	3
15U41A0411	RT31045	ANTENNAS AND WAVE PROPAGATION	24	36	3
15U41A0411	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	47	2
15U41A0411	RT31048	LICA LAB	25	47	2
15U41A0411	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	47	2
15U41A0412	RT31016	IPR & PATENTS	26	45	2
15U41A0412	RT31041	PULSE & DIGITAL CIRCUITS	30	55	3
15U41A0412	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	51	3
15U41A0412	RT31043	CONTROL SYSTEMS	30	54	3
15U41A0412	RT31044	DIGITAL SYSTEM DESIGN & DICA	26	52	3
15U41A0412	RT31045	ANTENNAS AND WAVE PROPAGATION	27	44	3
15U41A0412	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
15U41A0412	RT31048	LICA LAB	25	50	2
15U41A0412	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	45	2
15U41A0413	RT31016	IPR & PATENTS	24	15	0
15U41A0413	RT31041	PULSE & DIGITAL CIRCUITS	26	24	3
15U41A0413	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	17	25	3
15U41A0413	RT31043	CONTROL SYSTEMS	22	6	0
15U41A0413	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	14	0
15U41A0413	RT31045	ANTENNAS AND WAVE PROPAGATION	18	12	0
15U41A0413	RT31047	PULSE & DIGITAL CIRCUITS LAB	20	38	2
15U41A0413	RT31048	LICA LAB	22	30	2
15U41A0413	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	17	27	2
15U41A0414	RT31016	IPR & PATENTS	26	40	2
15U41A0414	RT31041	PULSE & DIGITAL CIRCUITS	28	28	3
15U41A0414	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	24	3
15U41A0414	RT31043	CONTROL SYSTEMS	28	25	3
15U41A0414	RT31044	DIGITAL SYSTEM DESIGN & DICA	19	16	0
15U41A0414	RT31045	ANTENNAS AND WAVE PROPAGATION	22	4	0
15U41A0414	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	45	2
15U41A0414	RT31048	LICA LAB	25	41	2
15U41A0414	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	35	2
15U41A0415	RT31016	IPR & PATENTS	29	40	2
15U41A0415	RT31041	PULSE & DIGITAL CIRCUITS	30	59	3
15U41A0415	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	34	3
15U41A0415	RT31043	CONTROL SYSTEMS	29	47	3
15U41A0415	RT31044	DIGITAL SYSTEM DESIGN & DICA	28	31	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0415	RT31045	ANTENNAS AND WAVE PROPAGATION	27	56	3
15U41A0415	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
15U41A0415	RT31048	LICA LAB	25	50	2
15U41A0415	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	50	2
15U41A0416	RT31016	IPR & PATENTS	25	35	2
15U41A0416	RT31041	PULSE & DIGITAL CIRCUITS	25	42	3
15U41A0416	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	24	37	3
15U41A0416	RT31043	CONTROL SYSTEMS	25	26	3
15U41A0416	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	38	3
15U41A0416	RT31045	ANTENNAS AND WAVE PROPAGATION	25	27	3
15U41A0416	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	46	2
15U41A0416	RT31048	LICA LAB	25	43	2
15U41A0416	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	43	2
15U41A0417	RT31016	IPR & PATENTS	29	30	2
15U41A0417	RT31041	PULSE & DIGITAL CIRCUITS	30	26	3
15U41A0417	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	37	3
15U41A0417	RT31043	CONTROL SYSTEMS	30	35	3
15U41A0417	RT31044	DIGITAL SYSTEM DESIGN & DICA	25	28	3
15U41A0417	RT31045	ANTENNAS AND WAVE PROPAGATION	25	24	3
15U41A0417	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	47	2
15U41A0417	RT31048	LICA LAB	23	46	2
15U41A0417	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	44	2
15U41A0418	RT31016	IPR & PATENTS	25	48	2
15U41A0418	RT31041	PULSE & DIGITAL CIRCUITS	29	44	3
15U41A0418	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	49	3
15U41A0418	RT31043	CONTROL SYSTEMS	25	26	3
15U41A0418	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	35	3
15U41A0418	RT31045	ANTENNAS AND WAVE PROPAGATION	24	24	3
15U41A0418	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	50	2
15U41A0418	RT31048	LICA LAB	25	49	2
15U41A0418	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	48	2
15U41A0419	RT31016	IPR & PATENTS	28	36	2
15U41A0419	RT31041	PULSE & DIGITAL CIRCUITS	28	59	3
15U41A0419	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	37	3
15U41A0419	RT31043	CONTROL SYSTEMS	29	46	3
15U41A0419	RT31044	DIGITAL SYSTEM DESIGN & DICA	24	41	3
15U41A0419	RT31045	ANTENNAS AND WAVE PROPAGATION	28	67	3
15U41A0419	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
15U41A0419	RT31048	LICA LAB	25	50	2
15U41A0419	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	48	2
15U41A0420	RT31016	IPR & PATENTS	27	27	2
15U41A0420	RT31041	PULSE & DIGITAL CIRCUITS	29	37	3
15U41A0420	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	33	3
15U41A0420	RT31043	CONTROL SYSTEMS	23	25	3
15U41A0420	RT31044	DIGITAL SYSTEM DESIGN & DICA	17	8	0
15U41A0420	RT31045	ANTENNAS AND WAVE PROPAGATION	25	27	3
15U41A0420	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	43	2
15U41A0420	RT31048	LICA LAB	24	40	2
15U41A0420	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	20	42	2
15U41A0422	RT31016	IPR & PATENTS	25	30	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0422	RT31041	PULSE & DIGITAL CIRCUITS	21	18	0
15U41A0422	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	16	31	3
15U41A0422	RT31043	CONTROL SYSTEMS	20	27	3
15U41A0422	RT31044	DIGITAL SYSTEM DESIGN & DICA	12	28	3
15U41A0422	RT31045	ANTENNAS AND WAVE PROPAGATION	20	19	0
15U41A0422	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	42	2
15U41A0422	RT31048	LICA LAB	24	41	2
15U41A0422	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	32	2
15U41A0423	RT31016	IPR & PATENTS	27	31	2
15U41A0423	RT31041	PULSE & DIGITAL CIRCUITS	29	40	3
15U41A0423	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	21	45	3
15U41A0423	RT31043	CONTROL SYSTEMS	28	36	3
15U41A0423	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	24	3
15U41A0423	RT31045	ANTENNAS AND WAVE PROPAGATION	24	15	0
15U41A0423	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	45	2
15U41A0423	RT31048	LICA LAB	25	44	2
15U41A0423	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	40	2
15U41A0424	RT31016	IPR & PATENTS	26	33	2
15U41A0424	RT31041	PULSE & DIGITAL CIRCUITS	26	49	3
15U41A0424	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	33	3
15U41A0424	RT31043	CONTROL SYSTEMS	26	5	0
15U41A0424	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	44	3
15U41A0424	RT31045	ANTENNAS AND WAVE PROPAGATION	26	42	3
15U41A0424	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	45	2
15U41A0424	RT31048	LICA LAB	24	50	2
15U41A0424	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	42	2
15U41A0425	RT31016	IPR & PATENTS	28	26	2
15U41A0425	RT31041	PULSE & DIGITAL CIRCUITS	26	41	3
15U41A0425	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	33	3
15U41A0425	RT31043	CONTROL SYSTEMS	27	32	3
15U41A0425	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	45	3
15U41A0425	RT31045	ANTENNAS AND WAVE PROPAGATION	26	27	3
15U41A0425	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	45	2
15U41A0425	RT31048	LICA LAB	25	49	2
15U41A0425	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	40	2
15U41A0426	RT31016	IPR & PATENTS	26	34	2
15U41A0426	RT31041	PULSE & DIGITAL CIRCUITS	30	31	3
15U41A0426	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	37	3
15U41A0426	RT31043	CONTROL SYSTEMS	28	38	3
15U41A0426	RT31044	DIGITAL SYSTEM DESIGN & DICA	25	38	3
15U41A0426	RT31045	ANTENNAS AND WAVE PROPAGATION	26	24	3
15U41A0426	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	45	2
15U41A0426	RT31048	LICA LAB	25	46	2
15U41A0426	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	43	2
15U41A0427	RT31016	IPR & PATENTS	23	24	2
15U41A0427	RT31041	PULSE & DIGITAL CIRCUITS	20	26	3
15U41A0427	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	19	40	3
15U41A0427	RT31043	CONTROL SYSTEMS	19	25	3
15U41A0427	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	27	3
15U41A0427	RT31045	ANTENNAS AND WAVE PROPAGATION	23	24	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0427	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	44	2
15U41A0427	RT31048	LICA LAB	23	44	2
15U41A0427	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	44	2
15U41A0428	RT31016	IPR & PATENTS	29	27	2
15U41A0428	RT31041	PULSE & DIGITAL CIRCUITS	12	30	3
15U41A0428	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	17	17	0
15U41A0428	RT31043	CONTROL SYSTEMS	19	0	0
15U41A0428	RT31044	DIGITAL SYSTEM DESIGN & DICA	19	26	3
15U41A0428	RT31045	ANTENNAS AND WAVE PROPAGATION	16	8	0
15U41A0428	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	45	2
15U41A0428	RT31048	LICA LAB	23	48	2
15U41A0428	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	35	2
15U41A0429	RT31016	IPR & PATENTS	26	31	2
15U41A0429	RT31041	PULSE & DIGITAL CIRCUITS	27	44	3
15U41A0429	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	38	3
15U41A0429	RT31043	CONTROL SYSTEMS	29	25	3
15U41A0429	RT31044	DIGITAL SYSTEM DESIGN & DICA	19	43	3
15U41A0429	RT31045	ANTENNAS AND WAVE PROPAGATION	22	17	0
15U41A0429	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	49	2
15U41A0429	RT31048	LICA LAB	24	49	2
15U41A0429	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	42	2
15U41A0430	RT31016	IPR & PATENTS	30	27	2
15U41A0430	RT31041	PULSE & DIGITAL CIRCUITS	29	40	3
15U41A0430	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	48	3
15U41A0430	RT31043	CONTROL SYSTEMS	29	49	3
15U41A0430	RT31044	DIGITAL SYSTEM DESIGN & DICA	23	30	3
15U41A0430	RT31045	ANTENNAS AND WAVE PROPAGATION	23	24	3
15U41A0430	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	49	2
15U41A0430	RT31048	LICA LAB	24	50	2
15U41A0430	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	48	2
15U41A0431	RT31016	IPR & PATENTS	28	30	2
15U41A0431	RT31041	PULSE & DIGITAL CIRCUITS	28	39	3
15U41A0431	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	49	3
15U41A0431	RT31043	CONTROL SYSTEMS	29	26	3
15U41A0431	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	27	3
15U41A0431	RT31045	ANTENNAS AND WAVE PROPAGATION	24	24	3
15U41A0431	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	46	2
15U41A0431	RT31048	LICA LAB	23	42	2
15U41A0431	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	45	2
15U41A0432	RT31016	IPR & PATENTS	28	35	2
15U41A0432	RT31041	PULSE & DIGITAL CIRCUITS	30	44	3
15U41A0432	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	46	3
15U41A0432	RT31043	CONTROL SYSTEMS	30	31	3
15U41A0432	RT31044	DIGITAL SYSTEM DESIGN & DICA	25	42	3
15U41A0432	RT31045	ANTENNAS AND WAVE PROPAGATION	24	36	3
15U41A0432	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	49	2
15U41A0432	RT31048	LICA LAB	25	50	2
15U41A0432	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	48	2
15U41A0433	RT31016	IPR & PATENTS	29	28	2
15U41A0433	RT31041	PULSE & DIGITAL CIRCUITS	30	58	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0433	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	47	3
15U41A0433	RT31043	CONTROL SYSTEMS	30	51	3
15U41A0433	RT31044	DIGITAL SYSTEM DESIGN & DICA	26	51	3
15U41A0433	RT31045	ANTENNAS AND WAVE PROPAGATION	28	42	3
15U41A0433	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
15U41A0433	RT31048	LICA LAB	25	50	2
15U41A0433	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	50	2
15U41A0435	RT31016	IPR & PATENTS	26	29	2
15U41A0435	RT31041	PULSE & DIGITAL CIRCUITS	30	32	3
15U41A0435	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	25	37	3
15U41A0435	RT31043	CONTROL SYSTEMS	28	34	3
15U41A0435	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	25	3
15U41A0435	RT31045	ANTENNAS AND WAVE PROPAGATION	23	24	3
15U41A0435	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	48	2
15U41A0435	RT31048	LICA LAB	23	41	2
15U41A0435	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	45	2
15U41A0436	RT31016	IPR & PATENTS	27	26	2
15U41A0436	RT31041	PULSE & DIGITAL CIRCUITS	21	16	0
15U41A0436	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	21	24	3
15U41A0436	RT31043	CONTROL SYSTEMS	24	12	0
15U41A0436	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	29	3
15U41A0436	RT31045	ANTENNAS AND WAVE PROPAGATION	20	9	0
15U41A0436	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	40	2
15U41A0436	RT31048	LICA LAB	21	40	2
15U41A0436	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	19	33	2
15U41A0438	RT31016	IPR & PATENTS	26	30	2
15U41A0438	RT31041	PULSE & DIGITAL CIRCUITS	29	46	3
15U41A0438	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	25	27	3
15U41A0438	RT31043	CONTROL SYSTEMS	25	29	3
15U41A0438	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	39	3
15U41A0438	RT31045	ANTENNAS AND WAVE PROPAGATION	23	53	3
15U41A0438	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	49	2
15U41A0438	RT31048	LICA LAB	25	49	2
15U41A0438	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	40	2
15U41A0439	RT31016	IPR & PATENTS	25	32	2
15U41A0439	RT31041	PULSE & DIGITAL CIRCUITS	23	31	3
15U41A0439	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	24	36	3
15U41A0439	RT31043	CONTROL SYSTEMS	25	24	3
15U41A0439	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	35	3
15U41A0439	RT31045	ANTENNAS AND WAVE PROPAGATION	23	12	0
15U41A0439	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	46	2
15U41A0439	RT31048	LICA LAB	24	46	2
15U41A0439	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	44	2
15U41A0440	RT31016	IPR & PATENTS	28	29	2
15U41A0440	RT31041	PULSE & DIGITAL CIRCUITS	28	40	3
15U41A0440	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	24	41	3
15U41A0440	RT31043	CONTROL SYSTEMS	27	41	3
15U41A0440	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	29	3
15U41A0440	RT31045	ANTENNAS AND WAVE PROPAGATION	26	30	3
15U41A0440	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	47	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0440	RT31048	LICA LAB	24	41	2
15U41A0440	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	44	2
15U41A0441	RT31016	IPR & PATENTS	23	26	2
15U41A0441	RT31041	PULSE & DIGITAL CIRCUITS	20	19	0
15U41A0441	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	18	34	3
15U41A0441	RT31043	CONTROL SYSTEMS	18	12	0
15U41A0441	RT31044	DIGITAL SYSTEM DESIGN & DICA	17	34	3
15U41A0441	RT31045	ANTENNAS AND WAVE PROPAGATION	20	24	3
15U41A0441	RT31047	PULSE & DIGITAL CIRCUITS LAB	15	38	2
15U41A0441	RT31048	LICA LAB	15	35	2
15U41A0441	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	11	27	2
15U41A0442	RT31016	IPR & PATENTS	26	31	2
15U41A0442	RT31041	PULSE & DIGITAL CIRCUITS	20	30	3
15U41A0442	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	17	0
15U41A0442	RT31043	CONTROL SYSTEMS	25	3	0
15U41A0442	RT31044	DIGITAL SYSTEM DESIGN & DICA	17	15	0
15U41A0442	RT31045	ANTENNAS AND WAVE PROPAGATION	20	36	3
15U41A0442	RT31047	PULSE & DIGITAL CIRCUITS LAB	17	38	2
15U41A0442	RT31048	LICA LAB	15	46	2
15U41A0442	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	10	27	2
15U41A0443	RT31016	IPR & PATENTS	27	38	2
15U41A0443	RT31041	PULSE & DIGITAL CIRCUITS	28	45	3
15U41A0443	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	36	3
15U41A0443	RT31043	CONTROL SYSTEMS	26	50	3
15U41A0443	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	52	3
15U41A0443	RT31045	ANTENNAS AND WAVE PROPAGATION	25	30	3
15U41A0443	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	47	2
15U41A0443	RT31048	LICA LAB	24	45	2
15U41A0443	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	45	2
15U41A0444	RT31016	IPR & PATENTS	25	27	2
15U41A0444	RT31041	PULSE & DIGITAL CIRCUITS	22	8	0
15U41A0444	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	18	25	3
15U41A0444	RT31043	CONTROL SYSTEMS	21	28	3
15U41A0444	RT31044	DIGITAL SYSTEM DESIGN & DICA	10	6	0
15U41A0444	RT31045	ANTENNAS AND WAVE PROPAGATION	20	15	0
15U41A0444	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	42	2
15U41A0444	RT31048	LICA LAB	23	38	2
15U41A0444	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	20	35	2
15U41A0445	RT31016	IPR & PATENTS	18	26	2
15U41A0445	RT31041	PULSE & DIGITAL CIRCUITS	16	27	3
15U41A0445	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	19	16	0
15U41A0445	RT31043	CONTROL SYSTEMS	20	4	0
15U41A0445	RT31044	DIGITAL SYSTEM DESIGN & DICA	17	27	3
15U41A0445	RT31045	ANTENNAS AND WAVE PROPAGATION	21	17	0
15U41A0445	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	40	2
15U41A0445	RT31048	LICA LAB	23	40	2
15U41A0445	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	19	32	2
15U41A0447	RT31016	IPR & PATENTS	22	40	2
15U41A0447	RT31041	PULSE & DIGITAL CIRCUITS	17	33	3
15U41A0447	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	25	33	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0447	RT31043	CONTROL SYSTEMS	21	29	3
15U41A0447	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	47	3
15U41A0447	RT31045	ANTENNAS AND WAVE PROPAGATION	21	39	3
15U41A0447	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	45	2
15U41A0447	RT31048	LICA LAB	15	38	2
15U41A0447	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	43	2
15U41A0448	RT31016	IPR & PATENTS	26	32	2
15U41A0448	RT31041	PULSE & DIGITAL CIRCUITS	25	35	3
15U41A0448	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	33	3
15U41A0448	RT31043	CONTROL SYSTEMS	29	24	3
15U41A0448	RT31044	DIGITAL SYSTEM DESIGN & DICA	23	38	3
15U41A0448	RT31045	ANTENNAS AND WAVE PROPAGATION	27	14	0
15U41A0448	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	46	2
15U41A0448	RT31048	LICA LAB	24	45	2
15U41A0448	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	43	2
15U41A0449	RT31016	IPR & PATENTS	24	35	2
15U41A0449	RT31041	PULSE & DIGITAL CIRCUITS	29	42	3
15U41A0449	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	38	3
15U41A0449	RT31043	CONTROL SYSTEMS	29	52	3
15U41A0449	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	24	3
15U41A0449	RT31045	ANTENNAS AND WAVE PROPAGATION	27	24	3
15U41A0449	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	47	2
15U41A0449	RT31048	LICA LAB	23	49	2
15U41A0449	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	43	2
15U41A0450	RT31016	IPR & PATENTS	25	39	2
15U41A0450	RT31041	PULSE & DIGITAL CIRCUITS	20	15	0
15U41A0450	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	28	3
15U41A0450	RT31043	CONTROL SYSTEMS	22	14	0
15U41A0450	RT31044	DIGITAL SYSTEM DESIGN & DICA	19	26	3
15U41A0450	RT31045	ANTENNAS AND WAVE PROPAGATION	20	24	3
15U41A0450	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	42	2
15U41A0450	RT31048	LICA LAB	23	35	2
15U41A0450	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	38	2
15U41A0451	RT31016	IPR & PATENTS	25	48	2
15U41A0451	RT31041	PULSE & DIGITAL CIRCUITS	30	49	3
15U41A0451	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	52	3
15U41A0451	RT31043	CONTROL SYSTEMS	28	37	3
15U41A0451	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	39	3
15U41A0451	RT31045	ANTENNAS AND WAVE PROPAGATION	27	31	3
15U41A0451	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	48	2
15U41A0451	RT31048	LICA LAB	25	50	2
15U41A0451	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	44	2
15U41A0452	RT31016	IPR & PATENTS	22	24	2
15U41A0452	RT31041	PULSE & DIGITAL CIRCUITS	17	11	0
15U41A0452	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	16	10	0
15U41A0452	RT31043	CONTROL SYSTEMS	20	9	0
15U41A0452	RT31044	DIGITAL SYSTEM DESIGN & DICA	14	30	3
15U41A0452	RT31045	ANTENNAS AND WAVE PROPAGATION	17	7	0
15U41A0452	RT31047	PULSE & DIGITAL CIRCUITS LAB	20	40	2
15U41A0452	RT31048	LICA LAB	23	35	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0452	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	18	32	2
15U41A0501	RT31051	COMPILER DESIGN	27	0	0
15U41A0501	RT31052	DATA COMMUNICATION	25	42	3
15U41A0501	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	50	3
15U41A0501	RT31054	DATABASE MANAGEMENT SYSTEMS	22	34	3
15U41A0501	RT31055	OPERATING SYSTEMS	22	27	3
15U41A0501	RT31056	COMPILER DESIGN LAB	25	47	2
15U41A0501	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	49	2
15U41A0501	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0501	RT31059	SEMINAR	43	0	1
15U41A0502	RT31051	COMPILER DESIGN	24	28	3
15U41A0502	RT31052	DATA COMMUNICATION	25	8	0
15U41A0502	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	19	35	3
15U41A0502	RT31054	DATABASE MANAGEMENT SYSTEMS	13	15	0
15U41A0502	RT31055	OPERATING SYSTEMS	17	12	0
15U41A0502	RT31056	COMPILER DESIGN LAB	24	48	2
15U41A0502	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	45	2
15U41A0502	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0502	RT31059	SEMINAR	50	0	1
15U41A0503	RT31051	COMPILER DESIGN	29	30	3
15U41A0503	RT31052	DATA COMMUNICATION	26	42	3
15U41A0503	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	53	3
15U41A0503	RT31054	DATABASE MANAGEMENT SYSTEMS	26	31	3
15U41A0503	RT31055	OPERATING SYSTEMS	23	52	3
15U41A0503	RT31056	COMPILER DESIGN LAB	25	45	2
15U41A0503	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	48	2
15U41A0503	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0503	RT31059	SEMINAR	45	0	1
15U41A0504	RT31051	COMPILER DESIGN	29	43	3
15U41A0504	RT31052	DATA COMMUNICATION	27	39	3
15U41A0504	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	39	3
15U41A0504	RT31054	DATABASE MANAGEMENT SYSTEMS	26	30	3
15U41A0504	RT31055	OPERATING SYSTEMS	26	47	3
15U41A0504	RT31056	COMPILER DESIGN LAB	24	46	2
15U41A0504	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0504	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	50	2
15U41A0504	RT31059	SEMINAR	50	0	1
15U41A0505	RT31051	COMPILER DESIGN	30	15	0
15U41A0505	RT31052	DATA COMMUNICATION	26	29	3
15U41A0505	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	31	3
15U41A0505	RT31054	DATABASE MANAGEMENT SYSTEMS	25	26	3
15U41A0505	RT31055	OPERATING SYSTEMS	27	28	3
15U41A0505	RT31056	COMPILER DESIGN LAB	24	44	2
15U41A0505	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	48	2
15U41A0505	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	49	2
15U41A0505	RT31059	SEMINAR	44	0	1
15U41A0506	RT31051	COMPILER DESIGN	26	46	3
15U41A0506	RT31052	DATA COMMUNICATION	22	24	3
15U41A0506	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	36	3
15U41A0506	RT31054	DATABASE MANAGEMENT SYSTEMS	25	33	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0506	RT31055	OPERATING SYSTEMS	26	26	3
15U41A0506	RT31056	COMPILER DESIGN LAB	22	45	2
15U41A0506	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	49	2
15U41A0506	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0506	RT31059	SEMINAR	43	0	1
15U41A0507	RT31051	COMPILER DESIGN	28	24	3
15U41A0507	RT31052	DATA COMMUNICATION	24	33	3
15U41A0507	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	49	3
15U41A0507	RT31054	DATABASE MANAGEMENT SYSTEMS	26	31	3
15U41A0507	RT31055	OPERATING SYSTEMS	25	52	3
15U41A0507	RT31056	COMPILER DESIGN LAB	23	46	2
15U41A0507	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A0507	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	49	2
15U41A0507	RT31059	SEMINAR	48	0	1
15U41A0508	RT31051	COMPILER DESIGN	29	13	0
15U41A0508	RT31052	DATA COMMUNICATION	28	33	3
15U41A0508	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	36	3
15U41A0508	RT31054	DATABASE MANAGEMENT SYSTEMS	26	34	3
15U41A0508	RT31055	OPERATING SYSTEMS	27	47	3
15U41A0508	RT31056	COMPILER DESIGN LAB	22	46	2
15U41A0508	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0508	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0508	RT31059	SEMINAR	47	0	1
15U41A0509	RT31051	COMPILER DESIGN	29	24	3
15U41A0509	RT31052	DATA COMMUNICATION	29	46	3
15U41A0509	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	31	3
15U41A0509	RT31054	DATABASE MANAGEMENT SYSTEMS	26	31	3
15U41A0509	RT31055	OPERATING SYSTEMS	26	47	3
15U41A0509	RT31056	COMPILER DESIGN LAB	24	43	2
15U41A0509	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A0509	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	50	2
15U41A0509	RT31059	SEMINAR	45	0	1
15U41A0510	RT31051	COMPILER DESIGN	21	37	3
15U41A0510	RT31052	DATA COMMUNICATION	13	10	0
15U41A0510	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	35	3
15U41A0510	RT31054	DATABASE MANAGEMENT SYSTEMS	19	30	3
15U41A0510	RT31055	OPERATING SYSTEMS	15	11	0
15U41A0510	RT31056	COMPILER DESIGN LAB	21	44	2
15U41A0510	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	44	2
15U41A0510	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0510	RT31059	SEMINAR	45	0	1
15U41A0511	RT31051	COMPILER DESIGN	24	15	0
15U41A0511	RT31052	DATA COMMUNICATION	20	31	3
15U41A0511	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	17	0
15U41A0511	RT31054	DATABASE MANAGEMENT SYSTEMS	20	24	3
15U41A0511	RT31055	OPERATING SYSTEMS	17	36	3
15U41A0511	RT31056	COMPILER DESIGN LAB	22	45	2
15U41A0511	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A0511	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0511	RT31059	SEMINAR	45	0	1

Htno	Subcode	Subname	Internal	External	Credits
15U41A0512	RT31051	COMPILER DESIGN	26	17	0
15U41A0512	RT31052	DATA COMMUNICATION	26	3	0
15U41A0512	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	18	27	3
15U41A0512	RT31054	DATABASE MANAGEMENT SYSTEMS	19	32	3
15U41A0512	RT31055	OPERATING SYSTEMS	19	26	3
15U41A0512	RT31056	COMPILER DESIGN LAB	21	41	2
15U41A0512	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	44	2
15U41A0512	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0512	RT31059	SEMINAR	42	0	1
15U41A0513	RT31051	COMPILER DESIGN	29	14	0
15U41A0513	RT31052	DATA COMMUNICATION	24	48	3
15U41A0513	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	35	3
15U41A0513	RT31054	DATABASE MANAGEMENT SYSTEMS	21	31	3
15U41A0513	RT31055	OPERATING SYSTEMS	23	43	3
15U41A0513	RT31056	COMPILER DESIGN LAB	22	40	2
15U41A0513	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A0513	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	46	2
15U41A0513	RT31059	SEMINAR	45	0	1
15U41A0514	RT31051	COMPILER DESIGN	23	40	3
15U41A0514	RT31052	DATA COMMUNICATION	15	11	0
15U41A0514	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	38	3
15U41A0514	RT31054	DATABASE MANAGEMENT SYSTEMS	21	24	3
15U41A0514	RT31055	OPERATING SYSTEMS	20	24	3
15U41A0514	RT31056	COMPILER DESIGN LAB	21	43	2
15U41A0514	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	45	2
15U41A0514	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	46	2
15U41A0514	RT31059	SEMINAR	48	0	1
15U41A0515	RT31051	COMPILER DESIGN	22	17	0
15U41A0515	RT31052	DATA COMMUNICATION	20	33	3
15U41A0515	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	15	47	3
15U41A0515	RT31054	DATABASE MANAGEMENT SYSTEMS	15	8	0
15U41A0515	RT31055	OPERATING SYSTEMS	20	46	3
15U41A0515	RT31056	COMPILER DESIGN LAB	19	40	2
15U41A0515	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	45	2
15U41A0515	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	45	2
15U41A0515	RT31059	SEMINAR	43	0	1
15U41A0516	RT31051	COMPILER DESIGN	23	30	3
15U41A0516	RT31052	DATA COMMUNICATION	13	10	0
15U41A0516	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	15	32	3
15U41A0516	RT31054	DATABASE MANAGEMENT SYSTEMS	12	34	3
15U41A0516	RT31055	OPERATING SYSTEMS	18	28	3
15U41A0516	RT31056	COMPILER DESIGN LAB	23	45	2
15U41A0516	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	48	2
15U41A0516	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	49	2
15U41A0516	RT31059	SEMINAR	50	0	1
15U41A0517	RT31051	COMPILER DESIGN	19	12	0
15U41A0517	RT31052	DATA COMMUNICATION	19	28	3
15U41A0517	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	15	11	0
15U41A0517	RT31054	DATABASE MANAGEMENT SYSTEMS	20	12	0
15U41A0517	RT31055	OPERATING SYSTEMS	17	32	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0517	RT31056	COMPILER DESIGN LAB	21	43	2
15U41A0517	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	46	2
15U41A0517	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	45	2
15U41A0517	RT31059	SEMINAR	45	0	1
15U41A0518	RT31051	COMPILER DESIGN	27	39	3
15U41A0518	RT31052	DATA COMMUNICATION	18	18	0
15U41A0518	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	27	3
15U41A0518	RT31054	DATABASE MANAGEMENT SYSTEMS	21	25	3
15U41A0518	RT31055	OPERATING SYSTEMS	22	43	3
15U41A0518	RT31056	COMPILER DESIGN LAB	22	42	2
15U41A0518	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	48	2
15U41A0518	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	48	2
15U41A0518	RT31059	SEMINAR	45	0	1
15U41A0519	RT31051	COMPILER DESIGN	29	35	3
15U41A0519	RT31052	DATA COMMUNICATION	26	47	3
15U41A0519	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	64	3
15U41A0519	RT31054	DATABASE MANAGEMENT SYSTEMS	29	32	3
15U41A0519	RT31055	OPERATING SYSTEMS	27	45	3
15U41A0519	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A0519	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0519	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0519	RT31059	SEMINAR	50	0	1
15U41A0520	RT31051	COMPILER DESIGN	25	30	3
15U41A0520	RT31052	DATA COMMUNICATION	23	6	0
15U41A0520	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	27	3
15U41A0520	RT31054	DATABASE MANAGEMENT SYSTEMS	22	32	3
15U41A0520	RT31055	OPERATING SYSTEMS	20	18	0
15U41A0520	RT31056	COMPILER DESIGN LAB	23	42	2
15U41A0520	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	46	2
15U41A0520	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	48	2
15U41A0520	RT31059	SEMINAR	49	0	1
15U41A0521	RT31051	COMPILER DESIGN	21	15	0
15U41A0521	RT31052	DATA COMMUNICATION	21	37	3
15U41A0521	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	16	17	0
15U41A0521	RT31054	DATABASE MANAGEMENT SYSTEMS	20	12	0
15U41A0521	RT31055	OPERATING SYSTEMS	21	37	3
15U41A0521	RT31056	COMPILER DESIGN LAB	21	41	2
15U41A0521	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	42	2
15U41A0521	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0521	RT31059	SEMINAR	42	0	1
15U41A0522	RT31051	COMPILER DESIGN	26	48	3
15U41A0522	RT31052	DATA COMMUNICATION	25	26	3
15U41A0522	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	30	3
15U41A0522	RT31054	DATABASE MANAGEMENT SYSTEMS	26	37	3
15U41A0522	RT31055	OPERATING SYSTEMS	23	43	3
15U41A0522	RT31056	COMPILER DESIGN LAB	22	44	2
15U41A0522	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	46	2
15U41A0522	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0522	RT31059	SEMINAR	48	0	1
15U41A0523	RT31051	COMPILER DESIGN	28	16	0

Htno	Subcode	Subname	Internal	External	Credits
15U41A0523	RT31052	DATA COMMUNICATION	22	38	3
15U41A0523	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	43	3
15U41A0523	RT31054	DATABASE MANAGEMENT SYSTEMS	22	13	0
15U41A0523	RT31055	OPERATING SYSTEMS	23	40	3
15U41A0523	RT31056	COMPILER DESIGN LAB	22	47	2
15U41A0523	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0523	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0523	RT31059	SEMINAR	46	0	1
15U41A0524	RT31051	COMPILER DESIGN	21	0	0
15U41A0524	RT31052	DATA COMMUNICATION	14	14	0
15U41A0524	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	8	4	0
15U41A0524	RT31054	DATABASE MANAGEMENT SYSTEMS	17	15	0
15U41A0524	RT31055	OPERATING SYSTEMS	15	5	0
15U41A0524	RT31056	COMPILER DESIGN LAB	20	37	2
15U41A0524	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	45	2
15U41A0524	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	21	45	2
15U41A0524	RT31059	SEMINAR	40	0	1
15U41A0525	RT31051	COMPILER DESIGN	19	36	3
15U41A0525	RT31052	DATA COMMUNICATION	20	42	3
15U41A0525	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	16	34	3
15U41A0525	RT31054	DATABASE MANAGEMENT SYSTEMS	18	17	0
15U41A0525	RT31055	OPERATING SYSTEMS	14	35	3
15U41A0525	RT31056	COMPILER DESIGN LAB	22	43	2
15U41A0525	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	43	2
15U41A0525	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	45	2
15U41A0525	RT31059	SEMINAR	40	0	1
15U41A0526	RT31051	COMPILER DESIGN	17	30	3
15U41A0526	RT31052	DATA COMMUNICATION	11	16	0
15U41A0526	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	11	29	3
15U41A0526	RT31054	DATABASE MANAGEMENT SYSTEMS	5	21	0
15U41A0526	RT31055	OPERATING SYSTEMS	12	36	3
15U41A0526	RT31056	COMPILER DESIGN LAB	19	36	2
15U41A0526	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	45	2
15U41A0526	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	45	2
15U41A0526	RT31059	SEMINAR	40	0	1
15U41A0527	RT31051	COMPILER DESIGN	24	34	3
15U41A0527	RT31052	DATA COMMUNICATION	27	33	3
15U41A0527	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	53	3
15U41A0527	RT31054	DATABASE MANAGEMENT SYSTEMS	25	37	3
15U41A0527	RT31055	OPERATING SYSTEMS	23	43	3
15U41A0527	RT31056	COMPILER DESIGN LAB	17	35	2
15U41A0527	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	45	2
15U41A0527	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A0527	RT31059	SEMINAR	49	0	1
15U41A0528	RT31051	COMPILER DESIGN	21	8	0
15U41A0528	RT31052	DATA COMMUNICATION	22	0	0
15U41A0528	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	18	7	0
15U41A0528	RT31054	DATABASE MANAGEMENT SYSTEMS	27	28	3
15U41A0528	RT31055	OPERATING SYSTEMS	24	7	0
15U41A0528	RT31056	COMPILER DESIGN LAB	23	40	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0528	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	45	2
15U41A0528	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0528	RT31059	SEMINAR	40	0	1
15U41A0530	RT31051	COMPILER DESIGN	27	60	3
15U41A0530	RT31052	DATA COMMUNICATION	26	55	3
15U41A0530	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	47	3
15U41A0530	RT31054	DATABASE MANAGEMENT SYSTEMS	27	49	3
15U41A0530	RT31055	OPERATING SYSTEMS	29	47	3
15U41A0530	RT31056	COMPILER DESIGN LAB	22	42	2
15U41A0530	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0530	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0530	RT31059	SEMINAR	50	0	1
15U41A0531	RT31051	COMPILER DESIGN	28	42	3
15U41A0531	RT31052	DATA COMMUNICATION	25	34	3
15U41A0531	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	44	3
15U41A0531	RT31054	DATABASE MANAGEMENT SYSTEMS	22	56	3
15U41A0531	RT31055	OPERATING SYSTEMS	28	49	3
15U41A0531	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A0531	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0531	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0531	RT31059	SEMINAR	48	0	1
15U41A0532	RT31051	COMPILER DESIGN	29	27	3
15U41A0532	RT31052	DATA COMMUNICATION	26	43	3
15U41A0532	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	51	3
15U41A0532	RT31054	DATABASE MANAGEMENT SYSTEMS	24	37	3
15U41A0532	RT31055	OPERATING SYSTEMS	25	44	3
15U41A0532	RT31056	COMPILER DESIGN LAB	24	45	2
15U41A0532	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	48	2
15U41A0532	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	49	2
15U41A0532	RT31059	SEMINAR	47	0	1
15U41A0533	RT31051	COMPILER DESIGN	26	24	3
15U41A0533	RT31052	DATA COMMUNICATION	23	24	3
15U41A0533	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	27	3
15U41A0533	RT31054	DATABASE MANAGEMENT SYSTEMS	24	33	3
15U41A0533	RT31055	OPERATING SYSTEMS	19	30	3
15U41A0533	RT31056	COMPILER DESIGN LAB	22	41	2
15U41A0533	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	46	2
15U41A0533	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0533	RT31059	SEMINAR	49	0	1
15U41A0534	RT31051	COMPILER DESIGN	20	14	0
15U41A0534	RT31052	DATA COMMUNICATION	13	37	3
15U41A0534	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	35	3
15U41A0534	RT31054	DATABASE MANAGEMENT SYSTEMS	13	27	3
15U41A0534	RT31055	OPERATING SYSTEMS	16	30	3
15U41A0534	RT31056	COMPILER DESIGN LAB	20	44	2
15U41A0534	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	44	2
15U41A0534	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	45	2
15U41A0534	RT31059	SEMINAR	45	0	1
15U41A0535	RT31051	COMPILER DESIGN	26	44	3
15U41A0535	RT31052	DATA COMMUNICATION	22	29	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0535	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	38	3
15U41A0535	RT31054	DATABASE MANAGEMENT SYSTEMS	23	41	3
15U41A0535	RT31055	OPERATING SYSTEMS	22	40	3
15U41A0535	RT31056	COMPILER DESIGN LAB	22	48	2
15U41A0535	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	46	2
15U41A0535	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0535	RT31059	SEMINAR	45	0	1
15U41A0536	RT31051	COMPILER DESIGN	25	24	3
15U41A0536	RT31052	DATA COMMUNICATION	18	27	3
15U41A0536	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	42	3
15U41A0536	RT31054	DATABASE MANAGEMENT SYSTEMS	19	16	0
15U41A0536	RT31055	OPERATING SYSTEMS	19	31	3
15U41A0536	RT31056	COMPILER DESIGN LAB	22	45	2
15U41A0536	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	46	2
15U41A0536	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0536	RT31059	SEMINAR	44	0	1
15U41A0537	RT31051	COMPILER DESIGN	30	47	3
15U41A0537	RT31052	DATA COMMUNICATION	29	38	3
15U41A0537	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	39	3
15U41A0537	RT31054	DATABASE MANAGEMENT SYSTEMS	22	29	3
15U41A0537	RT31055	OPERATING SYSTEMS	27	30	3
15U41A0537	RT31056	COMPILER DESIGN LAB	25	49	2
15U41A0537	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	49	2
15U41A0537	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0537	RT31059	SEMINAR	47	0	1
15U41A0538	RT31051	COMPILER DESIGN	27	38	3
15U41A0538	RT31052	DATA COMMUNICATION	26	48	3
15U41A0538	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	57	3
15U41A0538	RT31054	DATABASE MANAGEMENT SYSTEMS	20	33	3
15U41A0538	RT31055	OPERATING SYSTEMS	25	41	3
15U41A0538	RT31056	COMPILER DESIGN LAB	22	43	2
15U41A0538	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A0538	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0538	RT31059	SEMINAR	48	0	1
15U41A0539	RT31051	COMPILER DESIGN	26	39	3
15U41A0539	RT31052	DATA COMMUNICATION	25	24	3
15U41A0539	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	34	3
15U41A0539	RT31054	DATABASE MANAGEMENT SYSTEMS	19	31	3
15U41A0539	RT31055	OPERATING SYSTEMS	23	33	3
15U41A0539	RT31056	COMPILER DESIGN LAB	22	47	2
15U41A0539	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A0539	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	48	2
15U41A0539	RT31059	SEMINAR	44	0	1
15U41A0540	RT31051	COMPILER DESIGN	20	6	0
15U41A0540	RT31052	DATA COMMUNICATION	20	17	0
15U41A0540	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	33	3
15U41A0540	RT31054	DATABASE MANAGEMENT SYSTEMS	21	8	0
15U41A0540	RT31055	OPERATING SYSTEMS	18	27	3
15U41A0540	RT31056	COMPILER DESIGN LAB	24	44	2
15U41A0540	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	45	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0540	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	45	2
15U41A0540	RT31059	SEMINAR	45	0	1
15U41A0541	RT31051	COMPILER DESIGN	29	39	3
15U41A0541	RT31052	DATA COMMUNICATION	28	43	3
15U41A0541	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	39	3
15U41A0541	RT31054	DATABASE MANAGEMENT SYSTEMS	29	40	3
15U41A0541	RT31055	OPERATING SYSTEMS	28	41	3
15U41A0541	RT31056	COMPILER DESIGN LAB	23	46	2
15U41A0541	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0541	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0541	RT31059	SEMINAR	44	0	1
15U41A0542	RT31051	COMPILER DESIGN	26	17	0
15U41A0542	RT31052	DATA COMMUNICATION	18	47	3
15U41A0542	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	40	3
15U41A0542	RT31054	DATABASE MANAGEMENT SYSTEMS	20	24	3
15U41A0542	RT31055	OPERATING SYSTEMS	20	37	3
15U41A0542	RT31056	COMPILER DESIGN LAB	22	44	2
15U41A0542	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	46	2
15U41A0542	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	48	2
15U41A0542	RT31059	SEMINAR	40	0	1
15U41A0543	RT31051	COMPILER DESIGN	25	46	3
15U41A0543	RT31052	DATA COMMUNICATION	23	37	3
15U41A0543	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	36	3
15U41A0543	RT31054	DATABASE MANAGEMENT SYSTEMS	26	42	3
15U41A0543	RT31055	OPERATING SYSTEMS	27	53	3
15U41A0543	RT31056	COMPILER DESIGN LAB	21	46	2
15U41A0543	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A0543	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0543	RT31059	SEMINAR	47	0	1
15U41A0544	RT31051	COMPILER DESIGN	26	14	0
15U41A0544	RT31052	DATA COMMUNICATION	19	17	0
15U41A0544	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	15	38	3
15U41A0544	RT31054	DATABASE MANAGEMENT SYSTEMS	18	12	0
15U41A0544	RT31055	OPERATING SYSTEMS	18	31	3
15U41A0544	RT31056	COMPILER DESIGN LAB	20	42	2
15U41A0544	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	47	2
15U41A0544	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0544	RT31059	SEMINAR	49	0	1
15U41A0545	RT31051	COMPILER DESIGN	19	10	0
15U41A0545	RT31052	DATA COMMUNICATION	21	3	0
15U41A0545	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	28	3
15U41A0545	RT31054	DATABASE MANAGEMENT SYSTEMS	20	30	3
15U41A0545	RT31055	OPERATING SYSTEMS	22	17	0
15U41A0545	RT31056	COMPILER DESIGN LAB	19	41	2
15U41A0545	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	45	2
15U41A0545	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0545	RT31059	SEMINAR	44	0	1
15U41A0546	RT31051	COMPILER DESIGN	20	15	0
15U41A0546	RT31052	DATA COMMUNICATION	24	48	3
15U41A0546	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	19	47	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0546	RT31054	DATABASE MANAGEMENT SYSTEMS	23	29	3
15U41A0546	RT31055	OPERATING SYSTEMS	18	44	3
15U41A0546	RT31056	COMPILER DESIGN LAB	22	42	2
15U41A0546	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	45	2
15U41A0546	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0546	RT31059	SEMINAR	42	0	1
15U41A0547	RT31051	COMPILER DESIGN	23	13	0
15U41A0547	RT31052	DATA COMMUNICATION	19	26	3
15U41A0547	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	19	37	3
15U41A0547	RT31054	DATABASE MANAGEMENT SYSTEMS	21	38	3
15U41A0547	RT31055	OPERATING SYSTEMS	19	41	3
15U41A0547	RT31056	COMPILER DESIGN LAB	21	41	2
15U41A0547	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	48	2
15U41A0547	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0547	RT31059	SEMINAR	46	0	1
15U41A0548	RT31051	COMPILER DESIGN	28	25	3
15U41A0548	RT31052	DATA COMMUNICATION	22	27	3
15U41A0548	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	43	3
15U41A0548	RT31054	DATABASE MANAGEMENT SYSTEMS	27	17	0
15U41A0548	RT31055	OPERATING SYSTEMS	25	47	3
15U41A0548	RT31056	COMPILER DESIGN LAB	23	-1	0
15U41A0548	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	-1	0
15U41A0548	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	-1	0
15U41A0548	RT31059	SEMINAR	43	0	1
15U41A0549	RT31051	COMPILER DESIGN	21	8	0
15U41A0549	RT31052	DATA COMMUNICATION	23	16	0
15U41A0549	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	12	0
15U41A0549	RT31054	DATABASE MANAGEMENT SYSTEMS	20	19	0
15U41A0549	RT31055	OPERATING SYSTEMS	21	12	0
15U41A0549	RT31056	COMPILER DESIGN LAB	22	42	2
15U41A0549	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	46	2
15U41A0549	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	46	2
15U41A0549	RT31059	SEMINAR	40	0	1
15U41A0550	RT31051	COMPILER DESIGN	24	36	3
15U41A0550	RT31052	DATA COMMUNICATION	23	51	3
15U41A0550	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	54	3
15U41A0550	RT31054	DATABASE MANAGEMENT SYSTEMS	26	40	3
15U41A0550	RT31055	OPERATING SYSTEMS	25	37	3
15U41A0550	RT31056	COMPILER DESIGN LAB	22	43	2
15U41A0550	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A0550	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	50	2
15U41A0550	RT31059	SEMINAR	43	0	1
15U41A0551	RT31051	COMPILER DESIGN	22	10	0
15U41A0551	RT31052	DATA COMMUNICATION	17	14	0
15U41A0551	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	19	28	3
15U41A0551	RT31054	DATABASE MANAGEMENT SYSTEMS	16	24	3
15U41A0551	RT31055	OPERATING SYSTEMS	19	28	3
15U41A0551	RT31056	COMPILER DESIGN LAB	19	37	2
15U41A0551	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	46	2
15U41A0551	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	21	45	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0551	RT31059	SEMINAR	48	0	1
15U41A0552	RT31051	COMPILER DESIGN	23	7	0
15U41A0552	RT31052	DATA COMMUNICATION	29	11	0
15U41A0552	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	53	3
15U41A0552	RT31054	DATABASE MANAGEMENT SYSTEMS	26	31	3
15U41A0552	RT31055	OPERATING SYSTEMS	21	39	3
15U41A0552	RT31056	COMPILER DESIGN LAB	24	42	2
15U41A0552	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	48	2
15U41A0552	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0552	RT31059	SEMINAR	48	0	1
15U41A0553	RT31051	COMPILER DESIGN	23	17	0
15U41A0553	RT31052	DATA COMMUNICATION	26	0	0
15U41A0553	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	31	3
15U41A0553	RT31054	DATABASE MANAGEMENT SYSTEMS	24	37	3
15U41A0553	RT31055	OPERATING SYSTEMS	22	33	3
15U41A0553	RT31056	COMPILER DESIGN LAB	24	42	2
15U41A0553	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	45	2
15U41A0553	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	48	2
15U41A0553	RT31059	SEMINAR	40	0	1
15U41A0554	RT31051	COMPILER DESIGN	29	38	3
15U41A0554	RT31052	DATA COMMUNICATION	30	49	3
15U41A0554	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	45	3
15U41A0554	RT31054	DATABASE MANAGEMENT SYSTEMS	27	37	3
15U41A0554	RT31055	OPERATING SYSTEMS	25	61	3
15U41A0554	RT31056	COMPILER DESIGN LAB	25	43	2
15U41A0554	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	45	2
15U41A0554	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	49	2
15U41A0554	RT31059	SEMINAR	46	0	1
15U41A0555	RT31051	COMPILER DESIGN	27	46	3
15U41A0555	RT31052	DATA COMMUNICATION	25	32	3
15U41A0555	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	39	3
15U41A0555	RT31054	DATABASE MANAGEMENT SYSTEMS	27	58	3
15U41A0555	RT31055	OPERATING SYSTEMS	28	55	3
15U41A0555	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A0555	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0555	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0555	RT31059	SEMINAR	50	0	1
15U41A0557	RT31051	COMPILER DESIGN	28	38	3
15U41A0557	RT31052	DATA COMMUNICATION	29	51	3
15U41A0557	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	54	3
15U41A0557	RT31054	DATABASE MANAGEMENT SYSTEMS	28	47	3
15U41A0557	RT31055	OPERATING SYSTEMS	30	54	3
15U41A0557	RT31056	COMPILER DESIGN LAB	24	46	2
15U41A0557	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0557	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0557	RT31059	SEMINAR	46	0	1
15U41A0558	RT31051	COMPILER DESIGN	27	32	3
15U41A0558	RT31052	DATA COMMUNICATION	20	24	3
15U41A0558	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	31	3
15U41A0558	RT31054	DATABASE MANAGEMENT SYSTEMS	22	30	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0558	RT31055	OPERATING SYSTEMS	24	34	3
15U41A0558	RT31056	COMPILER DESIGN LAB	21	45	2
15U41A0558	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A0558	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	49	2
15U41A0558	RT31059	SEMINAR	40	0	1
15U41A0559	RT31051	COMPILER DESIGN	17	0	0
15U41A0559	RT31052	DATA COMMUNICATION	12	32	3
15U41A0559	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	21	8	0
15U41A0559	RT31054	DATABASE MANAGEMENT SYSTEMS	15	9	0
15U41A0559	RT31055	OPERATING SYSTEMS	20	8	0
15U41A0559	RT31056	COMPILER DESIGN LAB	21	43	2
15U41A0559	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	46	2
15U41A0559	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0559	RT31059	SEMINAR	40	0	1
15U41A0560	RT31051	COMPILER DESIGN	26	52	3
15U41A0560	RT31052	DATA COMMUNICATION	25	31	3
15U41A0560	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	31	3
15U41A0560	RT31054	DATABASE MANAGEMENT SYSTEMS	27	39	3
15U41A0560	RT31055	OPERATING SYSTEMS	26	53	3
15U41A0560	RT31056	COMPILER DESIGN LAB	22	45	2
15U41A0560	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A0560	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	49	2
15U41A0560	RT31059	SEMINAR	49	0	1
15U41A0561	RT31051	COMPILER DESIGN	27	30	3
15U41A0561	RT31052	DATA COMMUNICATION	28	38	3
15U41A0561	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	49	3
15U41A0561	RT31054	DATABASE MANAGEMENT SYSTEMS	25	36	3
15U41A0561	RT31055	OPERATING SYSTEMS	24	38	3
15U41A0561	RT31056	COMPILER DESIGN LAB	20	46	2
15U41A0561	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	49	2
15U41A0561	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	49	2
15U41A0561	RT31059	SEMINAR	45	0	1
15U41A0562	RT31051	COMPILER DESIGN	30	30	3
15U41A0562	RT31052	DATA COMMUNICATION	29	25	3
15U41A0562	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	33	3
15U41A0562	RT31054	DATABASE MANAGEMENT SYSTEMS	24	28	3
15U41A0562	RT31055	OPERATING SYSTEMS	26	41	3
15U41A0562	RT31056	COMPILER DESIGN LAB	24	47	2
15U41A0562	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	48	2
15U41A0562	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	49	2
15U41A0562	RT31059	SEMINAR	46	0	1
15U41A0563	RT31051	COMPILER DESIGN	18	6	0
15U41A0563	RT31052	DATA COMMUNICATION	21	27	3
15U41A0563	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	17	0
15U41A0563	RT31054	DATABASE MANAGEMENT SYSTEMS	13	12	0
15U41A0563	RT31055	OPERATING SYSTEMS	19	30	3
15U41A0563	RT31056	COMPILER DESIGN LAB	20	42	2
15U41A0563	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	46	2
15U41A0563	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	21	45	2
15U41A0563	RT31059	SEMINAR	41	0	1

Htno	Subcode	Subname	Internal	External	Credits
15U41A0564	RT31051	COMPILER DESIGN	29	54	3
15U41A0564	RT31052	DATA COMMUNICATION	29	48	3
15U41A0564	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	37	3
15U41A0564	RT31054	DATABASE MANAGEMENT SYSTEMS	27	32	3
15U41A0564	RT31055	OPERATING SYSTEMS	29	55	3
15U41A0564	RT31056	COMPILER DESIGN LAB	21	46	2
15U41A0564	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0564	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0564	RT31059	SEMINAR	46	0	1
15U41A0565	RT31051	COMPILER DESIGN	28	30	3
15U41A0565	RT31052	DATA COMMUNICATION	18	39	3
15U41A0565	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	46	3
15U41A0565	RT31054	DATABASE MANAGEMENT SYSTEMS	23	17	0
15U41A0565	RT31055	OPERATING SYSTEMS	27	35	3
15U41A0565	RT31056	COMPILER DESIGN LAB	23	45	2
15U41A0565	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0565	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	49	2
15U41A0565	RT31059	SEMINAR	45	0	1
15U41A0566	RT31051	COMPILER DESIGN	19	0	0
15U41A0566	RT31052	DATA COMMUNICATION	17	24	3
15U41A0566	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	21	26	3
15U41A0566	RT31054	DATABASE MANAGEMENT SYSTEMS	17	27	3
15U41A0566	RT31055	OPERATING SYSTEMS	13	8	0
15U41A0566	RT31056	COMPILER DESIGN LAB	22	36	2
15U41A0566	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	13	40	2
15U41A0566	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	21	45	2
15U41A0566	RT31059	SEMINAR	40	0	1
15U41A0567	RT31051	COMPILER DESIGN	30	62	3
15U41A0567	RT31052	DATA COMMUNICATION	30	50	3
15U41A0567	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	58	3
15U41A0567	RT31054	DATABASE MANAGEMENT SYSTEMS	27	49	3
15U41A0567	RT31055	OPERATING SYSTEMS	28	52	3
15U41A0567	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A0567	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0567	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0567	RT31059	SEMINAR	50	0	1
15U41A0568	RT31051	COMPILER DESIGN	29	36	3
15U41A0568	RT31052	DATA COMMUNICATION	25	29	3
15U41A0568	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	38	3
15U41A0568	RT31054	DATABASE MANAGEMENT SYSTEMS	24	32	3
15U41A0568	RT31055	OPERATING SYSTEMS	27	47	3
15U41A0568	RT31056	COMPILER DESIGN LAB	25	48	2
15U41A0568	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0568	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0568	RT31059	SEMINAR	49	0	1
15U41A0569	RT31051	COMPILER DESIGN	27	34	3
15U41A0569	RT31052	DATA COMMUNICATION	30	39	3
15U41A0569	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	44	3
15U41A0569	RT31054	DATABASE MANAGEMENT SYSTEMS	26	24	3
15U41A0569	RT31055	OPERATING SYSTEMS	27	36	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0569	RT31056	COMPILER DESIGN LAB	23	47	2
15U41A0569	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0569	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	50	2
15U41A0569	RT31059	SEMINAR	48	0	1
15U41A0570	RT31051	COMPILER DESIGN	25	17	0
15U41A0570	RT31052	DATA COMMUNICATION	22	6	0
15U41A0570	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	21	27	3
15U41A0570	RT31054	DATABASE MANAGEMENT SYSTEMS	20	30	3
15U41A0570	RT31055	OPERATING SYSTEMS	25	29	3
15U41A0570	RT31056	COMPILER DESIGN LAB	20	36	2
15U41A0570	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	48	2
15U41A0570	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	49	2
15U41A0570	RT31059	SEMINAR	46	0	1
15U41A0571	RT31051	COMPILER DESIGN	24	6	0
15U41A0571	RT31052	DATA COMMUNICATION	21	45	3
15U41A0571	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	43	3
15U41A0571	RT31054	DATABASE MANAGEMENT SYSTEMS	20	30	3
15U41A0571	RT31055	OPERATING SYSTEMS	20	32	3
15U41A0571	RT31056	COMPILER DESIGN LAB	20	40	2
15U41A0571	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	18	42	2
15U41A0571	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0571	RT31059	SEMINAR	45	0	1
15U41A0572	RT31051	COMPILER DESIGN	28	37	3
15U41A0572	RT31052	DATA COMMUNICATION	24	38	3
15U41A0572	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	33	3
15U41A0572	RT31054	DATABASE MANAGEMENT SYSTEMS	26	39	3
15U41A0572	RT31055	OPERATING SYSTEMS	22	46	3
15U41A0572	RT31056	COMPILER DESIGN LAB	22	45	2
15U41A0572	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	47	2
15U41A0572	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A0572	RT31059	SEMINAR	47	0	1
15U41A0573	RT31051	COMPILER DESIGN	30	45	3
15U41A0573	RT31052	DATA COMMUNICATION	29	45	3
15U41A0573	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	48	3
15U41A0573	RT31054	DATABASE MANAGEMENT SYSTEMS	29	31	3
15U41A0573	RT31055	OPERATING SYSTEMS	30	47	3
15U41A0573	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A0573	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	48	2
15U41A0573	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0573	RT31059	SEMINAR	50	0	1
15U41A0574	RT31051	COMPILER DESIGN	22	24	3
15U41A0574	RT31052	DATA COMMUNICATION	21	10	0
15U41A0574	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	20	26	3
15U41A0574	RT31054	DATABASE MANAGEMENT SYSTEMS	18	32	3
15U41A0574	RT31055	OPERATING SYSTEMS	20	28	3
15U41A0574	RT31056	COMPILER DESIGN LAB	22	47	2
15U41A0574	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	45	2
15U41A0574	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A0574	RT31059	SEMINAR	45	0	1
15U41A0575	RT31051	COMPILER DESIGN	27	18	0

Htno	Subcode	Subname	Internal	External	Credits
15U41A0575	RT31052	DATA COMMUNICATION	24	45	3
15U41A0575	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	48	3
15U41A0575	RT31054	DATABASE MANAGEMENT SYSTEMS	24	34	3
15U41A0575	RT31055	OPERATING SYSTEMS	23	34	3
15U41A0575	RT31056	COMPILER DESIGN LAB	21	41	2
15U41A0575	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	42	2
15U41A0575	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0575	RT31059	SEMINAR	44	0	1
15U41A0576	RT31051	COMPILER DESIGN	27	38	3
15U41A0576	RT31052	DATA COMMUNICATION	26	28	3
15U41A0576	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	34	3
15U41A0576	RT31054	DATABASE MANAGEMENT SYSTEMS	19	39	3
15U41A0576	RT31055	OPERATING SYSTEMS	25	41	3
15U41A0576	RT31056	COMPILER DESIGN LAB	21	35	2
15U41A0576	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	43	2
15U41A0576	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	21	46	2
15U41A0576	RT31059	SEMINAR	44	0	1
15U41A0577	RT31051	COMPILER DESIGN	29	30	3
15U41A0577	RT31052	DATA COMMUNICATION	29	54	3
15U41A0577	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	47	3
15U41A0577	RT31054	DATABASE MANAGEMENT SYSTEMS	27	14	0
15U41A0577	RT31055	OPERATING SYSTEMS	29	45	3
15U41A0577	RT31056	COMPILER DESIGN LAB	24	46	2
15U41A0577	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A0577	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A0577	RT31059	SEMINAR	47	0	1
15U41A0578	RT31051	COMPILER DESIGN	28	24	3
15U41A0578	RT31052	DATA COMMUNICATION	27	7	0
15U41A0578	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	30	3
15U41A0578	RT31054	DATABASE MANAGEMENT SYSTEMS	18	27	3
15U41A0578	RT31055	OPERATING SYSTEMS	20	27	3
15U41A0578	RT31056	COMPILER DESIGN LAB	22	41	2
15U41A0578	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	20	41	2
15U41A0578	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0578	RT31059	SEMINAR	42	0	1
15U41A0579	RT31051	COMPILER DESIGN	25	36	3
15U41A0579	RT31052	DATA COMMUNICATION	25	47	3
15U41A0579	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	57	3
15U41A0579	RT31054	DATABASE MANAGEMENT SYSTEMS	20	37	3
15U41A0579	RT31055	OPERATING SYSTEMS	20	43	3
15U41A0579	RT31056	COMPILER DESIGN LAB	23	45	2
15U41A0579	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	48	2
15U41A0579	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A0579	RT31059	SEMINAR	43	0	1
15U41A0580	RT31051	COMPILER DESIGN	28	38	3
15U41A0580	RT31052	DATA COMMUNICATION	23	33	3
15U41A0580	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	34	3
15U41A0580	RT31054	DATABASE MANAGEMENT SYSTEMS	25	38	3
15U41A0580	RT31055	OPERATING SYSTEMS	23	41	3
15U41A0580	RT31056	COMPILER DESIGN LAB	24	42	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0580	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	49	2
15U41A0580	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	49	2
15U41A0580	RT31059	SEMINAR	44	0	1
15U41A0581	RT31051	COMPILER DESIGN	27	27	3
15U41A0581	RT31052	DATA COMMUNICATION	24	45	3
15U41A0581	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	57	3
15U41A0581	RT31054	DATABASE MANAGEMENT SYSTEMS	26	24	3
15U41A0581	RT31055	OPERATING SYSTEMS	23	32	3
15U41A0581	RT31056	COMPILER DESIGN LAB	19	35	2
15U41A0581	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	17	41	2
15U41A0581	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0581	RT31059	SEMINAR	40	0	1
15U41A0582	RT31051	COMPILER DESIGN	28	42	3
15U41A0582	RT31052	DATA COMMUNICATION	28	42	3
15U41A0582	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	46	3
15U41A0582	RT31054	DATABASE MANAGEMENT SYSTEMS	30	41	3
15U41A0582	RT31055	OPERATING SYSTEMS	28	36	3
15U41A0582	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A0582	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A0582	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A0582	RT31059	SEMINAR	50	0	1
15U41A0583	RT31051	COMPILER DESIGN	18	14	0
15U41A0583	RT31052	DATA COMMUNICATION	18	30	3
15U41A0583	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	36	3
15U41A0583	RT31054	DATABASE MANAGEMENT SYSTEMS	11	13	0
15U41A0583	RT31055	OPERATING SYSTEMS	14	30	3
15U41A0583	RT31056	COMPILER DESIGN LAB	21	40	2
15U41A0583	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	17	41	2
15U41A0583	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0583	RT31059	SEMINAR	43	0	1
15U41A0584	RT31051	COMPILER DESIGN	30	35	3
15U41A0584	RT31052	DATA COMMUNICATION	22	26	3
15U41A0584	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	30	3
15U41A0584	RT31054	DATABASE MANAGEMENT SYSTEMS	18	14	0
15U41A0584	RT31055	OPERATING SYSTEMS	23	31	3
15U41A0584	RT31056	COMPILER DESIGN LAB	21	44	2
15U41A0584	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	48	2
15U41A0584	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	49	2
15U41A0584	RT31059	SEMINAR	42	0	1
15U41A0585	RT31051	COMPILER DESIGN	26	35	3
15U41A0585	RT31052	DATA COMMUNICATION	28	51	3
15U41A0585	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	47	3
15U41A0585	RT31054	DATABASE MANAGEMENT SYSTEMS	24	18	0
15U41A0585	RT31055	OPERATING SYSTEMS	24	40	3
15U41A0585	RT31056	COMPILER DESIGN LAB	23	43	2
15U41A0585	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	46	2
15U41A0585	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A0585	RT31059	SEMINAR	45	0	1
15U41A0586	RT31051	COMPILER DESIGN	28	24	3
15U41A0586	RT31052	DATA COMMUNICATION	26	10	0

Htno	Subcode	Subname	Internal	External	Credits
15U41A0586	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	38	3
15U41A0586	RT31054	DATABASE MANAGEMENT SYSTEMS	27	27	3
15U41A0586	RT31055	OPERATING SYSTEMS	27	29	3
15U41A0586	RT31056	COMPILER DESIGN LAB	21	46	2
15U41A0586	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	48	2
15U41A0586	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	49	2
15U41A0586	RT31059	SEMINAR	43	0	1
15U41A0587	RT31051	COMPILER DESIGN	19	15	0
15U41A0587	RT31052	DATA COMMUNICATION	22	45	3
15U41A0587	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	35	3
15U41A0587	RT31054	DATABASE MANAGEMENT SYSTEMS	18	28	3
15U41A0587	RT31055	OPERATING SYSTEMS	17	34	3
15U41A0587	RT31056	COMPILER DESIGN LAB	21	46	2
15U41A0587	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	48	2
15U41A0587	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	48	2
15U41A0587	RT31059	SEMINAR	44	0	1
15U41A0588	RT31051	COMPILER DESIGN	29	46	3
15U41A0588	RT31052	DATA COMMUNICATION	24	37	3
15U41A0588	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	41	3
15U41A0588	RT31054	DATABASE MANAGEMENT SYSTEMS	28	44	3
15U41A0588	RT31055	OPERATING SYSTEMS	26	44	3
15U41A0588	RT31056	COMPILER DESIGN LAB	22	47	2
15U41A0588	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	49	2
15U41A0588	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	48	2
15U41A0588	RT31059	SEMINAR	42	0	1
15U41A0589	RT31051	COMPILER DESIGN	25	24	3
15U41A0589	RT31052	DATA COMMUNICATION	20	53	3
15U41A0589	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	44	3
15U41A0589	RT31054	DATABASE MANAGEMENT SYSTEMS	20	31	3
15U41A0589	RT31055	OPERATING SYSTEMS	23	41	3
15U41A0589	RT31056	COMPILER DESIGN LAB	21	41	2
15U41A0589	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	45	2
15U41A0589	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0589	RT31059	SEMINAR	42	0	1
15U41A0590	RT31051	COMPILER DESIGN	24	24	3
15U41A0590	RT31052	DATA COMMUNICATION	25	32	3
15U41A0590	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	31	3
15U41A0590	RT31054	DATABASE MANAGEMENT SYSTEMS	18	29	3
15U41A0590	RT31055	OPERATING SYSTEMS	20	29	3
15U41A0590	RT31056	COMPILER DESIGN LAB	21	41	2
15U41A0590	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	45	2
15U41A0590	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	47	2
15U41A0590	RT31059	SEMINAR	43	0	1
15U41A0592	RT31051	COMPILER DESIGN	24	11	0
15U41A0592	RT31052	DATA COMMUNICATION	22	42	3
15U41A0592	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	53	3
15U41A0592	RT31054	DATABASE MANAGEMENT SYSTEMS	19	25	3
15U41A0592	RT31055	OPERATING SYSTEMS	24	52	3
15U41A0592	RT31056	COMPILER DESIGN LAB	22	40	2
15U41A0592	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A0592	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A0592	RT31059	SEMINAR	44	0	1
15U41A0593	RT31051	COMPILER DESIGN	28	40	3
15U41A0593	RT31052	DATA COMMUNICATION	22	27	3
15U41A0593	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	37	3
15U41A0593	RT31054	DATABASE MANAGEMENT SYSTEMS	21	38	3
15U41A0593	RT31055	OPERATING SYSTEMS	21	37	3
15U41A0593	RT31056	COMPILER DESIGN LAB	20	41	2
15U41A0593	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	20	41	2
15U41A0593	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0593	RT31059	SEMINAR	44	0	1
15U41A0594	RT31051	COMPILER DESIGN	19	8	0
15U41A0594	RT31052	DATA COMMUNICATION	16	33	3
15U41A0594	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	16	37	3
15U41A0594	RT31054	DATABASE MANAGEMENT SYSTEMS	11	12	0
15U41A0594	RT31055	OPERATING SYSTEMS	13	39	3
15U41A0594	RT31056	COMPILER DESIGN LAB	22	42	2
15U41A0594	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	43	2
15U41A0594	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0594	RT31059	SEMINAR	45	0	1
15U41A0595	RT31051	COMPILER DESIGN	16	0	0
15U41A0595	RT31052	DATA COMMUNICATION	11	9	0
15U41A0595	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	6	16	0
15U41A0595	RT31054	DATABASE MANAGEMENT SYSTEMS	12	10	0
15U41A0595	RT31055	OPERATING SYSTEMS	13	17	0
15U41A0595	RT31056	COMPILER DESIGN LAB	19	37	2
15U41A0595	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	18	41	2
15U41A0595	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	45	2
15U41A0595	RT31059	SEMINAR	43	0	1
15U41A0596	RT31051	COMPILER DESIGN	29	18	0
15U41A0596	RT31052	DATA COMMUNICATION	29	48	3
15U41A0596	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	47	3
15U41A0596	RT31054	DATABASE MANAGEMENT SYSTEMS	28	38	3
15U41A0596	RT31055	OPERATING SYSTEMS	28	52	3
15U41A0596	RT31056	COMPILER DESIGN LAB	23	46	2
15U41A0596	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	48	2
15U41A0596	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0596	RT31059	SEMINAR	45	0	1
15U41A0597	RT31051	COMPILER DESIGN	30	53	3
15U41A0597	RT31052	DATA COMMUNICATION	30	32	3
15U41A0597	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	45	3
15U41A0597	RT31054	DATABASE MANAGEMENT SYSTEMS	30	44	3
15U41A0597	RT31055	OPERATING SYSTEMS	23	39	3
15U41A0597	RT31056	COMPILER DESIGN LAB	22	47	2
15U41A0597	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A0597	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A0597	RT31059	SEMINAR	46	0	1
15U41A0598	RT31051	COMPILER DESIGN	23	13	0
15U41A0598	RT31052	DATA COMMUNICATION	15	25	3
15U41A0598	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	18	28	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A0598	RT31054	DATABASE MANAGEMENT SYSTEMS	18	16	0
15U41A0598	RT31055	OPERATING SYSTEMS	23	16	0
15U41A0598	RT31056	COMPILER DESIGN LAB	21	42	2
15U41A0598	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	45	2
15U41A0598	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	47	2
15U41A0598	RT31059	SEMINAR	42	0	1
15U41A0599	RT31051	COMPILER DESIGN	27	3	0
15U41A0599	RT31052	DATA COMMUNICATION	24	3	0
15U41A0599	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	16	18	0
15U41A0599	RT31054	DATABASE MANAGEMENT SYSTEMS	16	30	3
15U41A0599	RT31055	OPERATING SYSTEMS	22	13	0
15U41A0599	RT31056	COMPILER DESIGN LAB	20	36	2
15U41A0599	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	41	2
15U41A0599	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
15U41A0599	RT31059	SEMINAR	40	0	1
15U41A05A0	RT31051	COMPILER DESIGN	26	12	0
15U41A05A0	RT31052	DATA COMMUNICATION	25	49	3
15U41A05A0	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	38	3
15U41A05A0	RT31054	DATABASE MANAGEMENT SYSTEMS	23	40	3
15U41A05A0	RT31055	OPERATING SYSTEMS	22	43	3
15U41A05A0	RT31056	COMPILER DESIGN LAB	21	41	2
15U41A05A0	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	40	2
15U41A05A0	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A05A0	RT31059	SEMINAR	42	0	1
15U41A05A2	RT31051	COMPILER DESIGN	29	39	3
15U41A05A2	RT31052	DATA COMMUNICATION	25	27	3
15U41A05A2	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	28	3
15U41A05A2	RT31054	DATABASE MANAGEMENT SYSTEMS	28	33	3
15U41A05A2	RT31055	OPERATING SYSTEMS	25	44	3
15U41A05A2	RT31056	COMPILER DESIGN LAB	24	48	2
15U41A05A2	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	50	2
15U41A05A2	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A05A2	RT31059	SEMINAR	47	0	1
15U41A05A3	RT31051	COMPILER DESIGN	23	29	3
15U41A05A3	RT31052	DATA COMMUNICATION	19	37	3
15U41A05A3	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	18	47	3
15U41A05A3	RT31054	DATABASE MANAGEMENT SYSTEMS	21	18	0
15U41A05A3	RT31055	OPERATING SYSTEMS	17	49	3
15U41A05A3	RT31056	COMPILER DESIGN LAB	23	41	2
15U41A05A3	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A05A3	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A05A3	RT31059	SEMINAR	46	0	1
15U41A05A4	RT31051	COMPILER DESIGN	27	37	3
15U41A05A4	RT31052	DATA COMMUNICATION	29	14	0
15U41A05A4	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	40	3
15U41A05A4	RT31054	DATABASE MANAGEMENT SYSTEMS	29	41	3
15U41A05A4	RT31055	OPERATING SYSTEMS	29	36	3
15U41A05A4	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A05A4	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A05A4	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2

Htno	Subcode	Subname	Internal	External	Credits
15U41A05A4	RT31059	SEMINAR	50	0	1
15U41A05A6	RT31051	COMPILER DESIGN	30	53	3
15U41A05A6	RT31052	DATA COMMUNICATION	28	51	3
15U41A05A6	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	56	3
15U41A05A6	RT31054	DATABASE MANAGEMENT SYSTEMS	29	48	3
15U41A05A6	RT31055	OPERATING SYSTEMS	28	52	3
15U41A05A6	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A05A6	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
15U41A05A6	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05A6	RT31059	SEMINAR	48	0	1
15U41A05A8	RT31051	COMPILER DESIGN	30	47	3
15U41A05A8	RT31052	DATA COMMUNICATION	26	29	3
15U41A05A8	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	35	3
15U41A05A8	RT31054	DATABASE MANAGEMENT SYSTEMS	28	33	3
15U41A05A8	RT31055	OPERATING SYSTEMS	27	42	3
15U41A05A8	RT31056	COMPILER DESIGN LAB	25	50	2
15U41A05A8	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A05A8	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05A8	RT31059	SEMINAR	45	0	1
15U41A05A9	RT31051	COMPILER DESIGN	28	26	3
15U41A05A9	RT31052	DATA COMMUNICATION	25	38	3
15U41A05A9	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	51	3
15U41A05A9	RT31054	DATABASE MANAGEMENT SYSTEMS	29	43	3
15U41A05A9	RT31055	OPERATING SYSTEMS	27	53	3
15U41A05A9	RT31056	COMPILER DESIGN LAB	23	47	2
15U41A05A9	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	49	2
15U41A05A9	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	47	2
15U41A05A9	RT31059	SEMINAR	46	0	1
15U41A05B0	RT31051	COMPILER DESIGN	18	24	3
15U41A05B0	RT31052	DATA COMMUNICATION	20	5	0
15U41A05B0	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	19	17	0
15U41A05B0	RT31054	DATABASE MANAGEMENT SYSTEMS	20	28	3
15U41A05B0	RT31055	OPERATING SYSTEMS	22	17	0
15U41A05B0	RT31056	COMPILER DESIGN LAB	21	42	2
15U41A05B0	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	21	46	2
15U41A05B0	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	46	2
15U41A05B0	RT31059	SEMINAR	44	0	1
15U41A05B1	RT31051	COMPILER DESIGN	25	17	0
15U41A05B1	RT31052	DATA COMMUNICATION	23	39	3
15U41A05B1	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	50	3
15U41A05B1	RT31054	DATABASE MANAGEMENT SYSTEMS	24	41	3
15U41A05B1	RT31055	OPERATING SYSTEMS	19	39	3
15U41A05B1	RT31056	COMPILER DESIGN LAB	22	45	2
15U41A05B1	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	46	2
15U41A05B1	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05B1	RT31059	SEMINAR	43	0	1
15U41A05B2	RT31051	COMPILER DESIGN	29	45	3
15U41A05B2	RT31052	DATA COMMUNICATION	24	26	3
15U41A05B2	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	34	3
15U41A05B2	RT31054	DATABASE MANAGEMENT SYSTEMS	29	30	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A05B2	RT31055	OPERATING SYSTEMS	22	43	3
15U41A05B2	RT31056	COMPILER DESIGN LAB	24	47	2
15U41A05B2	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	45	2
15U41A05B2	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A05B2	RT31059	SEMINAR	46	0	1
15U41A05B3	RT31051	COMPILER DESIGN	22	3	0
15U41A05B3	RT31052	DATA COMMUNICATION	12	5	0
15U41A05B3	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	10	21	0
15U41A05B3	RT31054	DATABASE MANAGEMENT SYSTEMS	14	8	0
15U41A05B3	RT31055	OPERATING SYSTEMS	16	18	0
15U41A05B3	RT31056	COMPILER DESIGN LAB	21	44	2
15U41A05B3	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	45	2
15U41A05B3	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	46	2
15U41A05B3	RT31059	SEMINAR	44	0	1
15U41A05B5	RT31051	COMPILER DESIGN	29	36	3
15U41A05B5	RT31052	DATA COMMUNICATION	27	7	0
15U41A05B5	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	33	3
15U41A05B5	RT31054	DATABASE MANAGEMENT SYSTEMS	26	39	3
15U41A05B5	RT31055	OPERATING SYSTEMS	26	33	3
15U41A05B5	RT31056	COMPILER DESIGN LAB	24	46	2
15U41A05B5	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	48	2
15U41A05B5	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	48	2
15U41A05B5	RT31059	SEMINAR	47	0	1
15U41A05B6	RT31051	COMPILER DESIGN	25	42	3
15U41A05B6	RT31052	DATA COMMUNICATION	27	50	3
15U41A05B6	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	54	3
15U41A05B6	RT31054	DATABASE MANAGEMENT SYSTEMS	25	40	3
15U41A05B6	RT31055	OPERATING SYSTEMS	24	39	3
15U41A05B6	RT31056	COMPILER DESIGN LAB	23	44	2
15U41A05B6	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	49	2
15U41A05B6	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	49	2
15U41A05B6	RT31059	SEMINAR	46	0	1
15U41A05B7	RT31051	COMPILER DESIGN	18	40	3
15U41A05B7	RT31052	DATA COMMUNICATION	17	14	0
15U41A05B7	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	35	3
15U41A05B7	RT31054	DATABASE MANAGEMENT SYSTEMS	15	25	3
15U41A05B7	RT31055	OPERATING SYSTEMS	17	40	3
15U41A05B7	RT31056	COMPILER DESIGN LAB	22	42	2
15U41A05B7	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	44	2
15U41A05B7	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	46	2
15U41A05B7	RT31059	SEMINAR	44	0	1
15U41A05B8	RT31051	COMPILER DESIGN	30	50	3
15U41A05B8	RT31052	DATA COMMUNICATION	29	42	3
15U41A05B8	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	49	3
15U41A05B8	RT31054	DATABASE MANAGEMENT SYSTEMS	28	51	3
15U41A05B8	RT31055	OPERATING SYSTEMS	24	48	3
15U41A05B8	RT31056	COMPILER DESIGN LAB	19	40	2
15U41A05B8	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	20	50	2
15U41A05B8	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A05B8	RT31059	SEMINAR	43	0	1

Htno	Subcode	Subname	Internal	External	Credits
15U41A05C0	RT31051	COMPILER DESIGN	27	37	3
15U41A05C0	RT31052	DATA COMMUNICATION	26	17	0
15U41A05C0	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	34	3
15U41A05C0	RT31054	DATABASE MANAGEMENT SYSTEMS	24	34	3
15U41A05C0	RT31055	OPERATING SYSTEMS	27	25	3
15U41A05C0	RT31056	COMPILER DESIGN LAB	21	42	2
15U41A05C0	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	20	49	2
15U41A05C0	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	48	2
15U41A05C0	RT31059	SEMINAR	44	0	1
15U41A05C1	RT31051	COMPILER DESIGN	21	36	3
15U41A05C1	RT31052	DATA COMMUNICATION	27	52	3
15U41A05C1	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	58	3
15U41A05C1	RT31054	DATABASE MANAGEMENT SYSTEMS	21	56	3
15U41A05C1	RT31055	OPERATING SYSTEMS	23	47	3
15U41A05C1	RT31056	COMPILER DESIGN LAB	20	43	2
15U41A05C1	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	50	2
15U41A05C1	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	48	2
15U41A05C1	RT31059	SEMINAR	43	0	1
15U41A05C2	RT31051	COMPILER DESIGN	22	33	3
15U41A05C2	RT31052	DATA COMMUNICATION	21	24	3
15U41A05C2	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	19	24	3
15U41A05C2	RT31054	DATABASE MANAGEMENT SYSTEMS	16	28	3
15U41A05C2	RT31055	OPERATING SYSTEMS	17	34	3
15U41A05C2	RT31056	COMPILER DESIGN LAB	21	42	2
15U41A05C2	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	47	2
15U41A05C2	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05C2	RT31059	SEMINAR	44	0	1
15U41A05C3	RT31051	COMPILER DESIGN	30	42	3
15U41A05C3	RT31052	DATA COMMUNICATION	25	45	3
15U41A05C3	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	42	3
15U41A05C3	RT31054	DATABASE MANAGEMENT SYSTEMS	29	37	3
15U41A05C3	RT31055	OPERATING SYSTEMS	27	46	3
15U41A05C3	RT31056	COMPILER DESIGN LAB	21	43	2
15U41A05C3	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	47	2
15U41A05C3	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05C3	RT31059	SEMINAR	45	0	1
15U41A05C4	RT31051	COMPILER DESIGN	24	5	0
15U41A05C4	RT31052	DATA COMMUNICATION	21	2	0
15U41A05C4	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	15	11	0
15U41A05C4	RT31054	DATABASE MANAGEMENT SYSTEMS	15	10	0
15U41A05C4	RT31055	OPERATING SYSTEMS	18	10	0
15U41A05C4	RT31056	COMPILER DESIGN LAB	21	44	2
15U41A05C4	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	44	2
15U41A05C4	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	46	2
15U41A05C4	RT31059	SEMINAR	45	0	1
15U41A05C5	RT31051	COMPILER DESIGN	29	42	3
15U41A05C5	RT31052	DATA COMMUNICATION	26	43	3
15U41A05C5	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	49	3
15U41A05C5	RT31054	DATABASE MANAGEMENT SYSTEMS	28	41	3
15U41A05C5	RT31055	OPERATING SYSTEMS	28	39	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A05C5	RT31056	COMPILER DESIGN LAB	23	47	2
15U41A05C5	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	49	2
15U41A05C5	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
15U41A05C5	RT31059	SEMINAR	48	0	1
15U41A05C6	RT31051	COMPILER DESIGN	29	46	3
15U41A05C6	RT31052	DATA COMMUNICATION	29	48	3
15U41A05C6	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	25	37	3
15U41A05C6	RT31054	DATABASE MANAGEMENT SYSTEMS	24	41	3
15U41A05C6	RT31055	OPERATING SYSTEMS	27	47	3
15U41A05C6	RT31056	COMPILER DESIGN LAB	20	42	2
15U41A05C6	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	44	2
15U41A05C6	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	24	47	2
15U41A05C6	RT31059	SEMINAR	42	0	1
15U41A05C7	RT31051	COMPILER DESIGN	30	25	3
15U41A05C7	RT31052	DATA COMMUNICATION	27	36	3
15U41A05C7	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	49	3
15U41A05C7	RT31054	DATABASE MANAGEMENT SYSTEMS	25	31	3
15U41A05C7	RT31055	OPERATING SYSTEMS	27	41	3
15U41A05C7	RT31056	COMPILER DESIGN LAB	25	49	2
15U41A05C7	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	44	2
15U41A05C7	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05C7	RT31059	SEMINAR	47	0	1
15U41A05C8	RT31051	COMPILER DESIGN	25	24	3
15U41A05C8	RT31052	DATA COMMUNICATION	22	14	0
15U41A05C8	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	27	3
15U41A05C8	RT31054	DATABASE MANAGEMENT SYSTEMS	20	40	3
15U41A05C8	RT31055	OPERATING SYSTEMS	18	26	3
15U41A05C8	RT31056	COMPILER DESIGN LAB	21	47	2
15U41A05C8	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	44	2
15U41A05C8	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A05C8	RT31059	SEMINAR	43	0	1
15U41A05D0	RT31051	COMPILER DESIGN	24	24	3
15U41A05D0	RT31052	DATA COMMUNICATION	22	44	3
15U41A05D0	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	23	30	3
15U41A05D0	RT31054	DATABASE MANAGEMENT SYSTEMS	20	24	3
15U41A05D0	RT31055	OPERATING SYSTEMS	18	38	3
15U41A05D0	RT31056	COMPILER DESIGN LAB	22	45	2
15U41A05D0	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	47	2
15U41A05D0	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05D0	RT31059	SEMINAR	44	0	1
15U41A05D1	RT31051	COMPILER DESIGN	24	41	3
15U41A05D1	RT31052	DATA COMMUNICATION	20	29	3
15U41A05D1	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	33	3
15U41A05D1	RT31054	DATABASE MANAGEMENT SYSTEMS	20	36	3
15U41A05D1	RT31055	OPERATING SYSTEMS	19	41	3
15U41A05D1	RT31056	COMPILER DESIGN LAB	22	43	2
15U41A05D1	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	46	2
15U41A05D1	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
15U41A05D1	RT31059	SEMINAR	43	0	1
15U41A05D2	RT31051	COMPILER DESIGN	27	24	3

Htno	Subcode	Subname	Internal	External	Credits
15U41A05D2	RT31052	DATA COMMUNICATION	24	39	3
15U41A05D2	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	53	3
15U41A05D2	RT31054	DATABASE MANAGEMENT SYSTEMS	24	32	3
15U41A05D2	RT31055	OPERATING SYSTEMS	23	43	3
15U41A05D2	RT31056	COMPILER DESIGN LAB	21	42	2
15U41A05D2	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	48	2
15U41A05D2	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	48	2
15U41A05D2	RT31059	SEMINAR	42	0	1
15U45A0111	RT31011	GEOTECHNICAL ENGINEERING-I	15	25	3
15U45A0111	RT31012	STRUCTURAL ANALYSIS-II	14	27	3
15U45A0119	RT31011	GEOTECHNICAL ENGINEERING-I	7	12	0
15U45A0119	RT31012	STRUCTURAL ANALYSIS-II	11	9	0
15U45A0119	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	25	39	3
15U45A0121	RT31011	GEOTECHNICAL ENGINEERING-I	9	31	3
15U45A0121	RT31015	TRANSPORTATION ENGINEERING-I	20	32	3
15U45A0124	RT31012	STRUCTURAL ANALYSIS-II	13	4	0
15U45A0124	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	27	13	0
15U45A0127	RT31011	GEOTECHNICAL ENGINEERING-I	15	19	0
15U45A0134	RT31011	GEOTECHNICAL ENGINEERING-I	12	19	0
15U45A0134	RT31012	STRUCTURAL ANALYSIS-II	11	13	0
15U45A0134	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	29	20	0
15U45A0204	RT31016	IPR & PATENTS	11	29	2
15U45A0204	RT31021	ELECTRICAL MEASUREMENTS	20	32	3
15U45A0204	RT31023	POWER SYSTEMS-II	21	17	0
15U45A0204	RT31024	ELECTRICAL MACHINES-III	19	18	0
15U45A0204	RT31025	POWER ELECTRONICS	25	31	3
15U45A0208	RT31023	POWER SYSTEMS-II	18	11	0
15U45A0208	RT31024	ELECTRICAL MACHINES-III	10	-1	0
15U45A0208	RT31025	POWER ELECTRONICS	15	36	3
15U45A0208	RT31026	LINEAR & DIGITAL IC APPLICATIONS	14	0	0
15U45A0211	RT31023	POWER SYSTEMS-II	23	17	0
15U45A0211	RT31025	POWER ELECTRONICS	25	50	3
15U45A0212	RT31023	POWER SYSTEMS-II	18	20	0
15U45A0212	RT31025	POWER ELECTRONICS	18	3	0
15U45A0212	RT31026	LINEAR & DIGITAL IC APPLICATIONS	12	8	0
15U45A0213	RT31026	LINEAR & DIGITAL IC APPLICATIONS	13	46	3
15U45A0216	RT31025	POWER ELECTRONICS	20	30	3
15U45A0219	RT31023	POWER SYSTEMS-II	22	15	0
15U45A0219	RT31024	ELECTRICAL MACHINES-III	22	5	0
15U45A0219	RT31025	POWER ELECTRONICS	19	34	3
15U45A0219	RT31026	LINEAR & DIGITAL IC APPLICATIONS	17	-1	0
15U45A0220	RT31025	POWER ELECTRONICS	24	30	3
15U45A0220	RT31026	LINEAR & DIGITAL IC APPLICATIONS	15	9	0
15U45A0222	RT31026	LINEAR & DIGITAL IC APPLICATIONS	20	22	0
15U45A0240	RT31023	POWER SYSTEMS-II	20	20	0
15U45A0242	RT31024	ELECTRICAL MACHINES-III	18	-1	0
15U45A0242	RT31025	POWER ELECTRONICS	17	-1	0
15U45A0243	RT31025	POWER ELECTRONICS	22	31	3
15U45A0243	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	13	0
15U45A0246	RT31021	ELECTRICAL MEASUREMENTS	17	32	3

Htno	Subcode	Subname	Internal	External	Credits
15U45A0246	RT31023	POWER SYSTEMS-II	16	8	0
15U45A0246	RT31025	POWER ELECTRONICS	19	28	3
15U45A0246	RT31026	LINEAR & DIGITAL IC APPLICATIONS	14	9	0
15U45A0248	RT31024	ELECTRICAL MACHINES-III	16	0	0
15U45A0251	RT31024	ELECTRICAL MACHINES-III	5	8	0
15U45A0251	RT31025	POWER ELECTRONICS	18	32	3
15U45A0251	RT31026	LINEAR & DIGITAL IC APPLICATIONS	13	9	0
15U45A0252	RT31021	ELECTRICAL MEASUREMENTS	18	6	0
15U45A0252	RT31026	LINEAR & DIGITAL IC APPLICATIONS	12	23	0
15U45A0403	RT31041	PULSE & DIGITAL CIRCUITS	20	35	3
15U45A0404	RT31041	PULSE & DIGITAL CIRCUITS	20	27	3
15U45A0404	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	19	25	3
15U45A0412	RT31041	PULSE & DIGITAL CIRCUITS	28	14	0
16U45A0101	RT31011	GEOTECHNICAL ENGINEERING-I	27	28	3
16U45A0101	RT31012	STRUCTURAL ANALYSIS-II	27	52	3
16U45A0101	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	61	3
16U45A0101	RT31014	ENGINEERING GEOLOGY	27	43	3
16U45A0101	RT31015	TRANSPORTATION ENGINEERING-I	28	55	3
16U45A0101	RT31016	IPR & PATENTS	27	39	2
16U45A0101	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
16U45A0101	RT31018	ENGINEERING GEOLOGY LAB	25	50	2
16U45A0102	RT31011	GEOTECHNICAL ENGINEERING-I	30	41	3
16U45A0102	RT31012	STRUCTURAL ANALYSIS-II	26	67	3
16U45A0102	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	52	3
16U45A0102	RT31014	ENGINEERING GEOLOGY	28	45	3
16U45A0102	RT31015	TRANSPORTATION ENGINEERING-I	27	56	3
16U45A0102	RT31016	IPR & PATENTS	29	48	2
16U45A0102	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
16U45A0102	RT31018	ENGINEERING GEOLOGY LAB	23	48	2
16U45A0103	RT31011	GEOTECHNICAL ENGINEERING-I	24	52	3
16U45A0103	RT31012	STRUCTURAL ANALYSIS-II	23	46	3
16U45A0103	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	39	3
16U45A0103	RT31014	ENGINEERING GEOLOGY	25	32	3
16U45A0103	RT31015	TRANSPORTATION ENGINEERING-I	27	30	3
16U45A0103	RT31016	IPR & PATENTS	25	45	2
16U45A0103	RT31017	GEOTECHNICAL ENGINEERING LAB	24	48	2
16U45A0103	RT31018	ENGINEERING GEOLOGY LAB	23	47	2
16U45A0104	RT31011	GEOTECHNICAL ENGINEERING-I	21	36	3
16U45A0104	RT31012	STRUCTURAL ANALYSIS-II	21	32	3
16U45A0104	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	18	41	3
16U45A0104	RT31014	ENGINEERING GEOLOGY	24	42	3
16U45A0104	RT31015	TRANSPORTATION ENGINEERING-I	25	37	3
16U45A0104	RT31016	IPR & PATENTS	13	14	0
16U45A0104	RT31017	GEOTECHNICAL ENGINEERING LAB	24	47	2
16U45A0104	RT31018	ENGINEERING GEOLOGY LAB	21	40	2
16U45A0105	RT31011	GEOTECHNICAL ENGINEERING-I	17	28	3
16U45A0105	RT31012	STRUCTURAL ANALYSIS-II	20	16	0
16U45A0105	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	27	26	3
16U45A0105	RT31014	ENGINEERING GEOLOGY	19	25	3
16U45A0105	RT31015	TRANSPORTATION ENGINEERING-I	21	37	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0105	RT31016	IPR & PATENTS	21	25	2
16U45A0105	RT31017	GEOTECHNICAL ENGINEERING LAB	25	34	2
16U45A0105	RT31018	ENGINEERING GEOLOGY LAB	19	43	2
16U45A0106	RT31011	GEOTECHNICAL ENGINEERING-I	29	24	3
16U45A0106	RT31012	STRUCTURAL ANALYSIS-II	28	44	3
16U45A0106	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	30	57	3
16U45A0106	RT31014	ENGINEERING GEOLOGY	27	41	3
16U45A0106	RT31015	TRANSPORTATION ENGINEERING-I	23	64	3
16U45A0106	RT31016	IPR & PATENTS	24	33	2
16U45A0106	RT31017	GEOTECHNICAL ENGINEERING LAB	25	47	2
16U45A0106	RT31018	ENGINEERING GEOLOGY LAB	23	49	2
16U45A0107	RT31011	GEOTECHNICAL ENGINEERING-I	19	35	3
16U45A0107	RT31012	STRUCTURAL ANALYSIS-II	23	43	3
16U45A0107	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	24	3
16U45A0107	RT31014	ENGINEERING GEOLOGY	25	30	3
16U45A0107	RT31015	TRANSPORTATION ENGINEERING-I	26	33	3
16U45A0107	RT31016	IPR & PATENTS	21	38	2
16U45A0107	RT31017	GEOTECHNICAL ENGINEERING LAB	25	34	2
16U45A0107	RT31018	ENGINEERING GEOLOGY LAB	22	45	2
16U45A0108	RT31011	GEOTECHNICAL ENGINEERING-I	29	53	3
16U45A0108	RT31012	STRUCTURAL ANALYSIS-II	25	52	3
16U45A0108	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	30	40	3
16U45A0108	RT31014	ENGINEERING GEOLOGY	24	36	3
16U45A0108	RT31015	TRANSPORTATION ENGINEERING-I	27	40	3
16U45A0108	RT31016	IPR & PATENTS	26	24	2
16U45A0108	RT31017	GEOTECHNICAL ENGINEERING LAB	24	46	2
16U45A0108	RT31018	ENGINEERING GEOLOGY LAB	23	50	2
16U45A0109	RT31011	GEOTECHNICAL ENGINEERING-I	26	30	3
16U45A0109	RT31012	STRUCTURAL ANALYSIS-II	27	54	3
16U45A0109	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	52	3
16U45A0109	RT31014	ENGINEERING GEOLOGY	29	15	0
16U45A0109	RT31015	TRANSPORTATION ENGINEERING-I	28	33	3
16U45A0109	RT31016	IPR & PATENTS	25	34	2
16U45A0109	RT31017	GEOTECHNICAL ENGINEERING LAB	25	49	2
16U45A0109	RT31018	ENGINEERING GEOLOGY LAB	25	50	2
16U45A0110	RT31011	GEOTECHNICAL ENGINEERING-I	25	17	0
16U45A0110	RT31012	STRUCTURAL ANALYSIS-II	28	51	3
16U45A0110	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	30	44	3
16U45A0110	RT31014	ENGINEERING GEOLOGY	26	42	3
16U45A0110	RT31015	TRANSPORTATION ENGINEERING-I	22	51	3
16U45A0110	RT31016	IPR & PATENTS	25	34	2
16U45A0110	RT31017	GEOTECHNICAL ENGINEERING LAB	25	48	2
16U45A0110	RT31018	ENGINEERING GEOLOGY LAB	22	45	2
16U45A0111	RT31011	GEOTECHNICAL ENGINEERING-I	26	50	3
16U45A0111	RT31012	STRUCTURAL ANALYSIS-II	28	42	3
16U45A0111	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	29	31	3
16U45A0111	RT31014	ENGINEERING GEOLOGY	26	34	3
16U45A0111	RT31015	TRANSPORTATION ENGINEERING-I	28	42	3
16U45A0111	RT31016	IPR & PATENTS	24	50	2
16U45A0111	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0111	RT31018	ENGINEERING GEOLOGY LAB	24	46	2
16U45A0112	RT31011	GEOTECHNICAL ENGINEERING-I	20	17	0
16U45A0112	RT31012	STRUCTURAL ANALYSIS-II	27	30	3
16U45A0112	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	27	36	3
16U45A0112	RT31014	ENGINEERING GEOLOGY	27	14	0
16U45A0112	RT31015	TRANSPORTATION ENGINEERING-I	23	30	3
16U45A0112	RT31016	IPR & PATENTS	24	24	2
16U45A0112	RT31017	GEOTECHNICAL ENGINEERING LAB	24	42	2
16U45A0112	RT31018	ENGINEERING GEOLOGY LAB	22	41	2
16U45A0113	RT31011	GEOTECHNICAL ENGINEERING-I	27	29	3
16U45A0113	RT31012	STRUCTURAL ANALYSIS-II	20	30	3
16U45A0113	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	29	48	3
16U45A0113	RT31014	ENGINEERING GEOLOGY	21	24	3
16U45A0113	RT31015	TRANSPORTATION ENGINEERING-I	24	34	3
16U45A0113	RT31016	IPR & PATENTS	23	38	2
16U45A0113	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
16U45A0113	RT31018	ENGINEERING GEOLOGY LAB	22	45	2
16U45A0114	RT31011	GEOTECHNICAL ENGINEERING-I	25	29	3
16U45A0114	RT31012	STRUCTURAL ANALYSIS-II	26	46	3
16U45A0114	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	26	47	3
16U45A0114	RT31014	ENGINEERING GEOLOGY	29	42	3
16U45A0114	RT31015	TRANSPORTATION ENGINEERING-I	26	65	3
16U45A0114	RT31016	IPR & PATENTS	23	26	2
16U45A0114	RT31017	GEOTECHNICAL ENGINEERING LAB	25	41	2
16U45A0114	RT31018	ENGINEERING GEOLOGY LAB	22	46	2
16U45A0115	RT31011	GEOTECHNICAL ENGINEERING-I	19	16	0
16U45A0115	RT31012	STRUCTURAL ANALYSIS-II	18	16	0
16U45A0115	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	6	12	0
16U45A0115	RT31014	ENGINEERING GEOLOGY	21	25	3
16U45A0115	RT31015	TRANSPORTATION ENGINEERING-I	17	31	3
16U45A0115	RT31016	IPR & PATENTS	13	47	2
16U45A0115	RT31017	GEOTECHNICAL ENGINEERING LAB	23	38	2
16U45A0115	RT31018	ENGINEERING GEOLOGY LAB	17	39	2
16U45A0116	RT31011	GEOTECHNICAL ENGINEERING-I	23	49	3
16U45A0116	RT31012	STRUCTURAL ANALYSIS-II	20	29	3
16U45A0116	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	25	35	3
16U45A0116	RT31014	ENGINEERING GEOLOGY	26	32	3
16U45A0116	RT31015	TRANSPORTATION ENGINEERING-I	21	29	3
16U45A0116	RT31016	IPR & PATENTS	25	24	2
16U45A0116	RT31017	GEOTECHNICAL ENGINEERING LAB	24	42	2
16U45A0116	RT31018	ENGINEERING GEOLOGY LAB	23	40	2
16U45A0117	RT31011	GEOTECHNICAL ENGINEERING-I	25	34	3
16U45A0117	RT31012	STRUCTURAL ANALYSIS-II	26	43	3
16U45A0117	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	27	44	3
16U45A0117	RT31014	ENGINEERING GEOLOGY	21	13	0
16U45A0117	RT31015	TRANSPORTATION ENGINEERING-I	27	44	3
16U45A0117	RT31016	IPR & PATENTS	17	40	2
16U45A0117	RT31017	GEOTECHNICAL ENGINEERING LAB	24	47	2
16U45A0117	RT31018	ENGINEERING GEOLOGY LAB	23	44	2
16U45A0118	RT31011	GEOTECHNICAL ENGINEERING-I	25	13	0

Htno	Subcode	Subname	Internal	External	Credits
16U45A0118	RT31012	STRUCTURAL ANALYSIS-II	21	41	3
16U45A0118	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	22	0
16U45A0118	RT31014	ENGINEERING GEOLOGY	26	27	3
16U45A0118	RT31015	TRANSPORTATION ENGINEERING-I	22	51	3
16U45A0118	RT31016	IPR & PATENTS	24	26	2
16U45A0118	RT31017	GEOTECHNICAL ENGINEERING LAB	22	46	2
16U45A0118	RT31018	ENGINEERING GEOLOGY LAB	17	40	2
16U45A0120	RT31011	GEOTECHNICAL ENGINEERING-I	21	38	3
16U45A0120	RT31012	STRUCTURAL ANALYSIS-II	24	37	3
16U45A0120	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	27	37	3
16U45A0120	RT31014	ENGINEERING GEOLOGY	24	26	3
16U45A0120	RT31015	TRANSPORTATION ENGINEERING-I	28	38	3
16U45A0120	RT31016	IPR & PATENTS	24	42	2
16U45A0120	RT31017	GEOTECHNICAL ENGINEERING LAB	25	39	2
16U45A0120	RT31018	ENGINEERING GEOLOGY LAB	23	46	2
16U45A0121	RT31011	GEOTECHNICAL ENGINEERING-I	8	0	0
16U45A0121	RT31012	STRUCTURAL ANALYSIS-II	11	7	0
16U45A0121	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	5	22	0
16U45A0121	RT31014	ENGINEERING GEOLOGY	17	26	3
16U45A0121	RT31015	TRANSPORTATION ENGINEERING-I	21	6	0
16U45A0121	RT31016	IPR & PATENTS	15	11	0
16U45A0121	RT31017	GEOTECHNICAL ENGINEERING LAB	19	34	2
16U45A0121	RT31018	ENGINEERING GEOLOGY LAB	0	38	2
16U45A0122	RT31011	GEOTECHNICAL ENGINEERING-I	26	28	3
16U45A0122	RT31012	STRUCTURAL ANALYSIS-II	27	38	3
16U45A0122	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	23	40	3
16U45A0122	RT31014	ENGINEERING GEOLOGY	28	14	0
16U45A0122	RT31015	TRANSPORTATION ENGINEERING-I	25	44	3
16U45A0122	RT31016	IPR & PATENTS	26	38	2
16U45A0122	RT31017	GEOTECHNICAL ENGINEERING LAB	25	42	2
16U45A0122	RT31018	ENGINEERING GEOLOGY LAB	23	44	2
16U45A0123	RT31011	GEOTECHNICAL ENGINEERING-I	29	27	3
16U45A0123	RT31012	STRUCTURAL ANALYSIS-II	30	63	3
16U45A0123	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	30	42	3
16U45A0123	RT31014	ENGINEERING GEOLOGY	29	33	3
16U45A0123	RT31015	TRANSPORTATION ENGINEERING-I	29	61	3
16U45A0123	RT31016	IPR & PATENTS	26	37	2
16U45A0123	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
16U45A0123	RT31018	ENGINEERING GEOLOGY LAB	25	50	2
16U45A0124	RT31011	GEOTECHNICAL ENGINEERING-I	21	17	0
16U45A0124	RT31012	STRUCTURAL ANALYSIS-II	22	33	3
16U45A0124	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	28	19	0
16U45A0124	RT31014	ENGINEERING GEOLOGY	28	25	3
16U45A0124	RT31015	TRANSPORTATION ENGINEERING-I	27	34	3
16U45A0124	RT31016	IPR & PATENTS	28	40	2
16U45A0124	RT31017	GEOTECHNICAL ENGINEERING LAB	22	42	2
16U45A0124	RT31018	ENGINEERING GEOLOGY LAB	20	40	2
16U45A0125	RT31011	GEOTECHNICAL ENGINEERING-I	24	37	3
16U45A0125	RT31012	STRUCTURAL ANALYSIS-II	23	48	3
16U45A0125	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	23	49	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0125	RT31014	ENGINEERING GEOLOGY	27	26	3
16U45A0125	RT31015	TRANSPORTATION ENGINEERING-I	22	37	3
16U45A0125	RT31016	IPR & PATENTS	24	26	2
16U45A0125	RT31017	GEOTECHNICAL ENGINEERING LAB	25	36	2
16U45A0125	RT31018	ENGINEERING GEOLOGY LAB	22	41	2
16U45A0126	RT31011	GEOTECHNICAL ENGINEERING-I	22	35	3
16U45A0126	RT31012	STRUCTURAL ANALYSIS-II	28	29	3
16U45A0126	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	23	36	3
16U45A0126	RT31014	ENGINEERING GEOLOGY	26	17	0
16U45A0126	RT31015	TRANSPORTATION ENGINEERING-I	24	44	3
16U45A0126	RT31016	IPR & PATENTS	24	25	2
16U45A0126	RT31017	GEOTECHNICAL ENGINEERING LAB	21	34	2
16U45A0126	RT31018	ENGINEERING GEOLOGY LAB	21	40	2
16U45A0127	RT31011	GEOTECHNICAL ENGINEERING-I	25	17	0
16U45A0127	RT31012	STRUCTURAL ANALYSIS-II	18	41	3
16U45A0127	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	25	21	0
16U45A0127	RT31014	ENGINEERING GEOLOGY	26	33	3
16U45A0127	RT31015	TRANSPORTATION ENGINEERING-I	21	47	3
16U45A0127	RT31016	IPR & PATENTS	19	26	2
16U45A0127	RT31017	GEOTECHNICAL ENGINEERING LAB	23	39	2
16U45A0127	RT31018	ENGINEERING GEOLOGY LAB	19	42	2
16U45A0128	RT31011	GEOTECHNICAL ENGINEERING-I	22	24	3
16U45A0128	RT31012	STRUCTURAL ANALYSIS-II	19	43	3
16U45A0128	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	21	52	3
16U45A0128	RT31014	ENGINEERING GEOLOGY	21	26	3
16U45A0128	RT31015	TRANSPORTATION ENGINEERING-I	26	24	3
16U45A0128	RT31016	IPR & PATENTS	25	27	2
16U45A0128	RT31017	GEOTECHNICAL ENGINEERING LAB	23	34	2
16U45A0128	RT31018	ENGINEERING GEOLOGY LAB	21	45	2
16U45A0129	RT31011	GEOTECHNICAL ENGINEERING-I	20	17	0
16U45A0129	RT31012	STRUCTURAL ANALYSIS-II	21	39	3
16U45A0129	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	25	31	3
16U45A0129	RT31014	ENGINEERING GEOLOGY	28	15	0
16U45A0129	RT31015	TRANSPORTATION ENGINEERING-I	25	29	3
16U45A0129	RT31016	IPR & PATENTS	22	10	0
16U45A0129	RT31017	GEOTECHNICAL ENGINEERING LAB	22	43	2
16U45A0129	RT31018	ENGINEERING GEOLOGY LAB	20	42	2
16U45A0130	RT31011	GEOTECHNICAL ENGINEERING-I	25	28	3
16U45A0130	RT31012	STRUCTURAL ANALYSIS-II	25	30	3
16U45A0130	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	24	40	3
16U45A0130	RT31014	ENGINEERING GEOLOGY	28	26	3
16U45A0130	RT31015	TRANSPORTATION ENGINEERING-I	28	39	3
16U45A0130	RT31016	IPR & PATENTS	22	31	2
16U45A0130	RT31017	GEOTECHNICAL ENGINEERING LAB	24	40	2
16U45A0130	RT31018	ENGINEERING GEOLOGY LAB	21	42	2
16U45A0131	RT31011	GEOTECHNICAL ENGINEERING-I	28	24	3
16U45A0131	RT31012	STRUCTURAL ANALYSIS-II	27	50	3
16U45A0131	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	23	38	3
16U45A0131	RT31014	ENGINEERING GEOLOGY	28	30	3
16U45A0131	RT31015	TRANSPORTATION ENGINEERING-I	26	31	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0131	RT31016	IPR & PATENTS	27	27	2
16U45A0131	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
16U45A0131	RT31018	ENGINEERING GEOLOGY LAB	24	47	2
16U45A0132	RT31011	GEOTECHNICAL ENGINEERING-I	27	42	3
16U45A0132	RT31012	STRUCTURAL ANALYSIS-II	27	41	3
16U45A0132	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	24	40	3
16U45A0132	RT31014	ENGINEERING GEOLOGY	25	46	3
16U45A0132	RT31015	TRANSPORTATION ENGINEERING-I	27	38	3
16U45A0132	RT31016	IPR & PATENTS	19	36	2
16U45A0132	RT31017	GEOTECHNICAL ENGINEERING LAB	25	47	2
16U45A0132	RT31018	ENGINEERING GEOLOGY LAB	22	46	2
16U45A0133	RT31011	GEOTECHNICAL ENGINEERING-I	22	35	3
16U45A0133	RT31012	STRUCTURAL ANALYSIS-II	22	33	3
16U45A0133	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	19	31	3
16U45A0133	RT31014	ENGINEERING GEOLOGY	22	27	3
16U45A0133	RT31015	TRANSPORTATION ENGINEERING-I	25	33	3
16U45A0133	RT31016	IPR & PATENTS	18	17	0
16U45A0133	RT31017	GEOTECHNICAL ENGINEERING LAB	24	41	2
16U45A0133	RT31018	ENGINEERING GEOLOGY LAB	19	39	2
16U45A0134	RT31011	GEOTECHNICAL ENGINEERING-I	22	29	3
16U45A0134	RT31012	STRUCTURAL ANALYSIS-II	14	26	3
16U45A0134	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	29	31	3
16U45A0134	RT31014	ENGINEERING GEOLOGY	19	12	0
16U45A0134	RT31015	TRANSPORTATION ENGINEERING-I	18	40	3
16U45A0134	RT31016	IPR & PATENTS	20	38	2
16U45A0134	RT31017	GEOTECHNICAL ENGINEERING LAB	21	34	2
16U45A0134	RT31018	ENGINEERING GEOLOGY LAB	21	42	2
16U45A0135	RT31011	GEOTECHNICAL ENGINEERING-I	22	27	3
16U45A0135	RT31012	STRUCTURAL ANALYSIS-II	28	48	3
16U45A0135	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	15	44	3
16U45A0135	RT31014	ENGINEERING GEOLOGY	28	33	3
16U45A0135	RT31015	TRANSPORTATION ENGINEERING-I	23	32	3
16U45A0135	RT31016	IPR & PATENTS	22	26	2
16U45A0135	RT31017	GEOTECHNICAL ENGINEERING LAB	23	39	2
16U45A0135	RT31018	ENGINEERING GEOLOGY LAB	24	50	2
16U45A0136	RT31011	GEOTECHNICAL ENGINEERING-I	26	61	3
16U45A0136	RT31012	STRUCTURAL ANALYSIS-II	26	49	3
16U45A0136	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	20	40	3
16U45A0136	RT31014	ENGINEERING GEOLOGY	24	41	3
16U45A0136	RT31015	TRANSPORTATION ENGINEERING-I	25	39	3
16U45A0136	RT31016	IPR & PATENTS	26	43	2
16U45A0136	RT31017	GEOTECHNICAL ENGINEERING LAB	25	47	2
16U45A0136	RT31018	ENGINEERING GEOLOGY LAB	23	45	2
16U45A0137	RT31011	GEOTECHNICAL ENGINEERING-I	24	46	3
16U45A0137	RT31012	STRUCTURAL ANALYSIS-II	25	51	3
16U45A0137	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	29	57	3
16U45A0137	RT31014	ENGINEERING GEOLOGY	27	40	3
16U45A0137	RT31015	TRANSPORTATION ENGINEERING-I	24	36	3
16U45A0137	RT31016	IPR & PATENTS	22	24	2
16U45A0137	RT31017	GEOTECHNICAL ENGINEERING LAB	24	45	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0137	RT31018	ENGINEERING GEOLOGY LAB	22	43	2
16U45A0138	RT31011	GEOTECHNICAL ENGINEERING-I	27	36	3
16U45A0138	RT31012	STRUCTURAL ANALYSIS-II	19	33	3
16U45A0138	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	29	49	3
16U45A0138	RT31014	ENGINEERING GEOLOGY	26	32	3
16U45A0138	RT31015	TRANSPORTATION ENGINEERING-I	24	43	3
16U45A0138	RT31016	IPR & PATENTS	19	40	2
16U45A0138	RT31017	GEOTECHNICAL ENGINEERING LAB	24	32	2
16U45A0138	RT31018	ENGINEERING GEOLOGY LAB	23	46	2
16U45A0139	RT31011	GEOTECHNICAL ENGINEERING-I	21	13	0
16U45A0139	RT31012	STRUCTURAL ANALYSIS-II	23	42	3
16U45A0139	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	20	44	3
16U45A0139	RT31014	ENGINEERING GEOLOGY	28	17	0
16U45A0139	RT31015	TRANSPORTATION ENGINEERING-I	25	37	3
16U45A0139	RT31016	IPR & PATENTS	28	27	2
16U45A0139	RT31017	GEOTECHNICAL ENGINEERING LAB	25	42	2
16U45A0139	RT31018	ENGINEERING GEOLOGY LAB	21	45	2
16U45A0140	RT31011	GEOTECHNICAL ENGINEERING-I	18	24	3
16U45A0140	RT31012	STRUCTURAL ANALYSIS-II	12	7	0
16U45A0140	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	17	10	0
16U45A0140	RT31014	ENGINEERING GEOLOGY	21	-1	0
16U45A0140	RT31015	TRANSPORTATION ENGINEERING-I	10	16	0
16U45A0140	RT31016	IPR & PATENTS	5	37	2
16U45A0140	RT31017	GEOTECHNICAL ENGINEERING LAB	21	30	2
16U45A0140	RT31018	ENGINEERING GEOLOGY LAB	15	35	2
16U45A0141	RT31011	GEOTECHNICAL ENGINEERING-I	29	60	3
16U45A0141	RT31012	STRUCTURAL ANALYSIS-II	23	46	3
16U45A0141	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	21	54	3
16U45A0141	RT31014	ENGINEERING GEOLOGY	26	42	3
16U45A0141	RT31015	TRANSPORTATION ENGINEERING-I	27	50	3
16U45A0141	RT31016	IPR & PATENTS	24	30	2
16U45A0141	RT31017	GEOTECHNICAL ENGINEERING LAB	25	50	2
16U45A0141	RT31018	ENGINEERING GEOLOGY LAB	21	44	2
16U45A0143	RT31011	GEOTECHNICAL ENGINEERING-I	28	32	3
16U45A0143	RT31012	STRUCTURAL ANALYSIS-II	18	37	3
16U45A0143	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	24	31	3
16U45A0143	RT31014	ENGINEERING GEOLOGY	27	25	3
16U45A0143	RT31015	TRANSPORTATION ENGINEERING-I	18	37	3
16U45A0143	RT31016	IPR & PATENTS	18	7	0
16U45A0143	RT31017	GEOTECHNICAL ENGINEERING LAB	25	47	2
16U45A0143	RT31018	ENGINEERING GEOLOGY LAB	21	40	2
16U45A0144	RT31011	GEOTECHNICAL ENGINEERING-I	17	24	3
16U45A0144	RT31012	STRUCTURAL ANALYSIS-II	16	29	3
16U45A0144	RT31013	DESIGN AND DRAWING OF REINFORCED CONCRETE S	15	31	3
16U45A0144	RT31014	ENGINEERING GEOLOGY	23	24	3
16U45A0144	RT31015	TRANSPORTATION ENGINEERING-I	19	36	3
16U45A0144	RT31016	IPR & PATENTS	22	25	2
16U45A0144	RT31017	GEOTECHNICAL ENGINEERING LAB	25	43	2
16U45A0144	RT31018	ENGINEERING GEOLOGY LAB	20	42	2
16U45A0201	RT31016	IPR & PATENTS	27	41	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0201	RT31021	ELECTRICAL MEASUREMENTS	29	51	3
16U45A0201	RT31022	MEFA	29	52	3
16U45A0201	RT31023	POWER SYSTEMS-II	29	30	3
16U45A0201	RT31024	ELECTRICAL MACHINES-III	28	59	3
16U45A0201	RT31025	POWER ELECTRONICS	29	36	3
16U45A0201	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	33	3
16U45A0201	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
16U45A0201	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0202	RT31016	IPR & PATENTS	28	28	2
16U45A0202	RT31021	ELECTRICAL MEASUREMENTS	28	34	3
16U45A0202	RT31022	MEFA	26	60	3
16U45A0202	RT31023	POWER SYSTEMS-II	30	42	3
16U45A0202	RT31024	ELECTRICAL MACHINES-III	30	47	3
16U45A0202	RT31025	POWER ELECTRONICS	25	33	3
16U45A0202	RT31026	LINEAR & DIGITAL IC APPLICATIONS	29	43	3
16U45A0202	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
16U45A0202	RT31028	CONTROL SYSTEMS LAB	24	50	2
16U45A0203	RT31016	IPR & PATENTS	23	34	2
16U45A0203	RT31021	ELECTRICAL MEASUREMENTS	28	47	3
16U45A0203	RT31022	MEFA	28	42	3
16U45A0203	RT31023	POWER SYSTEMS-II	29	70	3
16U45A0203	RT31024	ELECTRICAL MACHINES-III	28	48	3
16U45A0203	RT31025	POWER ELECTRONICS	24	58	3
16U45A0203	RT31026	LINEAR & DIGITAL IC APPLICATIONS	28	35	3
16U45A0203	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
16U45A0203	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0204	RT31016	IPR & PATENTS	28	40	2
16U45A0204	RT31021	ELECTRICAL MEASUREMENTS	26	34	3
16U45A0204	RT31022	MEFA	28	35	3
16U45A0204	RT31023	POWER SYSTEMS-II	29	31	3
16U45A0204	RT31024	ELECTRICAL MACHINES-III	28	39	3
16U45A0204	RT31025	POWER ELECTRONICS	26	40	3
16U45A0204	RT31026	LINEAR & DIGITAL IC APPLICATIONS	27	35	3
16U45A0204	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
16U45A0204	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0205	RT31016	IPR & PATENTS	26	40	2
16U45A0205	RT31021	ELECTRICAL MEASUREMENTS	25	56	3
16U45A0205	RT31022	MEFA	20	44	3
16U45A0205	RT31023	POWER SYSTEMS-II	20	-1	0
16U45A0205	RT31024	ELECTRICAL MACHINES-III	20	44	3
16U45A0205	RT31025	POWER ELECTRONICS	18	24	3
16U45A0205	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	25	3
16U45A0205	RT31027	ELECTRICAL MACHINES-II LAB	21	41	2
16U45A0205	RT31028	CONTROL SYSTEMS LAB	22	49	2
16U45A0206	RT31016	IPR & PATENTS	26	33	2
16U45A0206	RT31021	ELECTRICAL MEASUREMENTS	27	32	3
16U45A0206	RT31022	MEFA	25	56	3
16U45A0206	RT31023	POWER SYSTEMS-II	26	35	3
16U45A0206	RT31024	ELECTRICAL MACHINES-III	27	48	3
16U45A0206	RT31025	POWER ELECTRONICS	26	40	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0206	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	41	3
16U45A0206	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
16U45A0206	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0207	RT31016	IPR & PATENTS	21	28	2
16U45A0207	RT31021	ELECTRICAL MEASUREMENTS	22	39	3
16U45A0207	RT31022	MEFA	17	34	3
16U45A0207	RT31023	POWER SYSTEMS-II	22	55	3
16U45A0207	RT31024	ELECTRICAL MACHINES-III	23	41	3
16U45A0207	RT31025	POWER ELECTRONICS	20	44	3
16U45A0207	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	38	3
16U45A0207	RT31027	ELECTRICAL MACHINES-II LAB	23	40	2
16U45A0207	RT31028	CONTROL SYSTEMS LAB	22	46	2
16U45A0208	RT31016	IPR & PATENTS	22	42	2
16U45A0208	RT31021	ELECTRICAL MEASUREMENTS	19	27	3
16U45A0208	RT31022	MEFA	19	34	3
16U45A0208	RT31023	POWER SYSTEMS-II	18	24	3
16U45A0208	RT31024	ELECTRICAL MACHINES-III	20	17	0
16U45A0208	RT31025	POWER ELECTRONICS	20	29	3
16U45A0208	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	24	3
16U45A0208	RT31027	ELECTRICAL MACHINES-II LAB	22	45	2
16U45A0208	RT31028	CONTROL SYSTEMS LAB	22	46	2
16U45A0209	RT31016	IPR & PATENTS	21	30	2
16U45A0209	RT31021	ELECTRICAL MEASUREMENTS	27	47	3
16U45A0209	RT31022	MEFA	22	42	3
16U45A0209	RT31023	POWER SYSTEMS-II	26	32	3
16U45A0209	RT31024	ELECTRICAL MACHINES-III	22	36	3
16U45A0209	RT31025	POWER ELECTRONICS	23	24	3
16U45A0209	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	25	3
16U45A0209	RT31027	ELECTRICAL MACHINES-II LAB	23	48	2
16U45A0209	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0210	RT31016	IPR & PATENTS	19	24	2
16U45A0210	RT31021	ELECTRICAL MEASUREMENTS	18	26	3
16U45A0210	RT31022	MEFA	23	41	3
16U45A0210	RT31023	POWER SYSTEMS-II	14	35	3
16U45A0210	RT31024	ELECTRICAL MACHINES-III	19	45	3
16U45A0210	RT31025	POWER ELECTRONICS	19	32	3
16U45A0210	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	17	0
16U45A0210	RT31027	ELECTRICAL MACHINES-II LAB	20	47	2
16U45A0210	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0211	RT31016	IPR & PATENTS	23	26	2
16U45A0211	RT31021	ELECTRICAL MEASUREMENTS	26	32	3
16U45A0211	RT31022	MEFA	23	40	3
16U45A0211	RT31023	POWER SYSTEMS-II	23	70	3
16U45A0211	RT31024	ELECTRICAL MACHINES-III	26	56	3
16U45A0211	RT31025	POWER ELECTRONICS	25	51	3
16U45A0211	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	48	3
16U45A0211	RT31027	ELECTRICAL MACHINES-II LAB	25	50	2
16U45A0211	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0212	RT31016	IPR & PATENTS	23	46	2
16U45A0212	RT31021	ELECTRICAL MEASUREMENTS	29	46	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0212	RT31022	MEFA	26	40	3
16U45A0212	RT31023	POWER SYSTEMS-II	26	38	3
16U45A0212	RT31024	ELECTRICAL MACHINES-III	27	46	3
16U45A0212	RT31025	POWER ELECTRONICS	28	53	3
16U45A0212	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	64	3
16U45A0212	RT31027	ELECTRICAL MACHINES-II LAB	20	50	2
16U45A0212	RT31028	CONTROL SYSTEMS LAB	23	47	2
16U45A0213	RT31016	IPR & PATENTS	27	40	2
16U45A0213	RT31021	ELECTRICAL MEASUREMENTS	25	45	3
16U45A0213	RT31022	MEFA	20	50	3
16U45A0213	RT31023	POWER SYSTEMS-II	18	28	3
16U45A0213	RT31024	ELECTRICAL MACHINES-III	19	37	3
16U45A0213	RT31025	POWER ELECTRONICS	22	11	0
16U45A0213	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	24	3
16U45A0213	RT31027	ELECTRICAL MACHINES-II LAB	22	43	2
16U45A0213	RT31028	CONTROL SYSTEMS LAB	23	47	2
16U45A0214	RT31016	IPR & PATENTS	20	25	2
16U45A0214	RT31021	ELECTRICAL MEASUREMENTS	22	24	3
16U45A0214	RT31022	MEFA	21	42	3
16U45A0214	RT31023	POWER SYSTEMS-II	20	35	3
16U45A0214	RT31024	ELECTRICAL MACHINES-III	23	34	3
16U45A0214	RT31025	POWER ELECTRONICS	21	34	3
16U45A0214	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	11	0
16U45A0214	RT31027	ELECTRICAL MACHINES-II LAB	22	43	2
16U45A0214	RT31028	CONTROL SYSTEMS LAB	24	43	2
16U45A0215	RT31016	IPR & PATENTS	27	26	2
16U45A0215	RT31021	ELECTRICAL MEASUREMENTS	26	31	3
16U45A0215	RT31022	MEFA	25	42	3
16U45A0215	RT31023	POWER SYSTEMS-II	30	53	3
16U45A0215	RT31024	ELECTRICAL MACHINES-III	29	48	3
16U45A0215	RT31025	POWER ELECTRONICS	25	36	3
16U45A0215	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	37	3
16U45A0215	RT31027	ELECTRICAL MACHINES-II LAB	24	50	2
16U45A0215	RT31028	CONTROL SYSTEMS LAB	24	47	2
16U45A0216	RT31016	IPR & PATENTS	27	46	2
16U45A0216	RT31021	ELECTRICAL MEASUREMENTS	25	31	3
16U45A0216	RT31022	MEFA	23	29	3
16U45A0216	RT31023	POWER SYSTEMS-II	27	35	3
16U45A0216	RT31024	ELECTRICAL MACHINES-III	25	30	3
16U45A0216	RT31025	POWER ELECTRONICS	19	40	3
16U45A0216	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	24	3
16U45A0216	RT31027	ELECTRICAL MACHINES-II LAB	20	39	2
16U45A0216	RT31028	CONTROL SYSTEMS LAB	21	41	2
16U45A0217	RT31016	IPR & PATENTS	25	30	2
16U45A0217	RT31021	ELECTRICAL MEASUREMENTS	23	30	3
16U45A0217	RT31022	MEFA	23	40	3
16U45A0217	RT31023	POWER SYSTEMS-II	17	34	3
16U45A0217	RT31024	ELECTRICAL MACHINES-III	25	29	3
16U45A0217	RT31025	POWER ELECTRONICS	19	8	0
16U45A0217	RT31026	LINEAR & DIGITAL IC APPLICATIONS	19	24	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0217	RT31027	ELECTRICAL MACHINES-II LAB	20	39	2
16U45A0217	RT31028	CONTROL SYSTEMS LAB	24	45	2
16U45A0218	RT31016	IPR & PATENTS	21	13	0
16U45A0218	RT31021	ELECTRICAL MEASUREMENTS	22	6	0
16U45A0218	RT31022	MEFA	19	13	0
16U45A0218	RT31023	POWER SYSTEMS-II	19	24	3
16U45A0218	RT31024	ELECTRICAL MACHINES-III	22	25	3
16U45A0218	RT31025	POWER ELECTRONICS	22	16	0
16U45A0218	RT31026	LINEAR & DIGITAL IC APPLICATIONS	16	9	0
16U45A0218	RT31027	ELECTRICAL MACHINES-II LAB	21	41	2
16U45A0218	RT31028	CONTROL SYSTEMS LAB	22	44	2
16U45A0220	RT31016	IPR & PATENTS	20	25	2
16U45A0220	RT31021	ELECTRICAL MEASUREMENTS	22	27	3
16U45A0220	RT31022	MEFA	22	38	3
16U45A0220	RT31023	POWER SYSTEMS-II	21	43	3
16U45A0220	RT31024	ELECTRICAL MACHINES-III	25	44	3
16U45A0220	RT31025	POWER ELECTRONICS	22	50	3
16U45A0220	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	16	0
16U45A0220	RT31027	ELECTRICAL MACHINES-II LAB	24	48	2
16U45A0220	RT31028	CONTROL SYSTEMS LAB	23	44	2
16U45A0221	RT31016	IPR & PATENTS	24	44	2
16U45A0221	RT31021	ELECTRICAL MEASUREMENTS	26	41	3
16U45A0221	RT31022	MEFA	28	41	3
16U45A0221	RT31023	POWER SYSTEMS-II	24	42	3
16U45A0221	RT31024	ELECTRICAL MACHINES-III	27	34	3
16U45A0221	RT31025	POWER ELECTRONICS	24	40	3
16U45A0221	RT31026	LINEAR & DIGITAL IC APPLICATIONS	27	48	3
16U45A0221	RT31027	ELECTRICAL MACHINES-II LAB	18	40	2
16U45A0221	RT31028	CONTROL SYSTEMS LAB	24	43	2
16U45A0222	RT31016	IPR & PATENTS	23	40	2
16U45A0222	RT31021	ELECTRICAL MEASUREMENTS	27	37	3
16U45A0222	RT31022	MEFA	20	49	3
16U45A0222	RT31023	POWER SYSTEMS-II	26	36	3
16U45A0222	RT31024	ELECTRICAL MACHINES-III	29	70	3
16U45A0222	RT31025	POWER ELECTRONICS	27	15	0
16U45A0222	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	38	3
16U45A0222	RT31027	ELECTRICAL MACHINES-II LAB	25	48	2
16U45A0222	RT31028	CONTROL SYSTEMS LAB	25	46	2
16U45A0223	RT31016	IPR & PATENTS	23	29	2
16U45A0223	RT31021	ELECTRICAL MEASUREMENTS	24	26	3
16U45A0223	RT31022	MEFA	24	38	3
16U45A0223	RT31023	POWER SYSTEMS-II	29	31	3
16U45A0223	RT31024	ELECTRICAL MACHINES-III	28	34	3
16U45A0223	RT31025	POWER ELECTRONICS	25	34	3
16U45A0223	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	26	3
16U45A0223	RT31027	ELECTRICAL MACHINES-II LAB	21	46	2
16U45A0223	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0224	RT31016	IPR & PATENTS	23	34	2
16U45A0224	RT31021	ELECTRICAL MEASUREMENTS	26	32	3
16U45A0224	RT31022	MEFA	26	39	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0224	RT31023	POWER SYSTEMS-II	26	70	3
16U45A0224	RT31024	ELECTRICAL MACHINES-III	26	51	3
16U45A0224	RT31025	POWER ELECTRONICS	24	52	3
16U45A0224	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	48	3
16U45A0224	RT31027	ELECTRICAL MACHINES-II LAB	20	46	2
16U45A0224	RT31028	CONTROL SYSTEMS LAB	24	46	2
16U45A0225	RT31016	IPR & PATENTS	26	42	2
16U45A0225	RT31021	ELECTRICAL MEASUREMENTS	22	13	0
16U45A0225	RT31022	MEFA	27	30	3
16U45A0225	RT31023	POWER SYSTEMS-II	22	14	0
16U45A0225	RT31024	ELECTRICAL MACHINES-III	25	24	3
16U45A0225	RT31025	POWER ELECTRONICS	21	26	3
16U45A0225	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	24	3
16U45A0225	RT31027	ELECTRICAL MACHINES-II LAB	22	44	2
16U45A0225	RT31028	CONTROL SYSTEMS LAB	23	46	2
16U45A0226	RT31016	IPR & PATENTS	24	45	2
16U45A0226	RT31021	ELECTRICAL MEASUREMENTS	26	47	3
16U45A0226	RT31022	MEFA	26	55	3
16U45A0226	RT31023	POWER SYSTEMS-II	27	45	3
16U45A0226	RT31024	ELECTRICAL MACHINES-III	24	39	3
16U45A0226	RT31025	POWER ELECTRONICS	25	39	3
16U45A0226	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	34	3
16U45A0226	RT31027	ELECTRICAL MACHINES-II LAB	24	49	2
16U45A0226	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0227	RT31016	IPR & PATENTS	21	24	2
16U45A0227	RT31021	ELECTRICAL MEASUREMENTS	21	5	0
16U45A0227	RT31022	MEFA	19	48	3
16U45A0227	RT31023	POWER SYSTEMS-II	23	25	3
16U45A0227	RT31024	ELECTRICAL MACHINES-III	21	40	3
16U45A0227	RT31025	POWER ELECTRONICS	21	33	3
16U45A0227	RT31026	LINEAR & DIGITAL IC APPLICATIONS	15	17	0
16U45A0227	RT31027	ELECTRICAL MACHINES-II LAB	21	45	2
16U45A0227	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0228	RT31016	IPR & PATENTS	24	29	2
16U45A0228	RT31021	ELECTRICAL MEASUREMENTS	26	26	3
16U45A0228	RT31022	MEFA	23	31	3
16U45A0228	RT31023	POWER SYSTEMS-II	29	47	3
16U45A0228	RT31024	ELECTRICAL MACHINES-III	28	29	3
16U45A0228	RT31025	POWER ELECTRONICS	24	48	3
16U45A0228	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	28	3
16U45A0228	RT31027	ELECTRICAL MACHINES-II LAB	24	44	2
16U45A0228	RT31028	CONTROL SYSTEMS LAB	24	49	2
16U45A0229	RT31016	IPR & PATENTS	19	41	2
16U45A0229	RT31021	ELECTRICAL MEASUREMENTS	23	39	3
16U45A0229	RT31022	MEFA	24	30	3
16U45A0229	RT31023	POWER SYSTEMS-II	26	34	3
16U45A0229	RT31024	ELECTRICAL MACHINES-III	27	30	3
16U45A0229	RT31025	POWER ELECTRONICS	24	47	3
16U45A0229	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	39	3
16U45A0229	RT31027	ELECTRICAL MACHINES-II LAB	22	45	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0229	RT31028	CONTROL SYSTEMS LAB	24	49	2
16U45A0230	RT31016	IPR & PATENTS	24	44	2
16U45A0230	RT31021	ELECTRICAL MEASUREMENTS	23	48	3
16U45A0230	RT31022	MEFA	19	63	3
16U45A0230	RT31023	POWER SYSTEMS-II	19	30	3
16U45A0230	RT31024	ELECTRICAL MACHINES-III	24	32	3
16U45A0230	RT31025	POWER ELECTRONICS	20	24	3
16U45A0230	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	24	3
16U45A0230	RT31027	ELECTRICAL MACHINES-II LAB	23	44	2
16U45A0230	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0231	RT31016	IPR & PATENTS	23	28	2
16U45A0231	RT31021	ELECTRICAL MEASUREMENTS	27	32	3
16U45A0231	RT31022	MEFA	26	44	3
16U45A0231	RT31023	POWER SYSTEMS-II	28	38	3
16U45A0231	RT31024	ELECTRICAL MACHINES-III	24	48	3
16U45A0231	RT31025	POWER ELECTRONICS	26	34	3
16U45A0231	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	27	3
16U45A0231	RT31027	ELECTRICAL MACHINES-II LAB	24	46	2
16U45A0231	RT31028	CONTROL SYSTEMS LAB	25	49	2
16U45A0232	RT31016	IPR & PATENTS	24	25	2
16U45A0232	RT31021	ELECTRICAL MEASUREMENTS	25	37	3
16U45A0232	RT31022	MEFA	27	34	3
16U45A0232	RT31023	POWER SYSTEMS-II	20	62	3
16U45A0232	RT31024	ELECTRICAL MACHINES-III	26	24	3
16U45A0232	RT31025	POWER ELECTRONICS	23	49	3
16U45A0232	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	45	3
16U45A0232	RT31027	ELECTRICAL MACHINES-II LAB	23	43	2
16U45A0232	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0233	RT31016	IPR & PATENTS	16	-1	0
16U45A0233	RT31021	ELECTRICAL MEASUREMENTS	20	-1	0
16U45A0233	RT31022	MEFA	18	-1	0
16U45A0233	RT31023	POWER SYSTEMS-II	19	-1	0
16U45A0233	RT31024	ELECTRICAL MACHINES-III	19	-1	0
16U45A0233	RT31025	POWER ELECTRONICS	22	-1	0
16U45A0233	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	-1	0
16U45A0233	RT31027	ELECTRICAL MACHINES-II LAB	21	-1	0
16U45A0233	RT31028	CONTROL SYSTEMS LAB	22	-1	0
16U45A0234	RT31016	IPR & PATENTS	23	34	2
16U45A0234	RT31021	ELECTRICAL MEASUREMENTS	24	38	3
16U45A0234	RT31022	MEFA	29	44	3
16U45A0234	RT31023	POWER SYSTEMS-II	23	32	3
16U45A0234	RT31024	ELECTRICAL MACHINES-III	29	56	3
16U45A0234	RT31025	POWER ELECTRONICS	26	36	3
16U45A0234	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	34	3
16U45A0234	RT31027	ELECTRICAL MACHINES-II LAB	24	48	2
16U45A0234	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0235	RT31016	IPR & PATENTS	20	28	2
16U45A0235	RT31021	ELECTRICAL MEASUREMENTS	20	29	3
16U45A0235	RT31022	MEFA	17	33	3
16U45A0235	RT31023	POWER SYSTEMS-II	20	24	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0235	RT31024	ELECTRICAL MACHINES-III	15	30	3
16U45A0235	RT31025	POWER ELECTRONICS	21	36	3
16U45A0235	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	13	0
16U45A0235	RT31027	ELECTRICAL MACHINES-II LAB	21	42	2
16U45A0235	RT31028	CONTROL SYSTEMS LAB	24	49	2
16U45A0236	RT31016	IPR & PATENTS	27	32	2
16U45A0236	RT31021	ELECTRICAL MEASUREMENTS	26	42	3
16U45A0236	RT31022	MEFA	28	40	3
16U45A0236	RT31023	POWER SYSTEMS-II	25	70	3
16U45A0236	RT31024	ELECTRICAL MACHINES-III	27	47	3
16U45A0236	RT31025	POWER ELECTRONICS	22	51	3
16U45A0236	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	60	3
16U45A0236	RT31027	ELECTRICAL MACHINES-II LAB	24	49	2
16U45A0236	RT31028	CONTROL SYSTEMS LAB	23	50	2
16U45A0237	RT31016	IPR & PATENTS	25	47	2
16U45A0237	RT31021	ELECTRICAL MEASUREMENTS	26	33	3
16U45A0237	RT31022	MEFA	27	31	3
16U45A0237	RT31023	POWER SYSTEMS-II	26	25	3
16U45A0237	RT31024	ELECTRICAL MACHINES-III	26	34	3
16U45A0237	RT31025	POWER ELECTRONICS	19	41	3
16U45A0237	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	27	3
16U45A0237	RT31027	ELECTRICAL MACHINES-II LAB	23	43	2
16U45A0237	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0238	RT31016	IPR & PATENTS	22	38	2
16U45A0238	RT31021	ELECTRICAL MEASUREMENTS	18	13	0
16U45A0238	RT31022	MEFA	22	46	3
16U45A0238	RT31023	POWER SYSTEMS-II	25	30	3
16U45A0238	RT31024	ELECTRICAL MACHINES-III	25	12	0
16U45A0238	RT31025	POWER ELECTRONICS	15	15	0
16U45A0238	RT31026	LINEAR & DIGITAL IC APPLICATIONS	17	10	0
16U45A0238	RT31027	ELECTRICAL MACHINES-II LAB	23	47	2
16U45A0238	RT31028	CONTROL SYSTEMS LAB	22	47	2
16U45A0239	RT31016	IPR & PATENTS	15	25	2
16U45A0239	RT31021	ELECTRICAL MEASUREMENTS	20	15	0
16U45A0239	RT31022	MEFA	19	40	3
16U45A0239	RT31023	POWER SYSTEMS-II	19	10	0
16U45A0239	RT31024	ELECTRICAL MACHINES-III	18	24	3
16U45A0239	RT31025	POWER ELECTRONICS	21	28	3
16U45A0239	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	8	0
16U45A0239	RT31027	ELECTRICAL MACHINES-II LAB	20	41	2
16U45A0239	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0240	RT31016	IPR & PATENTS	20	31	2
16U45A0240	RT31021	ELECTRICAL MEASUREMENTS	25	17	0
16U45A0240	RT31022	MEFA	22	30	3
16U45A0240	RT31023	POWER SYSTEMS-II	23	53	3
16U45A0240	RT31024	ELECTRICAL MACHINES-III	28	53	3
16U45A0240	RT31025	POWER ELECTRONICS	20	37	3
16U45A0240	RT31026	LINEAR & DIGITAL IC APPLICATIONS	26	40	3
16U45A0240	RT31027	ELECTRICAL MACHINES-II LAB	24	49	2
16U45A0240	RT31028	CONTROL SYSTEMS LAB	25	50	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0241	RT31016	IPR & PATENTS	21	47	2
16U45A0241	RT31021	ELECTRICAL MEASUREMENTS	21	13	0
16U45A0241	RT31022	MEFA	18	34	3
16U45A0241	RT31023	POWER SYSTEMS-II	19	16	0
16U45A0241	RT31024	ELECTRICAL MACHINES-III	22	32	3
16U45A0241	RT31025	POWER ELECTRONICS	19	32	3
16U45A0241	RT31026	LINEAR & DIGITAL IC APPLICATIONS	20	8	0
16U45A0241	RT31027	ELECTRICAL MACHINES-II LAB	20	43	2
16U45A0241	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0242	RT31016	IPR & PATENTS	23	53	2
16U45A0242	RT31021	ELECTRICAL MEASUREMENTS	24	46	3
16U45A0242	RT31022	MEFA	23	47	3
16U45A0242	RT31023	POWER SYSTEMS-II	25	24	3
16U45A0242	RT31024	ELECTRICAL MACHINES-III	26	27	3
16U45A0242	RT31025	POWER ELECTRONICS	24	8	0
16U45A0242	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	30	3
16U45A0242	RT31027	ELECTRICAL MACHINES-II LAB	24	48	2
16U45A0242	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0243	RT31016	IPR & PATENTS	20	29	2
16U45A0243	RT31021	ELECTRICAL MEASUREMENTS	20	14	0
16U45A0243	RT31022	MEFA	19	34	3
16U45A0243	RT31023	POWER SYSTEMS-II	20	27	3
16U45A0243	RT31024	ELECTRICAL MACHINES-III	19	31	3
16U45A0243	RT31025	POWER ELECTRONICS	20	34	3
16U45A0243	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	34	3
16U45A0243	RT31027	ELECTRICAL MACHINES-II LAB	22	42	2
16U45A0243	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0244	RT31016	IPR & PATENTS	20	25	2
16U45A0244	RT31021	ELECTRICAL MEASUREMENTS	22	26	3
16U45A0244	RT31022	MEFA	21	31	3
16U45A0244	RT31023	POWER SYSTEMS-II	28	47	3
16U45A0244	RT31024	ELECTRICAL MACHINES-III	18	36	3
16U45A0244	RT31025	POWER ELECTRONICS	21	16	0
16U45A0244	RT31026	LINEAR & DIGITAL IC APPLICATIONS	20	28	3
16U45A0244	RT31027	ELECTRICAL MACHINES-II LAB	22	45	2
16U45A0244	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0245	RT31016	IPR & PATENTS	27	43	2
16U45A0245	RT31021	ELECTRICAL MEASUREMENTS	28	42	3
16U45A0245	RT31022	MEFA	26	40	3
16U45A0245	RT31023	POWER SYSTEMS-II	27	49	3
16U45A0245	RT31024	ELECTRICAL MACHINES-III	23	33	3
16U45A0245	RT31025	POWER ELECTRONICS	24	52	3
16U45A0245	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	46	3
16U45A0245	RT31027	ELECTRICAL MACHINES-II LAB	24	50	2
16U45A0245	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0246	RT31016	IPR & PATENTS	21	32	2
16U45A0246	RT31021	ELECTRICAL MEASUREMENTS	18	31	3
16U45A0246	RT31022	MEFA	21	45	3
16U45A0246	RT31023	POWER SYSTEMS-II	21	27	3
16U45A0246	RT31024	ELECTRICAL MACHINES-III	22	12	0

Htno	Subcode	Subname	Internal	External	Credits
16U45A0246	RT31025	POWER ELECTRONICS	18	11	0
16U45A0246	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	8	0
16U45A0246	RT31027	ELECTRICAL MACHINES-II LAB	20	42	2
16U45A0246	RT31028	CONTROL SYSTEMS LAB	22	48	2
16U45A0247	RT31016	IPR & PATENTS	21	24	2
16U45A0247	RT31021	ELECTRICAL MEASUREMENTS	27	27	3
16U45A0247	RT31022	MEFA	25	38	3
16U45A0247	RT31023	POWER SYSTEMS-II	26	39	3
16U45A0247	RT31024	ELECTRICAL MACHINES-III	25	35	3
16U45A0247	RT31025	POWER ELECTRONICS	21	24	3
16U45A0247	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	13	0
16U45A0247	RT31027	ELECTRICAL MACHINES-II LAB	22	45	2
16U45A0247	RT31028	CONTROL SYSTEMS LAB	22	47	2
16U45A0248	RT31016	IPR & PATENTS	24	37	2
16U45A0248	RT31021	ELECTRICAL MEASUREMENTS	26	30	3
16U45A0248	RT31022	MEFA	19	37	3
16U45A0248	RT31023	POWER SYSTEMS-II	23	35	3
16U45A0248	RT31024	ELECTRICAL MACHINES-III	26	47	3
16U45A0248	RT31025	POWER ELECTRONICS	18	48	3
16U45A0248	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	35	3
16U45A0248	RT31027	ELECTRICAL MACHINES-II LAB	23	47	2
16U45A0248	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0249	RT31016	IPR & PATENTS	24	50	2
16U45A0249	RT31021	ELECTRICAL MEASUREMENTS	28	40	3
16U45A0249	RT31022	MEFA	23	46	3
16U45A0249	RT31023	POWER SYSTEMS-II	28	36	3
16U45A0249	RT31024	ELECTRICAL MACHINES-III	27	42	3
16U45A0249	RT31025	POWER ELECTRONICS	24	51	3
16U45A0249	RT31026	LINEAR & DIGITAL IC APPLICATIONS	25	52	3
16U45A0249	RT31027	ELECTRICAL MACHINES-II LAB	24	45	2
16U45A0249	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0250	RT31016	IPR & PATENTS	26	37	2
16U45A0250	RT31021	ELECTRICAL MEASUREMENTS	23	42	3
16U45A0250	RT31022	MEFA	28	48	3
16U45A0250	RT31023	POWER SYSTEMS-II	25	45	3
16U45A0250	RT31024	ELECTRICAL MACHINES-III	27	54	3
16U45A0250	RT31025	POWER ELECTRONICS	25	48	3
16U45A0250	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	42	3
16U45A0250	RT31027	ELECTRICAL MACHINES-II LAB	24	50	2
16U45A0250	RT31028	CONTROL SYSTEMS LAB	22	49	2
16U45A0251	RT31016	IPR & PATENTS	21	25	2
16U45A0251	RT31021	ELECTRICAL MEASUREMENTS	22	27	3
16U45A0251	RT31022	MEFA	25	39	3
16U45A0251	RT31023	POWER SYSTEMS-II	22	24	3
16U45A0251	RT31024	ELECTRICAL MACHINES-III	21	28	3
16U45A0251	RT31025	POWER ELECTRONICS	22	38	3
16U45A0251	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	9	0
16U45A0251	RT31027	ELECTRICAL MACHINES-II LAB	21	41	2
16U45A0251	RT31028	CONTROL SYSTEMS LAB	25	49	2
16U45A0252	RT31016	IPR & PATENTS	28	28	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0252	RT31021	ELECTRICAL MEASUREMENTS	26	34	3
16U45A0252	RT31022	MEFA	18	34	3
16U45A0252	RT31023	POWER SYSTEMS-II	26	48	3
16U45A0252	RT31024	ELECTRICAL MACHINES-III	24	53	3
16U45A0252	RT31025	POWER ELECTRONICS	23	42	3
16U45A0252	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	33	3
16U45A0252	RT31027	ELECTRICAL MACHINES-II LAB	22	42	2
16U45A0252	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0253	RT31016	IPR & PATENTS	23	41	2
16U45A0253	RT31021	ELECTRICAL MEASUREMENTS	25	14	0
16U45A0253	RT31022	MEFA	25	34	3
16U45A0253	RT31023	POWER SYSTEMS-II	24	25	3
16U45A0253	RT31024	ELECTRICAL MACHINES-III	21	32	3
16U45A0253	RT31025	POWER ELECTRONICS	22	39	3
16U45A0253	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	40	3
16U45A0253	RT31027	ELECTRICAL MACHINES-II LAB	21	41	2
16U45A0253	RT31028	CONTROL SYSTEMS LAB	25	50	2
16U45A0254	RT31016	IPR & PATENTS	23	30	2
16U45A0254	RT31021	ELECTRICAL MEASUREMENTS	25	34	3
16U45A0254	RT31022	MEFA	20	43	3
16U45A0254	RT31023	POWER SYSTEMS-II	23	34	3
16U45A0254	RT31024	ELECTRICAL MACHINES-III	21	27	3
16U45A0254	RT31025	POWER ELECTRONICS	19	11	0
16U45A0254	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	32	3
16U45A0254	RT31027	ELECTRICAL MACHINES-II LAB	22	42	2
16U45A0254	RT31028	CONTROL SYSTEMS LAB	24	50	2
16U45A0255	RT31016	IPR & PATENTS	22	27	2
16U45A0255	RT31021	ELECTRICAL MEASUREMENTS	20	32	3
16U45A0255	RT31022	MEFA	24	34	3
16U45A0255	RT31023	POWER SYSTEMS-II	21	27	3
16U45A0255	RT31024	ELECTRICAL MACHINES-III	27	44	3
16U45A0255	RT31025	POWER ELECTRONICS	22	30	3
16U45A0255	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	16	0
16U45A0255	RT31027	ELECTRICAL MACHINES-II LAB	23	45	2
16U45A0255	RT31028	CONTROL SYSTEMS LAB	24	49	2
16U45A0256	RT31016	IPR & PATENTS	22	26	2
16U45A0256	RT31021	ELECTRICAL MEASUREMENTS	23	26	3
16U45A0256	RT31022	MEFA	21	30	3
16U45A0256	RT31023	POWER SYSTEMS-II	24	70	3
16U45A0256	RT31024	ELECTRICAL MACHINES-III	19	24	3
16U45A0256	RT31025	POWER ELECTRONICS	21	33	3
16U45A0256	RT31026	LINEAR & DIGITAL IC APPLICATIONS	20	30	3
16U45A0256	RT31027	ELECTRICAL MACHINES-II LAB	23	47	2
16U45A0256	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0257	RT31016	IPR & PATENTS	23	41	2
16U45A0257	RT31021	ELECTRICAL MEASUREMENTS	26	37	3
16U45A0257	RT31022	MEFA	20	35	3
16U45A0257	RT31023	POWER SYSTEMS-II	25	33	3
16U45A0257	RT31024	ELECTRICAL MACHINES-III	24	38	3
16U45A0257	RT31025	POWER ELECTRONICS	24	46	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0257	RT31026	LINEAR & DIGITAL IC APPLICATIONS	28	41	3
16U45A0257	RT31027	ELECTRICAL MACHINES-II LAB	23	45	2
16U45A0257	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0258	RT31016	IPR & PATENTS	21	41	2
16U45A0258	RT31021	ELECTRICAL MEASUREMENTS	25	39	3
16U45A0258	RT31022	MEFA	22	33	3
16U45A0258	RT31023	POWER SYSTEMS-II	25	36	3
16U45A0258	RT31024	ELECTRICAL MACHINES-III	27	11	0
16U45A0258	RT31025	POWER ELECTRONICS	26	36	3
16U45A0258	RT31026	LINEAR & DIGITAL IC APPLICATIONS	22	25	3
16U45A0258	RT31027	ELECTRICAL MACHINES-II LAB	24	48	2
16U45A0258	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0259	RT31016	IPR & PATENTS	24	27	2
16U45A0259	RT31021	ELECTRICAL MEASUREMENTS	24	26	3
16U45A0259	RT31022	MEFA	21	45	3
16U45A0259	RT31023	POWER SYSTEMS-II	24	25	3
16U45A0259	RT31024	ELECTRICAL MACHINES-III	24	40	3
16U45A0259	RT31025	POWER ELECTRONICS	20	33	3
16U45A0259	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	27	3
16U45A0259	RT31027	ELECTRICAL MACHINES-II LAB	22	42	2
16U45A0259	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0260	RT31016	IPR & PATENTS	24	30	2
16U45A0260	RT31021	ELECTRICAL MEASUREMENTS	19	11	0
16U45A0260	RT31022	MEFA	20	34	3
16U45A0260	RT31023	POWER SYSTEMS-II	20	43	3
16U45A0260	RT31024	ELECTRICAL MACHINES-III	18	26	3
16U45A0260	RT31025	POWER ELECTRONICS	18	43	3
16U45A0260	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	35	3
16U45A0260	RT31027	ELECTRICAL MACHINES-II LAB	23	44	2
16U45A0260	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0261	RT31016	IPR & PATENTS	17	42	2
16U45A0261	RT31021	ELECTRICAL MEASUREMENTS	17	5	0
16U45A0261	RT31022	MEFA	18	30	3
16U45A0261	RT31023	POWER SYSTEMS-II	12	48	3
16U45A0261	RT31024	ELECTRICAL MACHINES-III	21	31	3
16U45A0261	RT31025	POWER ELECTRONICS	18	36	3
16U45A0261	RT31026	LINEAR & DIGITAL IC APPLICATIONS	16	24	3
16U45A0261	RT31027	ELECTRICAL MACHINES-II LAB	24	43	2
16U45A0261	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0262	RT31016	IPR & PATENTS	21	28	2
16U45A0262	RT31021	ELECTRICAL MEASUREMENTS	16	31	3
16U45A0262	RT31022	MEFA	18	35	3
16U45A0262	RT31023	POWER SYSTEMS-II	19	15	0
16U45A0262	RT31024	ELECTRICAL MACHINES-III	16	9	0
16U45A0262	RT31025	POWER ELECTRONICS	15	3	0
16U45A0262	RT31026	LINEAR & DIGITAL IC APPLICATIONS	18	8	0
16U45A0262	RT31027	ELECTRICAL MACHINES-II LAB	20	38	2
16U45A0262	RT31028	CONTROL SYSTEMS LAB	22	47	2
16U45A0263	RT31016	IPR & PATENTS	28	25	2
16U45A0263	RT31021	ELECTRICAL MEASUREMENTS	28	27	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0263	RT31022	MEFA	23	46	3
16U45A0263	RT31023	POWER SYSTEMS-II	26	29	3
16U45A0263	RT31024	ELECTRICAL MACHINES-III	25	37	3
16U45A0263	RT31025	POWER ELECTRONICS	27	39	3
16U45A0263	RT31026	LINEAR & DIGITAL IC APPLICATIONS	28	36	3
16U45A0263	RT31027	ELECTRICAL MACHINES-II LAB	24	48	2
16U45A0263	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0264	RT31016	IPR & PATENTS	23	25	2
16U45A0264	RT31021	ELECTRICAL MEASUREMENTS	22	34	3
16U45A0264	RT31022	MEFA	25	31	3
16U45A0264	RT31023	POWER SYSTEMS-II	29	59	3
16U45A0264	RT31024	ELECTRICAL MACHINES-III	25	30	3
16U45A0264	RT31025	POWER ELECTRONICS	23	32	3
16U45A0264	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	40	3
16U45A0264	RT31027	ELECTRICAL MACHINES-II LAB	22	46	2
16U45A0264	RT31028	CONTROL SYSTEMS LAB	24	47	2
16U45A0265	RT31016	IPR & PATENTS	21	48	2
16U45A0265	RT31021	ELECTRICAL MEASUREMENTS	27	36	3
16U45A0265	RT31022	MEFA	22	37	3
16U45A0265	RT31023	POWER SYSTEMS-II	26	42	3
16U45A0265	RT31024	ELECTRICAL MACHINES-III	25	33	3
16U45A0265	RT31025	POWER ELECTRONICS	25	52	3
16U45A0265	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	27	3
16U45A0265	RT31027	ELECTRICAL MACHINES-II LAB	24	50	2
16U45A0265	RT31028	CONTROL SYSTEMS LAB	22	48	2
16U45A0266	RT31016	IPR & PATENTS	20	37	2
16U45A0266	RT31021	ELECTRICAL MEASUREMENTS	20	43	3
16U45A0266	RT31022	MEFA	24	55	3
16U45A0266	RT31023	POWER SYSTEMS-II	26	43	3
16U45A0266	RT31024	ELECTRICAL MACHINES-III	26	41	3
16U45A0266	RT31025	POWER ELECTRONICS	16	24	3
16U45A0266	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	35	3
16U45A0266	RT31027	ELECTRICAL MACHINES-II LAB	23	45	2
16U45A0266	RT31028	CONTROL SYSTEMS LAB	25	49	2
16U45A0267	RT31016	IPR & PATENTS	25	26	2
16U45A0267	RT31021	ELECTRICAL MEASUREMENTS	21	12	0
16U45A0267	RT31022	MEFA	23	41	3
16U45A0267	RT31023	POWER SYSTEMS-II	26	33	3
16U45A0267	RT31024	ELECTRICAL MACHINES-III	29	31	3
16U45A0267	RT31025	POWER ELECTRONICS	23	31	3
16U45A0267	RT31026	LINEAR & DIGITAL IC APPLICATIONS	27	13	0
16U45A0267	RT31027	ELECTRICAL MACHINES-II LAB	23	44	2
16U45A0267	RT31028	CONTROL SYSTEMS LAB	23	48	2
16U45A0268	RT31016	IPR & PATENTS	26	30	2
16U45A0268	RT31021	ELECTRICAL MEASUREMENTS	22	26	3
16U45A0268	RT31022	MEFA	22	39	3
16U45A0268	RT31023	POWER SYSTEMS-II	27	52	3
16U45A0268	RT31024	ELECTRICAL MACHINES-III	27	42	3
16U45A0268	RT31025	POWER ELECTRONICS	21	29	3
16U45A0268	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	28	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0268	RT31027	ELECTRICAL MACHINES-II LAB	23	42	2
16U45A0268	RT31028	CONTROL SYSTEMS LAB	24	48	2
16U45A0269	RT31016	IPR & PATENTS	19	48	2
16U45A0269	RT31021	ELECTRICAL MEASUREMENTS	19	7	0
16U45A0269	RT31022	MEFA	22	33	3
16U45A0269	RT31023	POWER SYSTEMS-II	23	27	3
16U45A0269	RT31024	ELECTRICAL MACHINES-III	23	17	0
16U45A0269	RT31025	POWER ELECTRONICS	22	34	3
16U45A0269	RT31026	LINEAR & DIGITAL IC APPLICATIONS	23	25	3
16U45A0269	RT31027	ELECTRICAL MACHINES-II LAB	22	41	2
16U45A0269	RT31028	CONTROL SYSTEMS LAB	23	49	2
16U45A0270	RT31016	IPR & PATENTS	22	38	2
16U45A0270	RT31021	ELECTRICAL MEASUREMENTS	23	46	3
16U45A0270	RT31022	MEFA	23	35	3
16U45A0270	RT31023	POWER SYSTEMS-II	21	31	3
16U45A0270	RT31024	ELECTRICAL MACHINES-III	21	24	3
16U45A0270	RT31025	POWER ELECTRONICS	26	8	0
16U45A0270	RT31026	LINEAR & DIGITAL IC APPLICATIONS	21	18	0
16U45A0270	RT31027	ELECTRICAL MACHINES-II LAB	23	44	2
16U45A0270	RT31028	CONTROL SYSTEMS LAB	24	47	2
16U45A0271	RT31016	IPR & PATENTS	27	30	2
16U45A0271	RT31021	ELECTRICAL MEASUREMENTS	26	6	0
16U45A0271	RT31022	MEFA	25	44	3
16U45A0271	RT31023	POWER SYSTEMS-II	29	17	0
16U45A0271	RT31024	ELECTRICAL MACHINES-III	26	36	3
16U45A0271	RT31025	POWER ELECTRONICS	26	37	3
16U45A0271	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	37	3
16U45A0271	RT31027	ELECTRICAL MACHINES-II LAB	24	45	2
16U45A0271	RT31028	CONTROL SYSTEMS LAB	22	48	2
16U45A0272	RT31016	IPR & PATENTS	15	25	2
16U45A0272	RT31021	ELECTRICAL MEASUREMENTS	18	25	3
16U45A0272	RT31022	MEFA	18	28	3
16U45A0272	RT31023	POWER SYSTEMS-II	24	24	3
16U45A0272	RT31024	ELECTRICAL MACHINES-III	18	45	3
16U45A0272	RT31025	POWER ELECTRONICS	16	25	3
16U45A0272	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	29	3
16U45A0272	RT31027	ELECTRICAL MACHINES-II LAB	20	39	2
16U45A0272	RT31028	CONTROL SYSTEMS LAB	23	47	2
16U45A0273	RT31016	IPR & PATENTS	22	33	2
16U45A0273	RT31021	ELECTRICAL MEASUREMENTS	18	12	0
16U45A0273	RT31022	MEFA	20	28	3
16U45A0273	RT31023	POWER SYSTEMS-II	21	16	0
16U45A0273	RT31024	ELECTRICAL MACHINES-III	22	27	3
16U45A0273	RT31025	POWER ELECTRONICS	21	41	3
16U45A0273	RT31026	LINEAR & DIGITAL IC APPLICATIONS	24	24	3
16U45A0273	RT31027	ELECTRICAL MACHINES-II LAB	23	43	2
16U45A0273	RT31028	CONTROL SYSTEMS LAB	23	47	2
16U45A0401	RT31016	IPR & PATENTS	21	26	2
16U45A0401	RT31041	PULSE & DIGITAL CIRCUITS	19	13	0
16U45A0401	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	23	26	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0401	RT31043	CONTROL SYSTEMS	17	26	3
16U45A0401	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	24	3
16U45A0401	RT31045	ANTENNAS AND WAVE PROPAGATION	16	11	0
16U45A0401	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	40	2
16U45A0401	RT31048	LICA LAB	24	44	2
16U45A0401	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	20	32	2
16U45A0402	RT31016	IPR & PATENTS	30	50	2
16U45A0402	RT31041	PULSE & DIGITAL CIRCUITS	28	40	3
16U45A0402	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	23	41	3
16U45A0402	RT31043	CONTROL SYSTEMS	25	26	3
16U45A0402	RT31044	DIGITAL SYSTEM DESIGN & DICA	24	34	3
16U45A0402	RT31045	ANTENNAS AND WAVE PROPAGATION	26	30	3
16U45A0402	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	47	2
16U45A0402	RT31048	LICA LAB	25	46	2
16U45A0402	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	38	2
16U45A0403	RT31016	IPR & PATENTS	23	36	2
16U45A0403	RT31041	PULSE & DIGITAL CIRCUITS	24	35	3
16U45A0403	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	17	35	3
16U45A0403	RT31043	CONTROL SYSTEMS	21	47	3
16U45A0403	RT31044	DIGITAL SYSTEM DESIGN & DICA	24	27	3
16U45A0403	RT31045	ANTENNAS AND WAVE PROPAGATION	19	31	3
16U45A0403	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	46	2
16U45A0403	RT31048	LICA LAB	24	46	2
16U45A0403	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	20	42	2
16U45A0404	RT31016	IPR & PATENTS	29	30	2
16U45A0404	RT31041	PULSE & DIGITAL CIRCUITS	30	45	3
16U45A0404	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	40	3
16U45A0404	RT31043	CONTROL SYSTEMS	30	45	3
16U45A0404	RT31044	DIGITAL SYSTEM DESIGN & DICA	28	46	3
16U45A0404	RT31045	ANTENNAS AND WAVE PROPAGATION	30	48	3
16U45A0404	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	49	2
16U45A0404	RT31048	LICA LAB	25	50	2
16U45A0404	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	50	2
16U45A0405	RT31016	IPR & PATENTS	25	25	2
16U45A0405	RT31041	PULSE & DIGITAL CIRCUITS	30	38	3
16U45A0405	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	39	3
16U45A0405	RT31043	CONTROL SYSTEMS	29	47	3
16U45A0405	RT31044	DIGITAL SYSTEM DESIGN & DICA	26	33	3
16U45A0405	RT31045	ANTENNAS AND WAVE PROPAGATION	27	24	3
16U45A0405	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
16U45A0405	RT31048	LICA LAB	25	50	2
16U45A0405	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	45	2
16U45A0406	RT31016	IPR & PATENTS	26	28	2
16U45A0406	RT31041	PULSE & DIGITAL CIRCUITS	28	45	3
16U45A0406	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	43	3
16U45A0406	RT31043	CONTROL SYSTEMS	22	27	3
16U45A0406	RT31044	DIGITAL SYSTEM DESIGN & DICA	27	15	0
16U45A0406	RT31045	ANTENNAS AND WAVE PROPAGATION	25	28	3
16U45A0406	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
16U45A0406	RT31048	LICA LAB	25	50	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0406	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	48	2
16U45A0407	RT31016	IPR & PATENTS	27	41	2
16U45A0407	RT31041	PULSE & DIGITAL CIRCUITS	30	62	3
16U45A0407	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	44	3
16U45A0407	RT31043	CONTROL SYSTEMS	26	53	3
16U45A0407	RT31044	DIGITAL SYSTEM DESIGN & DICA	25	45	3
16U45A0407	RT31045	ANTENNAS AND WAVE PROPAGATION	26	43	3
16U45A0407	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	49	2
16U45A0407	RT31048	LICA LAB	24	43	2
16U45A0407	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	43	2
16U45A0408	RT31016	IPR & PATENTS	30	34	2
16U45A0408	RT31041	PULSE & DIGITAL CIRCUITS	28	49	3
16U45A0408	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	39	3
16U45A0408	RT31043	CONTROL SYSTEMS	29	34	3
16U45A0408	RT31044	DIGITAL SYSTEM DESIGN & DICA	23	43	3
16U45A0408	RT31045	ANTENNAS AND WAVE PROPAGATION	24	37	3
16U45A0408	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	49	2
16U45A0408	RT31048	LICA LAB	25	50	2
16U45A0408	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	47	2
16U45A0409	RT31016	IPR & PATENTS	26	25	2
16U45A0409	RT31041	PULSE & DIGITAL CIRCUITS	28	30	3
16U45A0409	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	42	3
16U45A0409	RT31043	CONTROL SYSTEMS	30	47	3
16U45A0409	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	40	3
16U45A0409	RT31045	ANTENNAS AND WAVE PROPAGATION	26	17	0
16U45A0409	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	45	2
16U45A0409	RT31048	LICA LAB	24	42	2
16U45A0409	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	45	2
16U45A0410	RT31016	IPR & PATENTS	27	32	2
16U45A0410	RT31041	PULSE & DIGITAL CIRCUITS	26	45	3
16U45A0410	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	24	47	3
16U45A0410	RT31043	CONTROL SYSTEMS	20	32	3
16U45A0410	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	29	3
16U45A0410	RT31045	ANTENNAS AND WAVE PROPAGATION	23	17	0
16U45A0410	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	48	2
16U45A0410	RT31048	LICA LAB	25	48	2
16U45A0410	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	19	35	2
16U45A0411	RT31016	IPR & PATENTS	26	32	2
16U45A0411	RT31041	PULSE & DIGITAL CIRCUITS	24	37	3
16U45A0411	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	27	3
16U45A0411	RT31043	CONTROL SYSTEMS	27	36	3
16U45A0411	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	28	3
16U45A0411	RT31045	ANTENNAS AND WAVE PROPAGATION	20	37	3
16U45A0411	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	46	2
16U45A0411	RT31048	LICA LAB	24	42	2
16U45A0411	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	37	2
16U45A0412	RT31016	IPR & PATENTS	25	24	2
16U45A0412	RT31041	PULSE & DIGITAL CIRCUITS	23	36	3
16U45A0412	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	20	25	3
16U45A0412	RT31043	CONTROL SYSTEMS	16	24	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0412	RT31044	DIGITAL SYSTEM DESIGN & DICA	19	50	3
16U45A0412	RT31045	ANTENNAS AND WAVE PROPAGATION	20	16	0
16U45A0412	RT31047	PULSE & DIGITAL CIRCUITS LAB	19	42	2
16U45A0412	RT31048	LICA LAB	21	41	2
16U45A0412	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	19	32	2
16U45A0413	RT31016	IPR & PATENTS	30	37	2
16U45A0413	RT31041	PULSE & DIGITAL CIRCUITS	29	45	3
16U45A0413	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	47	3
16U45A0413	RT31043	CONTROL SYSTEMS	28	51	3
16U45A0413	RT31044	DIGITAL SYSTEM DESIGN & DICA	25	56	3
16U45A0413	RT31045	ANTENNAS AND WAVE PROPAGATION	28	36	3
16U45A0413	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
16U45A0413	RT31048	LICA LAB	24	46	2
16U45A0413	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	45	2
16U45A0414	RT31016	IPR & PATENTS	26	31	2
16U45A0414	RT31041	PULSE & DIGITAL CIRCUITS	29	49	3
16U45A0414	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	67	3
16U45A0414	RT31043	CONTROL SYSTEMS	29	40	3
16U45A0414	RT31044	DIGITAL SYSTEM DESIGN & DICA	27	50	3
16U45A0414	RT31045	ANTENNAS AND WAVE PROPAGATION	26	37	3
16U45A0414	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	50	2
16U45A0414	RT31048	LICA LAB	24	43	2
16U45A0414	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	45	2
16U45A0415	RT31016	IPR & PATENTS	26	42	2
16U45A0415	RT31041	PULSE & DIGITAL CIRCUITS	26	62	3
16U45A0415	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	24	3
16U45A0415	RT31043	CONTROL SYSTEMS	25	43	3
16U45A0415	RT31044	DIGITAL SYSTEM DESIGN & DICA	23	44	3
16U45A0415	RT31045	ANTENNAS AND WAVE PROPAGATION	25	43	3
16U45A0415	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	45	2
16U45A0415	RT31048	LICA LAB	15	46	2
16U45A0415	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	40	2
16U45A0416	RT31016	IPR & PATENTS	25	28	2
16U45A0416	RT31041	PULSE & DIGITAL CIRCUITS	25	39	3
16U45A0416	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	51	3
16U45A0416	RT31043	CONTROL SYSTEMS	23	38	3
16U45A0416	RT31044	DIGITAL SYSTEM DESIGN & DICA	23	38	3
16U45A0416	RT31045	ANTENNAS AND WAVE PROPAGATION	23	27	3
16U45A0416	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	45	2
16U45A0416	RT31048	LICA LAB	24	46	2
16U45A0416	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	40	2
16U45A0417	RT31016	IPR & PATENTS	29	30	2
16U45A0417	RT31041	PULSE & DIGITAL CIRCUITS	30	43	3
16U45A0417	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	30	47	3
16U45A0417	RT31043	CONTROL SYSTEMS	28	55	3
16U45A0417	RT31044	DIGITAL SYSTEM DESIGN & DICA	24	30	3
16U45A0417	RT31045	ANTENNAS AND WAVE PROPAGATION	25	26	3
16U45A0417	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	50	2
16U45A0417	RT31048	LICA LAB	24	50	2
16U45A0417	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	50	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0418	RT31016	IPR & PATENTS	29	29	2
16U45A0418	RT31041	PULSE & DIGITAL CIRCUITS	29	56	3
16U45A0418	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	24	65	3
16U45A0418	RT31043	CONTROL SYSTEMS	29	39	3
16U45A0418	RT31044	DIGITAL SYSTEM DESIGN & DICA	28	36	3
16U45A0418	RT31045	ANTENNAS AND WAVE PROPAGATION	25	34	3
16U45A0418	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	50	2
16U45A0418	RT31048	LICA LAB	25	50	2
16U45A0418	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	23	45	2
16U45A0419	RT31016	IPR & PATENTS	27	42	2
16U45A0419	RT31041	PULSE & DIGITAL CIRCUITS	27	44	3
16U45A0419	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	34	3
16U45A0419	RT31043	CONTROL SYSTEMS	27	58	3
16U45A0419	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	41	3
16U45A0419	RT31045	ANTENNAS AND WAVE PROPAGATION	23	39	3
16U45A0419	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
16U45A0419	RT31048	LICA LAB	24	50	2
16U45A0419	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	50	2
16U45A0420	RT31016	IPR & PATENTS	30	31	2
16U45A0420	RT31041	PULSE & DIGITAL CIRCUITS	30	50	3
16U45A0420	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	52	3
16U45A0420	RT31043	CONTROL SYSTEMS	28	44	3
16U45A0420	RT31044	DIGITAL SYSTEM DESIGN & DICA	28	47	3
16U45A0420	RT31045	ANTENNAS AND WAVE PROPAGATION	28	33	3
16U45A0420	RT31047	PULSE & DIGITAL CIRCUITS LAB	25	50	2
16U45A0420	RT31048	LICA LAB	25	50	2
16U45A0420	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	50	2
16U45A0421	RT31016	IPR & PATENTS	25	34	2
16U45A0421	RT31041	PULSE & DIGITAL CIRCUITS	25	37	3
16U45A0421	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	34	3
16U45A0421	RT31043	CONTROL SYSTEMS	28	27	3
16U45A0421	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	24	3
16U45A0421	RT31045	ANTENNAS AND WAVE PROPAGATION	24	15	0
16U45A0421	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	43	2
16U45A0421	RT31048	LICA LAB	24	50	2
16U45A0421	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	42	2
16U45A0422	RT31016	IPR & PATENTS	26	27	2
16U45A0422	RT31041	PULSE & DIGITAL CIRCUITS	30	42	3
16U45A0422	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	60	3
16U45A0422	RT31043	CONTROL SYSTEMS	28	26	3
16U45A0422	RT31044	DIGITAL SYSTEM DESIGN & DICA	23	45	3
16U45A0422	RT31045	ANTENNAS AND WAVE PROPAGATION	23	28	3
16U45A0422	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	46	2
16U45A0422	RT31048	LICA LAB	25	49	2
16U45A0422	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	40	2
16U45A0423	RT31016	IPR & PATENTS	30	45	2
16U45A0423	RT31041	PULSE & DIGITAL CIRCUITS	26	54	3
16U45A0423	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	33	3
16U45A0423	RT31043	CONTROL SYSTEMS	26	57	3
16U45A0423	RT31044	DIGITAL SYSTEM DESIGN & DICA	24	48	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0423	RT31045	ANTENNAS AND WAVE PROPAGATION	24	52	3
16U45A0423	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	45	2
16U45A0423	RT31048	LICA LAB	24	48	2
16U45A0423	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	45	2
16U45A0424	RT31016	IPR & PATENTS	28	43	2
16U45A0424	RT31041	PULSE & DIGITAL CIRCUITS	30	51	3
16U45A0424	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	47	3
16U45A0424	RT31043	CONTROL SYSTEMS	29	42	3
16U45A0424	RT31044	DIGITAL SYSTEM DESIGN & DICA	28	44	3
16U45A0424	RT31045	ANTENNAS AND WAVE PROPAGATION	26	35	3
16U45A0424	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	45	2
16U45A0424	RT31048	LICA LAB	24	50	2
16U45A0424	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	25	48	2
16U45A0425	RT31016	IPR & PATENTS	26	26	2
16U45A0425	RT31041	PULSE & DIGITAL CIRCUITS	27	39	3
16U45A0425	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	26	33	3
16U45A0425	RT31043	CONTROL SYSTEMS	26	29	3
16U45A0425	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	24	3
16U45A0425	RT31045	ANTENNAS AND WAVE PROPAGATION	26	13	0
16U45A0425	RT31047	PULSE & DIGITAL CIRCUITS LAB	24	45	2
16U45A0425	RT31048	LICA LAB	23	46	2
16U45A0425	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	38	2
16U45A0426	RT31016	IPR & PATENTS	27	25	2
16U45A0426	RT31041	PULSE & DIGITAL CIRCUITS	25	31	3
16U45A0426	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	20	42	3
16U45A0426	RT31043	CONTROL SYSTEMS	22	26	3
16U45A0426	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	24	3
16U45A0426	RT31045	ANTENNAS AND WAVE PROPAGATION	20	10	0
16U45A0426	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	43	2
16U45A0426	RT31048	LICA LAB	23	44	2
16U45A0426	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	18	42	2
16U45A0428	RT31016	IPR & PATENTS	26	39	2
16U45A0428	RT31041	PULSE & DIGITAL CIRCUITS	27	52	3
16U45A0428	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	29	44	3
16U45A0428	RT31043	CONTROL SYSTEMS	26	42	3
16U45A0428	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	42	3
16U45A0428	RT31045	ANTENNAS AND WAVE PROPAGATION	24	44	3
16U45A0428	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	43	2
16U45A0428	RT31048	LICA LAB	25	49	2
16U45A0428	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	45	2
16U45A0429	RT31016	IPR & PATENTS	29	30	2
16U45A0429	RT31041	PULSE & DIGITAL CIRCUITS	22	38	3
16U45A0429	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	45	3
16U45A0429	RT31043	CONTROL SYSTEMS	26	34	3
16U45A0429	RT31044	DIGITAL SYSTEM DESIGN & DICA	22	31	3
16U45A0429	RT31045	ANTENNAS AND WAVE PROPAGATION	22	27	3
16U45A0429	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	45	2
16U45A0429	RT31048	LICA LAB	24	48	2
16U45A0429	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	21	38	2
16U45A0430	RT31016	IPR & PATENTS	27	32	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0430	RT31041	PULSE & DIGITAL CIRCUITS	25	31	3
16U45A0430	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	32	3
16U45A0430	RT31043	CONTROL SYSTEMS	23	33	3
16U45A0430	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	34	3
16U45A0430	RT31045	ANTENNAS AND WAVE PROPAGATION	20	17	0
16U45A0430	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	42	2
16U45A0430	RT31048	LICA LAB	23	43	2
16U45A0430	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	38	2
16U45A0431	RT31016	IPR & PATENTS	21	29	2
16U45A0431	RT31041	PULSE & DIGITAL CIRCUITS	24	37	3
16U45A0431	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	21	41	3
16U45A0431	RT31043	CONTROL SYSTEMS	22	26	3
16U45A0431	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	29	3
16U45A0431	RT31045	ANTENNAS AND WAVE PROPAGATION	17	15	0
16U45A0431	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	48	2
16U45A0431	RT31048	LICA LAB	23	48	2
16U45A0431	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	32	2
16U45A0432	RT31016	IPR & PATENTS	28	35	2
16U45A0432	RT31041	PULSE & DIGITAL CIRCUITS	24	38	3
16U45A0432	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	22	18	0
16U45A0432	RT31043	CONTROL SYSTEMS	22	24	3
16U45A0432	RT31044	DIGITAL SYSTEM DESIGN & DICA	18	38	3
16U45A0432	RT31045	ANTENNAS AND WAVE PROPAGATION	18	18	0
16U45A0432	RT31047	PULSE & DIGITAL CIRCUITS LAB	21	46	2
16U45A0432	RT31048	LICA LAB	24	42	2
16U45A0432	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	20	32	2
16U45A0433	RT31016	IPR & PATENTS	23	31	2
16U45A0433	RT31041	PULSE & DIGITAL CIRCUITS	23	25	3
16U45A0433	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	23	31	3
16U45A0433	RT31043	CONTROL SYSTEMS	19	24	3
16U45A0433	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	39	3
16U45A0433	RT31045	ANTENNAS AND WAVE PROPAGATION	26	10	0
16U45A0433	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	45	2
16U45A0433	RT31048	LICA LAB	23	47	2
16U45A0433	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	20	32	2
16U45A0434	RT31016	IPR & PATENTS	26	25	2
16U45A0434	RT31041	PULSE & DIGITAL CIRCUITS	27	41	3
16U45A0434	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	28	36	3
16U45A0434	RT31043	CONTROL SYSTEMS	23	37	3
16U45A0434	RT31044	DIGITAL SYSTEM DESIGN & DICA	20	25	3
16U45A0434	RT31045	ANTENNAS AND WAVE PROPAGATION	25	15	0
16U45A0434	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	45	2
16U45A0434	RT31048	LICA LAB	23	46	2
16U45A0434	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	18	38	2
16U45A0435	RT31016	IPR & PATENTS	26	38	2
16U45A0435	RT31041	PULSE & DIGITAL CIRCUITS	21	41	3
16U45A0435	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	23	54	3
16U45A0435	RT31043	CONTROL SYSTEMS	22	27	3
16U45A0435	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	31	3
16U45A0435	RT31045	ANTENNAS AND WAVE PROPAGATION	22	14	0

Htno	Subcode	Subname	Internal	External	Credits
16U45A0435	RT31047	PULSE & DIGITAL CIRCUITS LAB	23	47	2
16U45A0435	RT31048	LICA LAB	25	46	2
16U45A0435	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	22	35	2
16U45A0436	RT31016	IPR & PATENTS	26	45	2
16U45A0436	RT31041	PULSE & DIGITAL CIRCUITS	30	44	3
16U45A0436	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	27	31	3
16U45A0436	RT31043	CONTROL SYSTEMS	27	41	3
16U45A0436	RT31044	DIGITAL SYSTEM DESIGN & DICA	21	30	3
16U45A0436	RT31045	ANTENNAS AND WAVE PROPAGATION	21	31	3
16U45A0436	RT31047	PULSE & DIGITAL CIRCUITS LAB	22	47	2
16U45A0436	RT31048	LICA LAB	23	48	2
16U45A0436	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	24	45	2
16U45A0438	RT31016	IPR & PATENTS	21	24	2
16U45A0438	RT31041	PULSE & DIGITAL CIRCUITS	15	11	0
16U45A0438	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	17	18	0
16U45A0438	RT31043	CONTROL SYSTEMS	11	5	0
16U45A0438	RT31044	DIGITAL SYSTEM DESIGN & DICA	16	13	0
16U45A0438	RT31045	ANTENNAS AND WAVE PROPAGATION	16	7	0
16U45A0438	RT31047	PULSE & DIGITAL CIRCUITS LAB	11	38	2
16U45A0438	RT31048	LICA LAB	20	41	2
16U45A0438	RT31049	DIGITAL SYSTEM DESIGN & DICA LAB	16	30	2
16U45A0501	RT31051	COMPILER DESIGN	24	30	3
16U45A0501	RT31052	DATA COMMUNICATION	24	38	3
16U45A0501	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	35	3
16U45A0501	RT31054	DATABASE MANAGEMENT SYSTEMS	24	38	3
16U45A0501	RT31055	OPERATING SYSTEMS	25	25	3
16U45A0501	RT31056	COMPILER DESIGN LAB	23	43	2
16U45A0501	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	49	2
16U45A0501	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
16U45A0501	RT31059	SEMINAR	44	0	1
16U45A0502	RT31051	COMPILER DESIGN	29	26	3
16U45A0502	RT31052	DATA COMMUNICATION	27	47	3
16U45A0502	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	27	47	3
16U45A0502	RT31054	DATABASE MANAGEMENT SYSTEMS	26	44	3
16U45A0502	RT31055	OPERATING SYSTEMS	27	52	3
16U45A0502	RT31056	COMPILER DESIGN LAB	23	45	2
16U45A0502	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	48	2
16U45A0502	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	48	2
16U45A0502	RT31059	SEMINAR	45	0	1
16U45A0503	RT31051	COMPILER DESIGN	30	52	3
16U45A0503	RT31052	DATA COMMUNICATION	29	45	3
16U45A0503	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	36	3
16U45A0503	RT31054	DATABASE MANAGEMENT SYSTEMS	30	54	3
16U45A0503	RT31055	OPERATING SYSTEMS	29	55	3
16U45A0503	RT31056	COMPILER DESIGN LAB	25	49	2
16U45A0503	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
16U45A0503	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
16U45A0503	RT31059	SEMINAR	50	0	1
16U45A0504	RT31051	COMPILER DESIGN	29	58	3
16U45A0504	RT31052	DATA COMMUNICATION	29	51	3

Htno	Subcode	Subname	Internal	External	Credits
16U45A0504	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	53	3
16U45A0504	RT31054	DATABASE MANAGEMENT SYSTEMS	29	43	3
16U45A0504	RT31055	OPERATING SYSTEMS	29	54	3
16U45A0504	RT31056	COMPILER DESIGN LAB	25	49	2
16U45A0504	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
16U45A0504	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
16U45A0504	RT31059	SEMINAR	49	0	1
16U45A0505	RT31051	COMPILER DESIGN	29	39	3
16U45A0505	RT31052	DATA COMMUNICATION	30	44	3
16U45A0505	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	30	33	3
16U45A0505	RT31054	DATABASE MANAGEMENT SYSTEMS	29	41	3
16U45A0505	RT31055	OPERATING SYSTEMS	27	33	3
16U45A0505	RT31056	COMPILER DESIGN LAB	24	50	2
16U45A0505	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	25	50	2
16U45A0505	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	50	2
16U45A0505	RT31059	SEMINAR	50	0	1
16U45A0506	RT31051	COMPILER DESIGN	26	36	3
16U45A0506	RT31052	DATA COMMUNICATION	25	41	3
16U45A0506	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	24	38	3
16U45A0506	RT31054	DATABASE MANAGEMENT SYSTEMS	24	32	3
16U45A0506	RT31055	OPERATING SYSTEMS	24	39	3
16U45A0506	RT31056	COMPILER DESIGN LAB	22	44	2
16U45A0506	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	23	43	2
16U45A0506	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	22	46	2
16U45A0506	RT31059	SEMINAR	47	0	1
16U45A0507	RT31051	COMPILER DESIGN	28	45	3
16U45A0507	RT31052	DATA COMMUNICATION	21	27	3
16U45A0507	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	22	36	3
16U45A0507	RT31054	DATABASE MANAGEMENT SYSTEMS	23	37	3
16U45A0507	RT31055	OPERATING SYSTEMS	23	26	3
16U45A0507	RT31056	COMPILER DESIGN LAB	22	43	2
16U45A0507	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	22	41	2
16U45A0507	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	23	47	2
16U45A0507	RT31059	SEMINAR	49	0	1
16U45A0508	RT31051	COMPILER DESIGN	29	32	3
16U45A0508	RT31052	DATA COMMUNICATION	26	37	3
16U45A0508	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	29	56	3
16U45A0508	RT31054	DATABASE MANAGEMENT SYSTEMS	28	46	3
16U45A0508	RT31055	OPERATING SYSTEMS	29	38	3
16U45A0508	RT31056	COMPILER DESIGN LAB	22	47	2
16U45A0508	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	47	2
16U45A0508	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	48	2
16U45A0508	RT31059	SEMINAR	48	0	1
16U45A0509	RT31051	COMPILER DESIGN	28	24	3
16U45A0509	RT31052	DATA COMMUNICATION	23	24	3
16U45A0509	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	26	31	3
16U45A0509	RT31054	DATABASE MANAGEMENT SYSTEMS	29	35	3
16U45A0509	RT31055	OPERATING SYSTEMS	26	35	3
16U45A0509	RT31056	COMPILER DESIGN LAB	23	44	2
16U45A0509	RT31057	OPERATING SYSTEM & LINUX PROGRAMMING LAB	24	47	2

Htno	Subcode	Subname	Internal	External	Credits
16U45A0509	RT31058	DATABASE MANAGEMENT SYSTEMS LAB	25	48	2
16U45A0509	RT31059	SEMINAR	47	0	1

**NOTE:1 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: 03-01-2018]

**NOTE:2 [Please inform to the students enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

** Note:**

* -1 in the filed of externals indicates student absent for the respective subject.

* -2 in the filed of externals indicates student Withheld for the respective subject.

* -3 in the filed of externals indicates student Malpractice for the respective subject.

Date:23-12-2017

N. Mohan Rao
Controller of Examinations